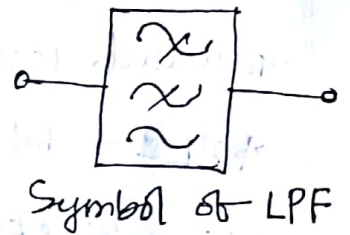
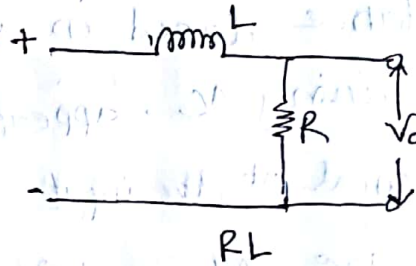
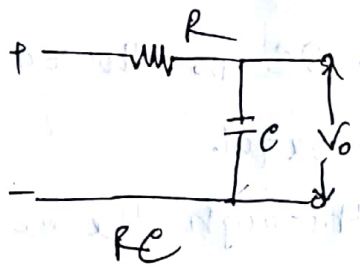


Filter: A circuit that can remove unwanted portions of a signal at its input.

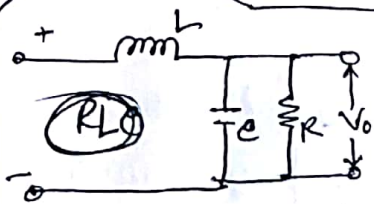
- * Capacitor: Allow AC; Block DC
- * Inductor: Allow DC; Block AC

Low pass: A filter circuit which allows a set of frequencies that are below a specified value can be termed as a low pass filter.



* RC: as the capacitor is placed in shunt, the AC it allows is grounded. This by passes all the high frequency components while allows DC at the output.

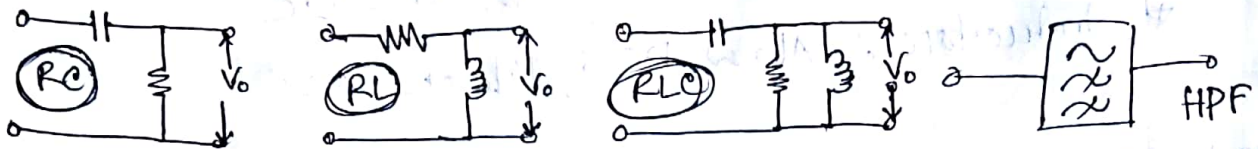
* RL: as the inductor is placed in series, the DC is allowed to the output. The inductor blocks AC which is not allowed at the output.



* L: Block AC, allow DC

* The output is again passed through the capacitor in shunt, which grounds any remaining AC component if any present in the signal. Allow DC at output.

High Pass filter: A filter circuit which allows a set of frequencies that are above a specified value can be termed as a high pass filter.

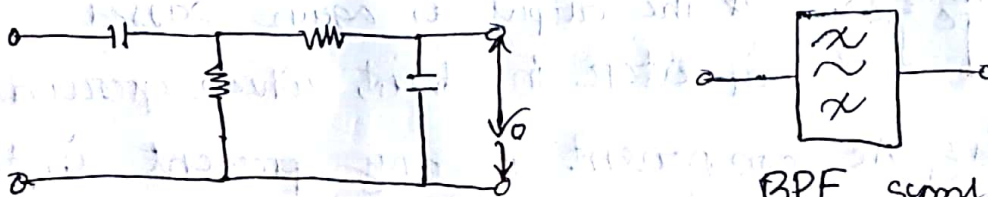


* **RC** = as the capacitor is placed in series, it blocks DC components and allows AC to output. High frequency appears at the output across the resistor.

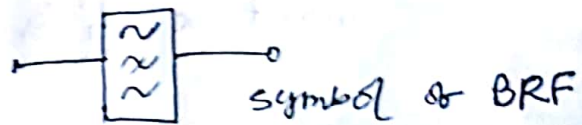
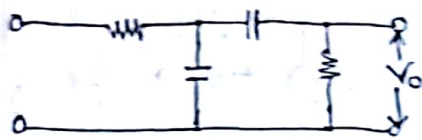
* **RL** = inductance placed in series, DC is allowed to ground, remaining AC, appears at the output.

* **RLC** = the signal at the input goes through the capacitor which blocks DC, allows AC. The output again passes through the inductor in shunt, which grounds the remaining DC components, if any present in the signal. This is the better high pass than both of them.

Band Pass Filter: A filter circuit which allows a set of frequencies that are between two specified values can be termed as band pass.

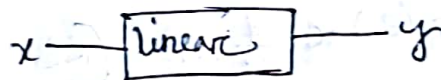


Band Reject filter: a filter circuit that blocks a set of frequencies that are between two specific values can be called as BRF.



Characteristics of linear wave shaping:

1. Homogeneity: If x be input then y be output corresponding to x . If input be $2x$ then output must be $2y$.



2. Additivity: If x_1 and x_2 be input then output be $x_1 + x_2$.

$x_1 + x_2$



3. Shift Invariance: If x_1 be input at time t_1 and output be y_1 then x_2 be input at time t_2 then y_2 be output.



4. It can hold the waveform to a particular d.c. level.

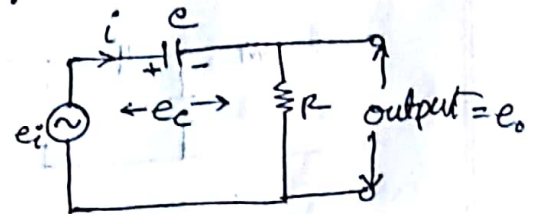
5. It also generate one waveform to another.

6. It limits the voltage level of the waveform to some presenting value.

7. It cuts-off the positive and negative portion of output.

Differentiating circuit: a circuit in which output voltage is directly proportional to the derivative of the input is known as a differentiating circuit.

$$\text{output} \propto \frac{d}{dt} (\text{Input})$$



The output across R will be the derivative of the input.

To achieve good differentiation there must be -

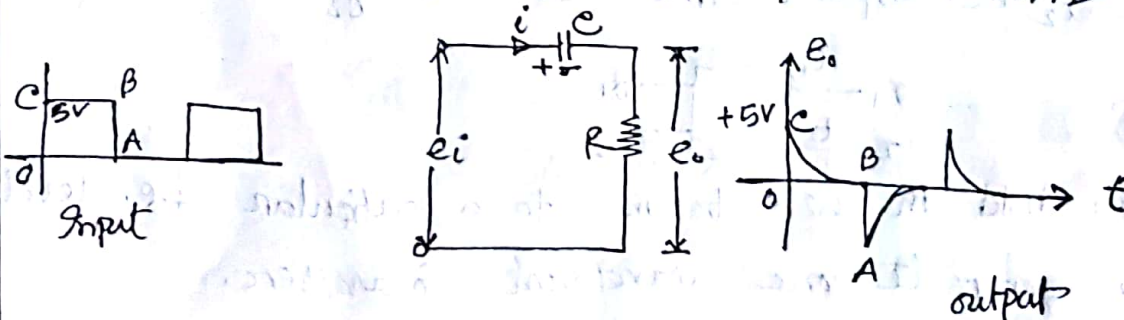
1. time constant must be very smaller.
2. the value of X_C should be 10 or more times larger.

output,

$$e_o = iR = RC \frac{d}{dt} (e_i) \propto \frac{d}{dt} (e_i) \quad [RC = \text{constant}]$$

output waveform: output waveform of this circuit depends upon the time constant and shape of input.

i) When input is a square wave: During OC part of input wave, its amplitude changes abruptly and hence the differentiated wave will be a sharp narrow pulse shown as -



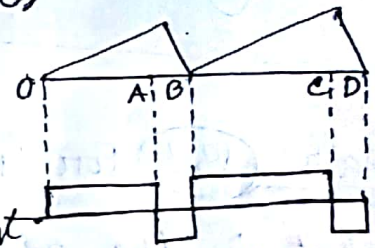
Since time constant RC of the circuit is very small w.r.t. time period of input wave and $X_C \gg R$, the capacitor will become fully charged during the early part

of each half cycle of input wave. During the remainder part of the half cycle, the output of the ~~wire~~ circuit will be zero because the capacitor voltage neutralises the input voltage and there can be no current flow through R .



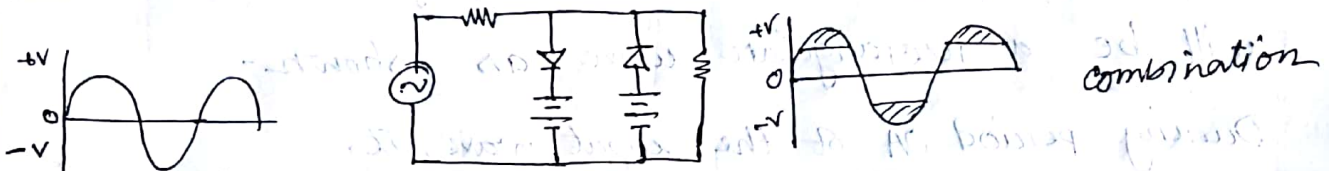
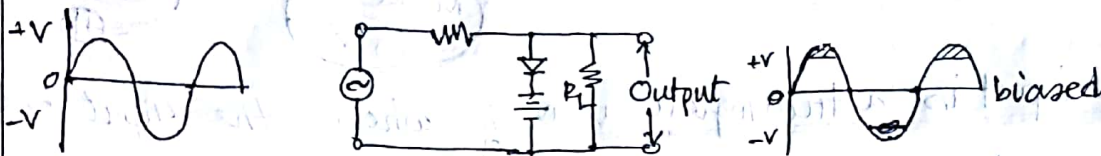
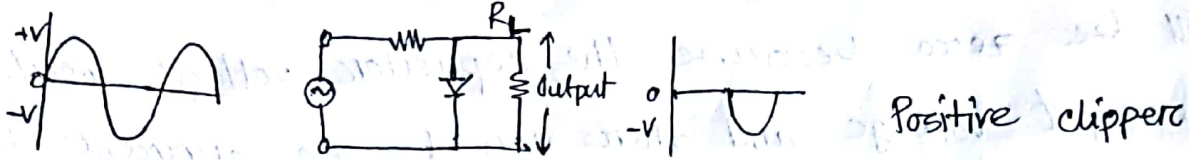
② When input is a triangular wave: when the input fed to a differentiating circuit is a triangular wave, the output will be a rectangular wave as shown -

During period OA of the input wave, its amplitude changes at a constant rate and therefore, the differentiated wave has a constant value for each constant rate of change.

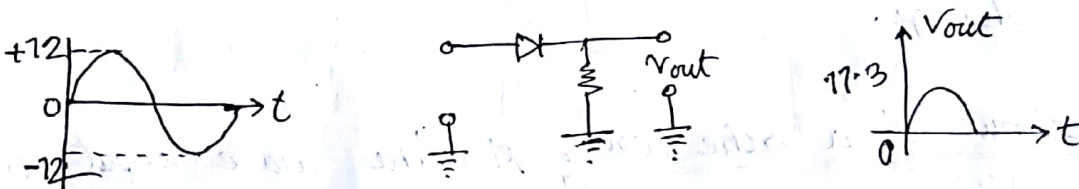


③ When input is a sine wave: A sine wave input becomes a cosine wave and a cosine wave input becomes an inverted sine wave at the output.

Clippers & Clampers:

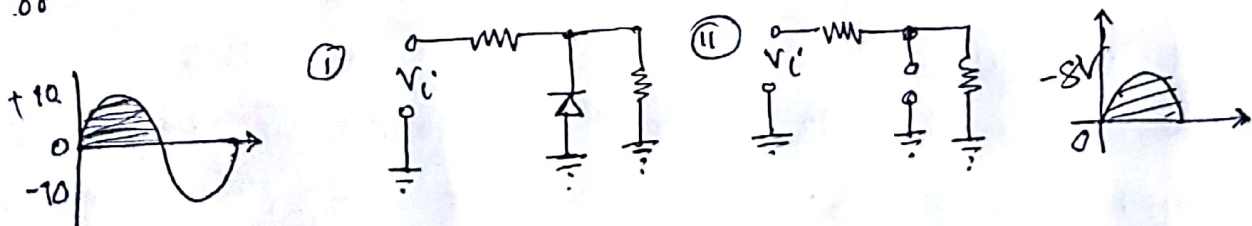


Maths (18.7) For the negative series clipper shown in fig. 18-28 what is the peak output voltage from the circuit?



$$V_{out(\text{peak})} = V_{in(\text{peak})} - 0.7 = 12 - 0.7 = 11.3 \text{ V}$$

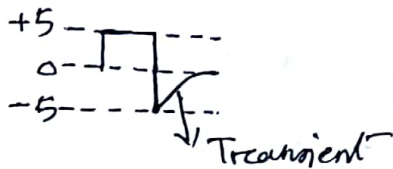
(18.8) negative shunt clipper (i) has a peak input voltage of +10V. What is the peak output voltage from circuit?



$$V_{out(peak)} = \text{Peak voltage across } R_L = \frac{R_L}{R + R_L} V_{in} = \frac{4}{1+4} \times 10 = 8$$

Application of clippers:

1. Changing the shape of a waveform;
2. Circuit transient protection: a transient is a sudden current or voltage rise that has an extremely short duration



3. Used as half wave rectifier in power supply;
4. Used as voltage limiter and amplitude selectors;
5. It clips either positive or negative cycle;
6. Used for separation of synchronizing signal;
7. Used for the generation of new wave forms or shaping the existing waveforms.

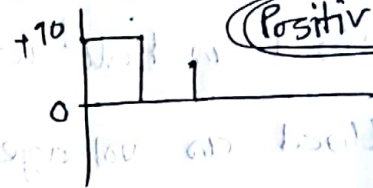
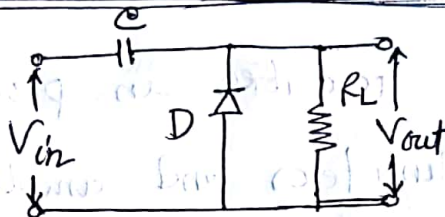
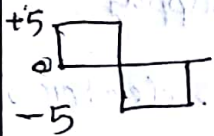
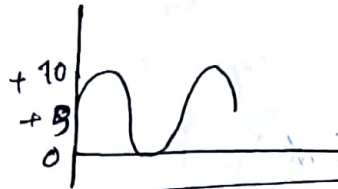
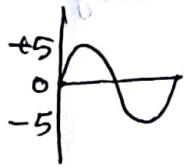
Application of clampers:

1. They clamp the wave forms to a fixed DC potential.
2. Used in test equipment, sonar and radar systems;
3. Used for protection of the amplifier;
4. Used as a base line stabilizer to define section of the luminance signals to present level.
5. Used for removing distortions.

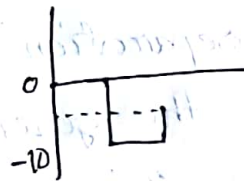
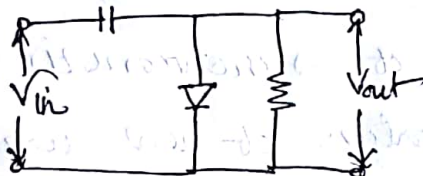
6. Used as voltage or voltage multipliers.

7. Used for improving the overdrive recovery time.

Clamping circuit: A circuit that places either the positive or negative peak of a signal at a desired d.c. level is known as clamping circuit.



(Positive clamper)



(Negative clamper)

Example: (18.19)

$$5 - V_c + 2 = 0 \Rightarrow V_c = 7V$$

$$-5 - 7 + V_{out} = 0 \Rightarrow V_{out} = -12V$$

(18.20)