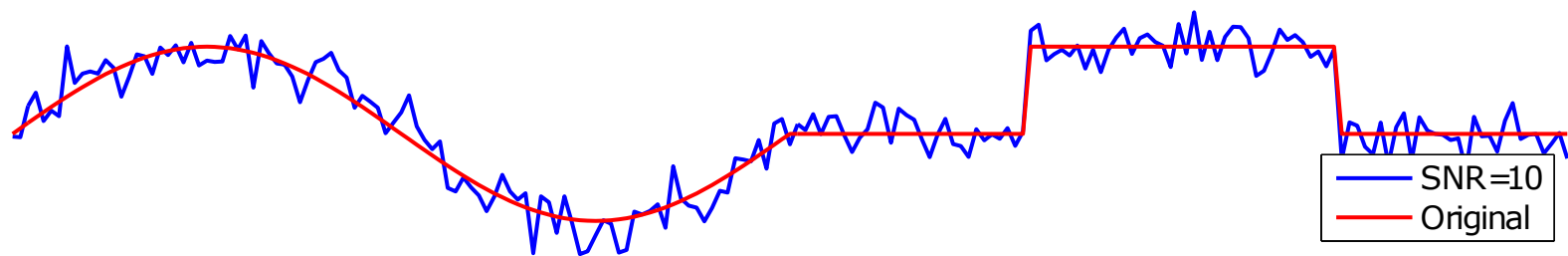
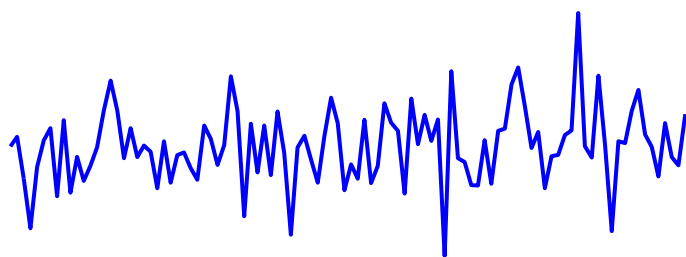


Input signals



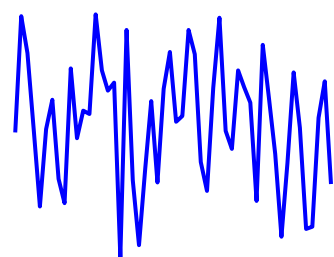
A1

D1



A2

D2



A3

D3

