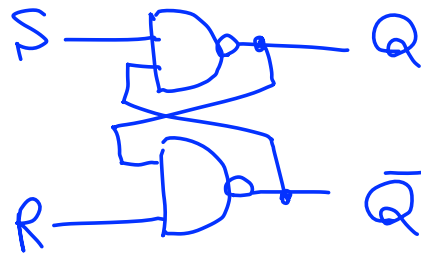


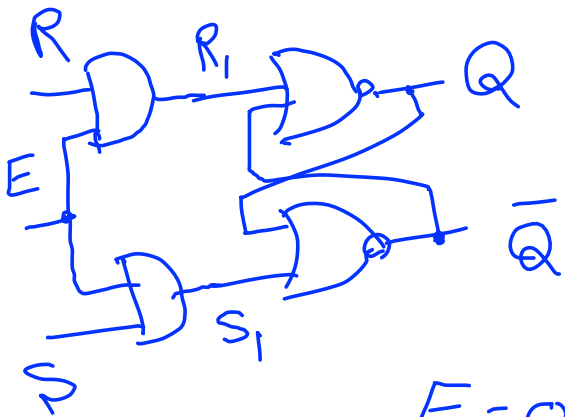
| S | R | Q^+ | \bar{Q}^+ | |
|---|---|-------|-------------|------------|
| 0 | 0 | Q | \bar{Q} | (memory) |
| 0 | 1 | 0 | 1 | (reset) |
| 1 | 0 | 1 | 0 | (set) |
| 1 | 1 | | | (غیر مجاز) |

active-high inputs



| S | R | Q^+ | \bar{Q}^+ | |
|---|---|-------|-------------|------------|
| 0 | 0 | | | (غیر مجاز) |
| 0 | 1 | 1 | 0 | (set) |
| 1 | 0 | 0 | 1 | (reset) |
| 1 | 1 | Q | \bar{Q} | (memory) |

active-low inputs



gated SR Latch

Clocked SR Latch

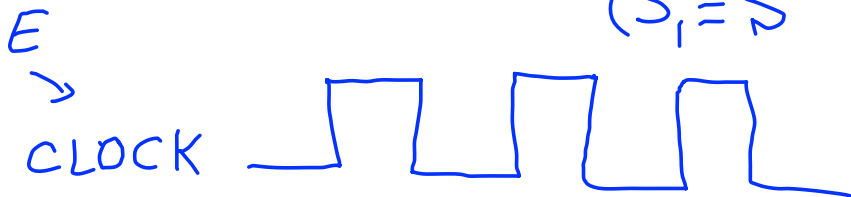
$$E=0 \Rightarrow S_1=R_1=0 \Rightarrow (\text{memory})$$

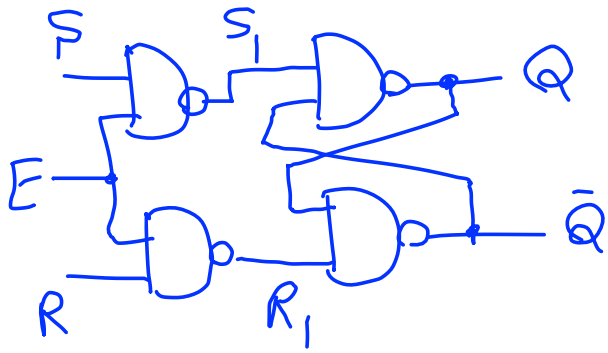
Enable

$$Q^+ = Q$$

$$E=1 \Rightarrow \begin{cases} R_1=R \\ S_1=S \end{cases} \rightarrow$$

عملکرد عادی





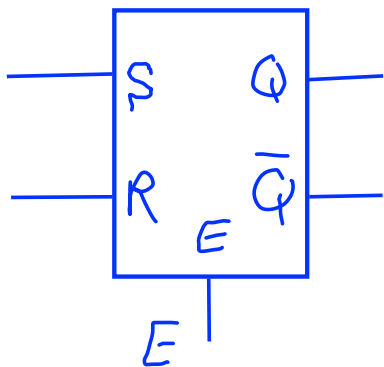
$E=0 \Rightarrow S_1=R_1=1 \Rightarrow \text{memory}$

$$Q^{n+1} = Q^n$$

$E=1 \Rightarrow \begin{cases} S_1 = \bar{S} \\ R_1 = \bar{R} \end{cases} \Rightarrow \text{عملگر عادی}$

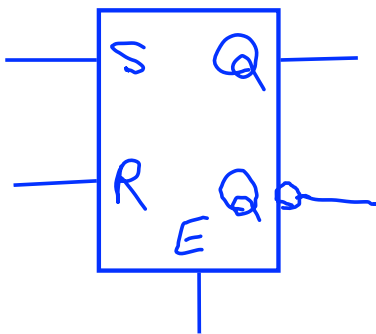
ورودی های S و R ، active high میباشند.

Clocked SR Latch }
gated = = }

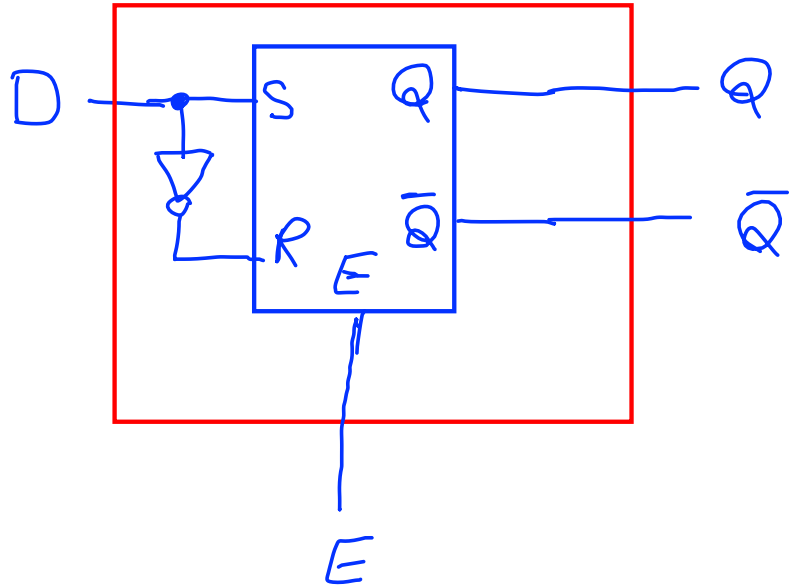
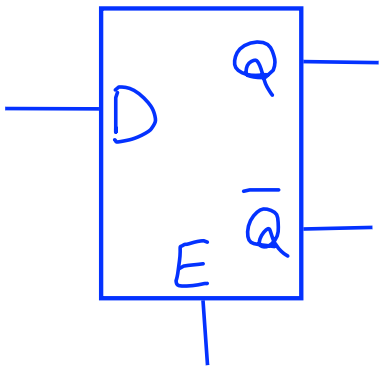


active - high

| S | R | Q^{n+1} | $\overline{Q^{n+1}}$ |
|---|---|-----------|----------------------|
| 0 | 0 | Q^n | $\overline{Q^n}$ |
| 0 | 1 | 0 | 1 |
| 1 | 0 | 1 | 0 |
| 1 | 1 | غیر مجاز | |



D Latch (Data Latch)



$$E=1$$

| S | R | Q^{n+1} |
|---|---|-----------|
| 0 | 0 | Q^n |
| 0 | 1 | 0 |
| 1 | 0 | 1 |
| 1 | 1 | غير محدد |

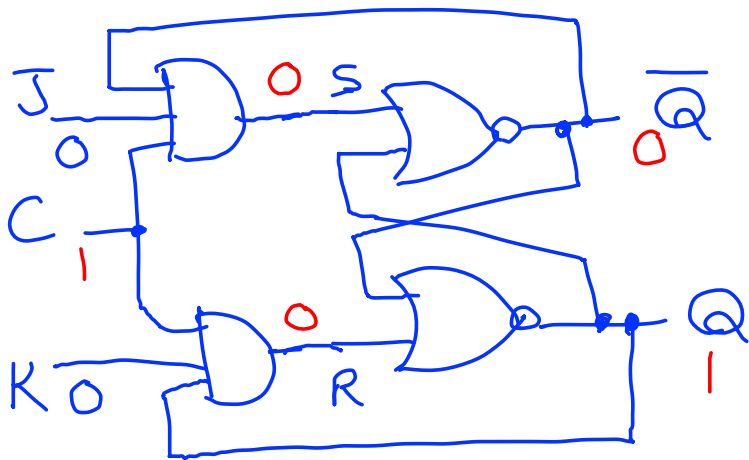
$$\Rightarrow$$

| D | Q^{n+1} |
|---|-----------|
| 0 | 0 |
| 1 | 1 |

$$E=0 \Rightarrow Q^{n+1} = Q^n$$

(memory)

J-K Latch

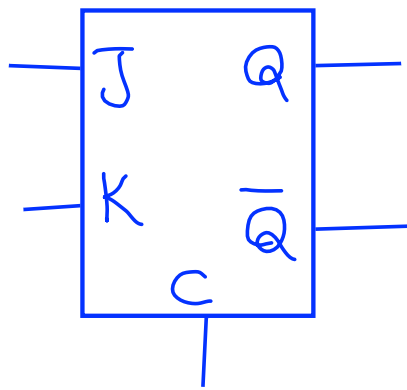


$$C=0 \Rightarrow Q^+ = Q$$

$$C=1$$

$$C \leq \text{Clock} = \text{CLK} = \text{ck}$$

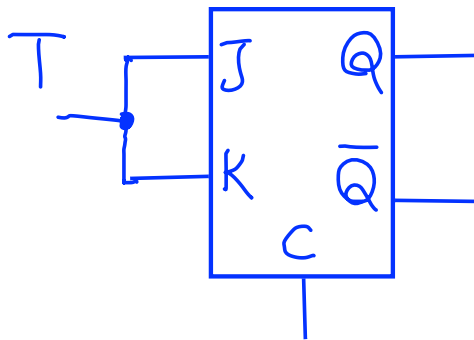
| J | K | Q | Q^+ | |
|---|---|---|-------|-----------------|
| 0 | 0 | 0 | 0 | } memory |
| 0 | 0 | 1 | 1 | |
| 0 | 1 | 0 | 0 | } reset |
| 0 | 1 | 1 | 0 | |
| 1 | 0 | 0 | 1 | } set |
| 1 | 0 | 1 | 1 | |
| 1 | 1 | 0 | 1 | } (toggle) |
| 1 | 1 | 1 | 0 | |
| | | | | $Q^+ = \bar{Q}$ |



| J | K | Q^{n+1} | |
|---|---|------------------|---------------------|
| 0 | 0 | Q^n | (memory) |
| X | 0 | 0 | (reset) |
| X | 1 | 1 | (set) |
| | 1 | $\overline{Q^n}$ | (toggle-Complement) |

$C=1$ ~ فرض

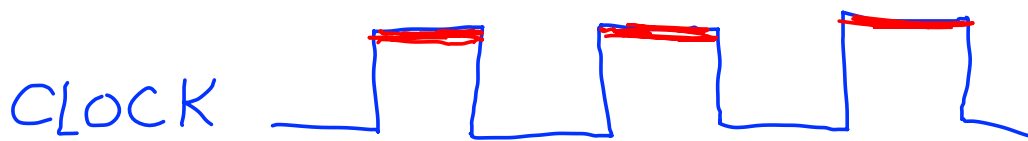
T-Latch



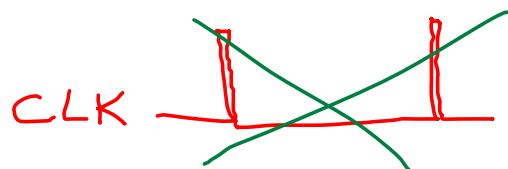
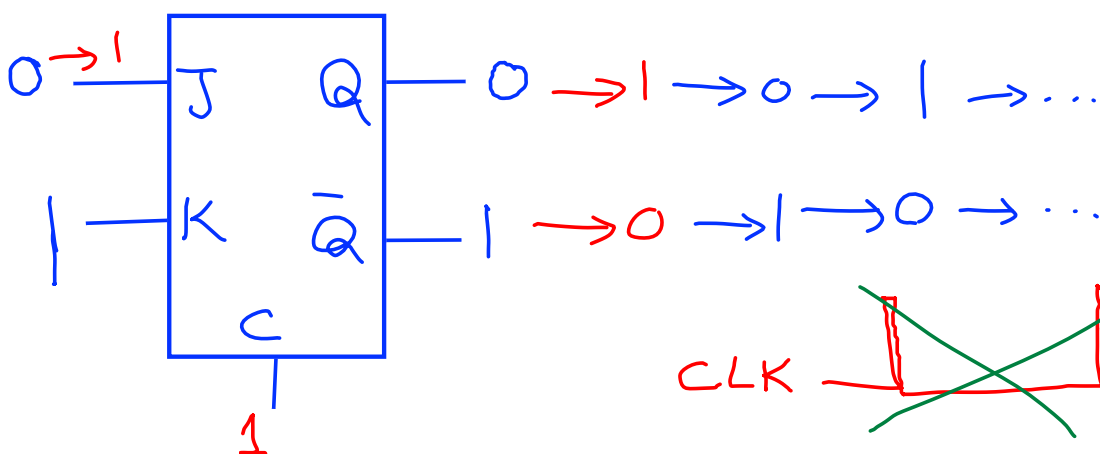
| T | Q^{n+1} | |
|---|------------------|----------|
| 0 | Q^n | (memory) |
| 1 | $\overline{Q^n}$ | (toggle) |

$$C=0 \Rightarrow Q^{n+1} = Q^n$$

$C=1$ ↗



Level sensitive Latch



Flip-Flop (FF)

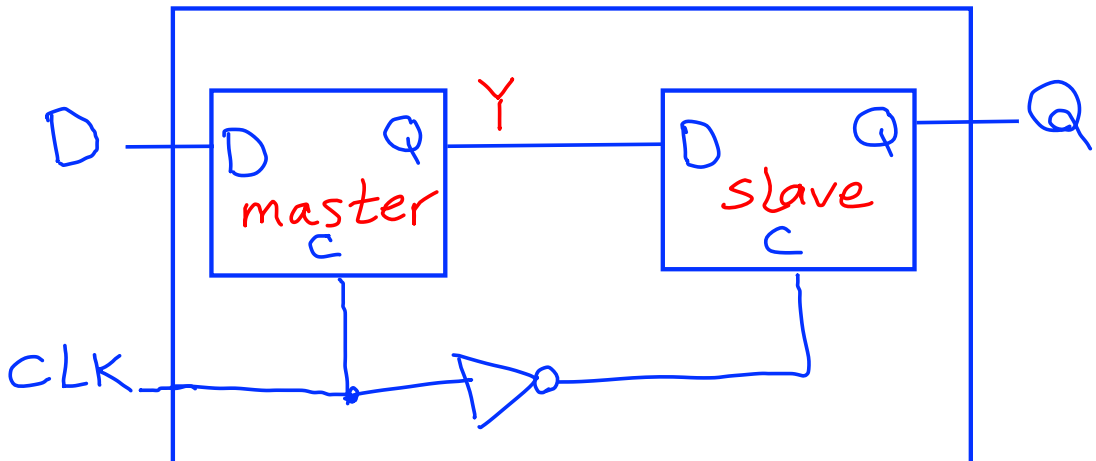
edge-sensitive CLK 

لب بالا رونده rising edge
لب مثبت positive edge

CLK 

لب پائین رونده falling edge
لب منفی negative edge

راه اندازی : trigger



master-slave D Flip-Flop

$CLK=0 \Rightarrow Q^+ = Q$ تغییر نمی‌نمایم

$CLK=1 \Rightarrow$ master: enable (فعال) $\rightarrow Y = D$
slave: disable (غیرفعال)

داده در master ذخیره می‌شود (ولی خروجی برای تغییر نمی‌کند)

تغییر ورودی بی اثر است \rightarrow master: disable \rightarrow CLK=0 \Rightarrow $\left\{ \begin{array}{l} \text{slave: enable} \rightarrow Q^+ = Y = D \end{array} \right.$
 خروجی FF صاف با D ذخیره شده می شود.

