## **College of Engineering and Technology (CoET)**

Weekly Report No: 15 from: 2025-10-20 to: 2025-10-24

## **Weekly Work Summary**

Day	Brief description of work performed	Hours	
Monday	Learned the basics of soldering, including handling tools, technic	qu6eS, and sat	ety measures.
Tuesday	Studied how to design an electric circuit using an electric design	ൾവെ, coverin	g schematics and prin
Wednesday	Focused on designing a printed circuit board (PCB) for a basic of	ir <b>5</b> រេបិt, using (	CAD tools.
Thursday	Learned electronics troubleshooting techniques to identify and fi	ix & Ommon fa	ults in circuits.
Friday	Designed an audio amplifier circuit on a PCB, selecting the requ	ir <b>5</b> @compon	ents and tracing the la
	Total hrs	28.0	

## **SEQUENCE OF CREATING AN AUDIO AMPLIFIER CIRCUIT**

No	Operation	Tools, Machinery, Equipment	
1	Components was placed on the PCB following the circuit diag	ra@apvándiecesi,sueristisstopiss;pæurcióci éancártí ob	M86, speakers and
2	Carefully soldering the components to the PCB, ensuring clear	n sololeeictigogsrantdovees/hsotslehintyevi	ne <b>s</b> c <b>jao</b> ரைச்ருins or trace
3	Attaching the power supply, audio input jack, and speaker to t	neAuesipejetike Beatteinyalsperaktee, PCB.	and
4	Powering the circuit for testing, play an audio signal, and test	h Moltinpoette A, dipasteeny, y spectalogs, läkeelid	p <b>jank</b> e laptop as a sourc

Signature Training Officer

Date