Computer Structure and Language

Hamid Sarbazi-Azad

Department of Computer Engineering Sharif University of Technology (SUT) Tehran, Iran



(c) Hamid Sarbazi-Azad

Computer Structure & Language -- Lecture #15: IBM360 Machine

2

Base Register Definition and Initialization

We use directive: USING BaseAddress, BR

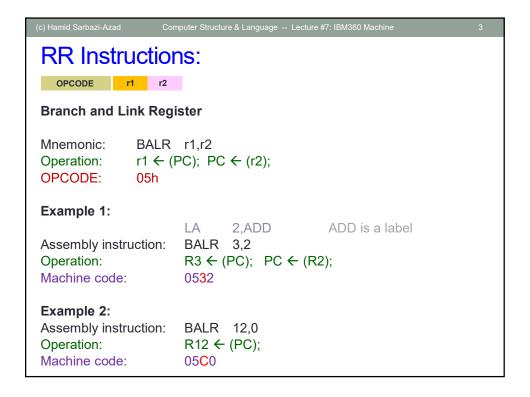
to tell to assembler that base register is BR and its content is BaseAddress. Note that this directive only informs assembler and does not initialize the base register BR. \rightarrow It is the duty of programmer to do so.

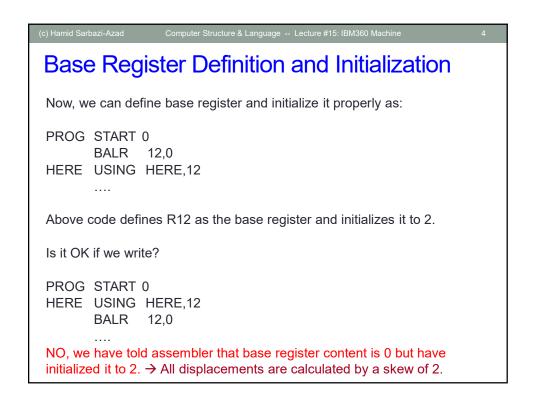
For example:

HERE USING HERE,12

Tells the assembler that base address is HERE and base register is R12. So, assembler generates the address for symbol LABEL as:

To initialize the base register, we usually use a specific instruction that is also used for subroutine call (BALR instruction).





```
Base Register Definition and Initialization

What about below code?

PROG START 0
HERE USING HERE+2,12
BALR 12,0
....

YES, it is OK. Base register is initialize to 2 and assembler knows that the base address is 2. 

Why not below code?

PROG START 0
HERE USING HERE+12
LA 12,HERE

NO, we do not have a base register is initialized to generate address HERE in LA instruction?
```

```
Base Register Definition and Initialization

Note that if we use * as a symbol in assembler instructions, it mean the Location Counter (address of current instruction).

So, the popular way to define base register is usually:

PROG START 0

BALR 12,0

USING *,12

....

Above code defines R12 as the base register and initializes it to 2.

Is the following code OK?

PROG START 0

USING *+2,12

BALR 12,0

....

Yes, it is OK.
```

(c) Hamid Sarbazi-Azad

Computer Structure & Language -- Lecture #15: IBM360 Machine

.

Base Register Definition and Initialization

What if we need more than one segment?

We can use directive USING as: USING BaseAddress,r1,r2,...

For example, below code defines 3 base registers R12, R11 and R9 in order.

```
BALR 12,0
USING *,12,11,9
LA 11,4095(12)
LA 11,1(11)
LA 9,4095(11)
LA 9,1(9)
```

The address of symbols in range:

BaseAddress...BaseAddress+4095 are generated by base register R12,

BaseAddress+4096...BaseAddress+8191 are generated by base register R11, BaseAddress+8192...BaseAddress+12287 are generated by base register R9.

(c) Hamid Sarbazi-Azad

Computer Structure & Language -- Lecture #15: IBM360 Machine

8

Base Register Definition and Initialization

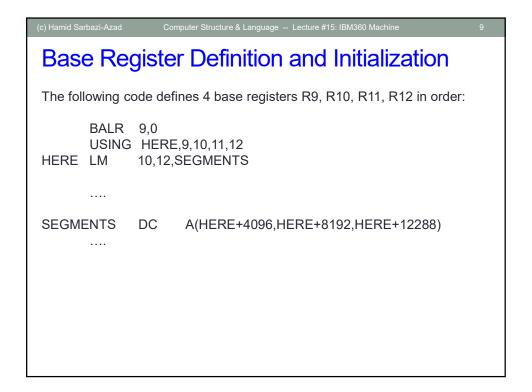
Alternative way to define 3 base registers R12, R11, and R9 in order, is:

```
BALR 12,0
USING HERE,12,11,9
HERE L 11,SEGMENT2
L 9,SEGMENT3
....
```

If we have the following variable definitions in the first segment:

```
SEGMENT2 DC A(HERE+4096)
SEGMENT3 DC A(HERE+8192)
```

Note: Type A defines a full-word that contains an address.



(c) Hamid Sarbazi-Azad

Computer Structure & Language -- Lecture #15: IBM360 Machine

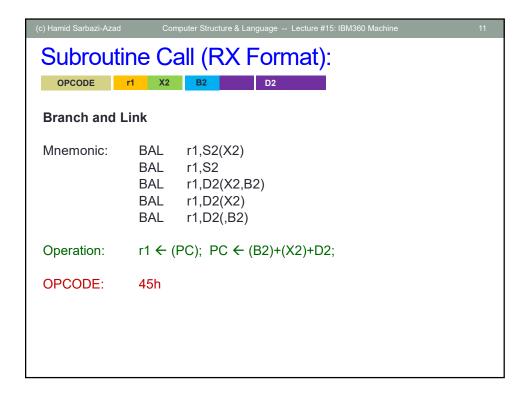
10

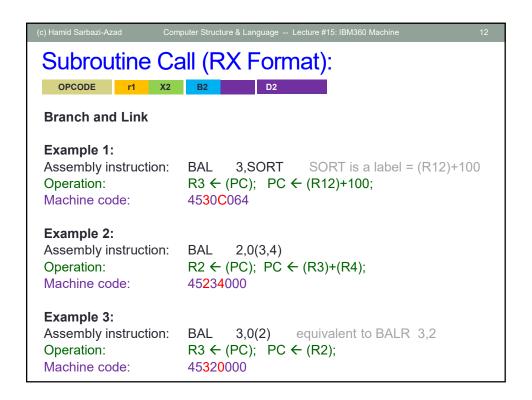
Subroutines

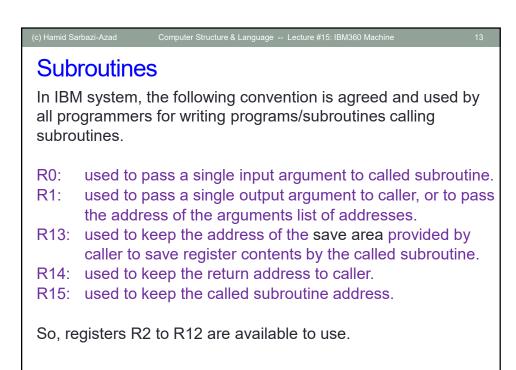
Subroutine calls can be done by BALR instruction where r1 keeps a copy of PC (to be used at the end of subroutine for return) and fills PC with the address of the first instruction of the subroutine which is in r2.

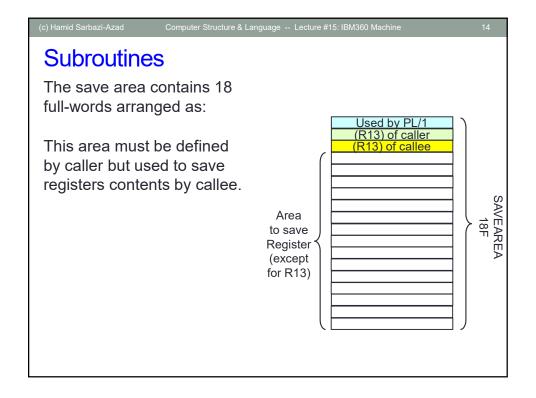
Another instruction in RX format can be used to directly call a subroutine (no need to copy its address into register r2 and then use BALR r1,r2).

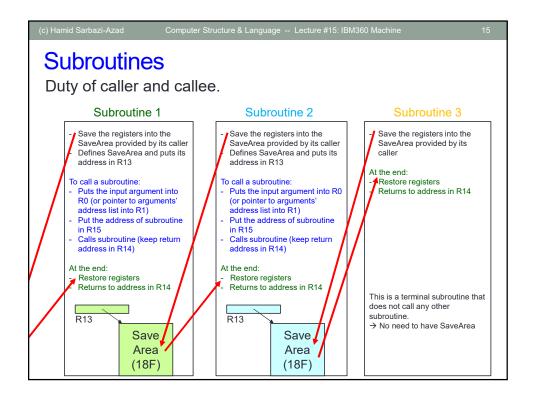
It is BAL instruction.

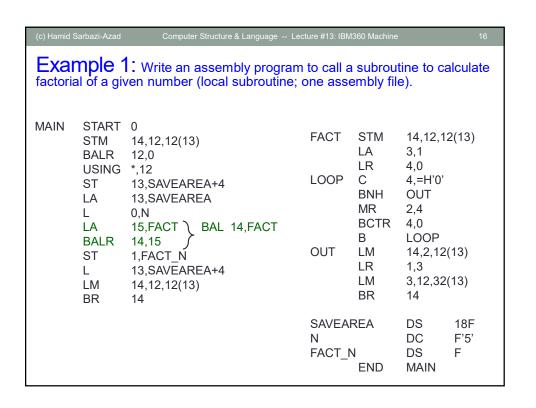












Example 2: Write an assembly program to call a subroutine to calculate factorial of a given number (external subroutine; two assembly files). MAIN START 0 STM 14,12,12(13) **BALR** 12,0 USING *,12 ST 13,SAVEAREA+4 LA 13, SAVEAREA L 0.N 15, FACTADDR L BALR 14,15 1,FACT N ST L 13,SAVEAREA+4 14,12,12(13) LM BR 14 SAVEAREA DS 18F DC F'5' Ν DS FACT N

V(FACT)

FACTADDR

END

DC

MAIN

.12(13)
12(13)
)'
)
2(13)
2(13)

```
(c) Hamid Sarbazi-Azad
                     Computer Structure & Language -- Lecture #13: IBM360 Machine
Example 3:
Write an assembly program to call a subroutine to calculate Σ(ARRAY[i]!) for
array ARRAY of N positive halfwords. Suppose we have external subroutine FACT
in Example 2.
                                        ADDER
                                                 START
 MAIN
         START 0
                                                 STM
                                                         14,12,12(13)
         STM
                 14,12,12(13)
                                                 BALR
                                                         12,0
         BALR
                 12,0
                                                 USING
                                                         *,12
                                                         13,SAVEA+4
         USING *,12
                                                 ST
                                                 LA
                                                         13.SAVEA
         ST
                 13,SAVEA+4
                                                                  @N
                                                         2,0(1)
         LA
                 13,SAVEA
                                                 LH
                                                         2,0(2)
         LA
                 1,ARGS
                                                                  @ARRAY
                                                         3,4(1)
                                                 L
                 15,=V(ADDER)
         L
                                                         5,8(1)
                                                                  @SUM
                                                 XR
                                                         4.4
         BALR
                 14,15
                                         LOOP
                                                 LH
                                                         0,0(3)
                 13,SAVEA+4
                                                 BAL
                                                         14,=V(FACT)
         LM
                 14,12,12(13)
                                                 AR
                                                         4,1
         BR
                 14
                                                 LA
                                                         3,2(3)
 SAVEA DS
                 18F
                                                 BCT
                                                         2,LOOP
                                                         4,0(5)
                                                                 Store in SUM
         DC
                 H'5'
                                                 ST
 Ν
                                                         13, SÁVEA+4
 ARRAY DC
                 H'3,2,7,4,5'
                                                 I M
                                                         14,12,12(13)
 SUM
         DS
                                                 BR
                                                         14
 ARGS
         DC
                 V(N,ARRAY,SUM)
                                         SAVEA
                                                 DS
                                                         18F
         END
                 MAIN
                                                 END
                                                         ADDER
```

