eax	ebx	есх	edx	esi	edi	offset	memory	offset	memory	stack
						1001	S	100A	k	
						1002	t	100B	i	
						1003	а	100C	n	
						1004	С	100D	g	
						1005	k	100E	0	
						1006	i	100F		1
						1007	n	1010		
						1008	g	1011		
						1009	0	1012		_

eax	ebx	есх	edx	esi	edi	offset	memory	offset	memory	stack
						1001	S	100A	t	
						1002	t	100B	а	
						1003	а	100C	С	
						1004	С	100D	0	
						1005	k	100E		
						1006	i	100F		
						1007	n	1010		
						1008	g	1011		
						1009	0	1012		_

eax	ebx	есх	edx	esi	edi	offset	memory	offset	memory	stack
						1001	S	100A	b	
						1002	t	100B	а	
						1003	а	100C	С	
						1004	С	100D	k	
						1005	k	100E	0	
						1006	i	100F		
						1007	n	1010		
						1008	g	1011		
						1009	0	1012		

ebx	есх	edx	esi	edi	offset	memory	offset	memory	stack
					1001		100A		
					1002		100B		
					1003		100C		
					1004		100D		
					1005		100E		
					1006		100F		
					1007		1010		
					1008		1011		
					1009		1012		
						1002 1003 1004 1005 1006 1007	1002 1003 1004 1005 1006 1007 1008	1002 1008 1003 100C 1004 100D 1005 100E 1006 100F 1007 1010 1008 1011	1002 1008 1003 100C 1004 100D 1005 100E 1006 100F 1007 1010 1008 1011

eax	ebx	есх	edx	esi	edi	offset	memory	offset	memory	stack
						1001		100A		
						1002		100B		
						1003		100C		
						1004		100D		
						1005		100E		
						1006		100F		
						1007		1010		
						1008		1011		
						1009		1012		

eax	ebx	есх	edx	esi	edi	offset	memory	offset	memory	stack
						1001		100A		
						1002		100B		
						1003		100C		
						1004		100D		
						1005		100E		
						1006		100F		
						1007		1010		
						1008		1011		
						1009		1012		