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14 POWER OPTION

Revision	Description	Date	Drawn	Checked	
Ver 1.0		2015-01-05	2015-01-05		
Ver 1.1		2015-01-23			

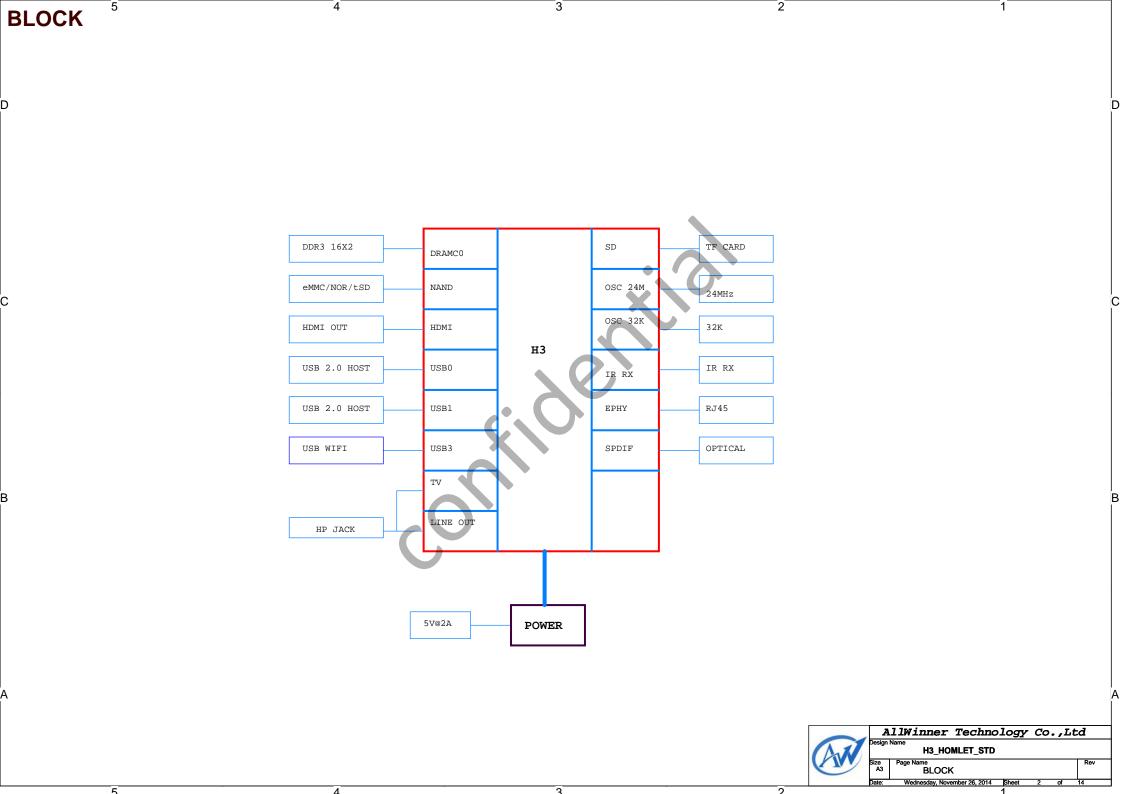


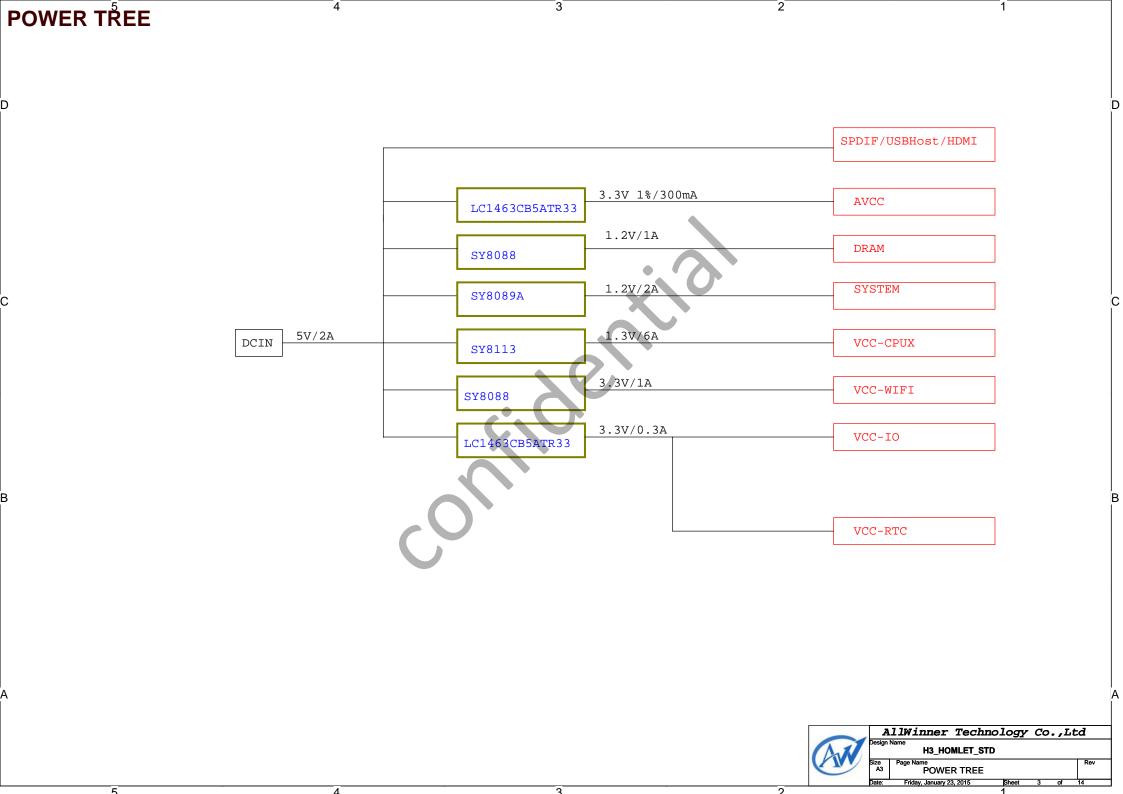
AllWinner Technology Co.,Ltd

Design Name
H3_HOMLET_STD

Size Page Name
REVISION HISTORY

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GPIO ASSIGNMENT

PIN Define CFGFunction JTAG /USB 3/1 PA3 TDI PA4 UART-TX UART PA5 UART-RX SIM-PWREN SIM-CLK SMART SIM-DAT PA8 CARD SIM-RST PA10 SIM-DET PA11 NC PA12 NC PA13 NC PA14 USBID PA15 STATUS-LED 1 LED PA16 MUTE 1 AV PA17 SPDIF-OUT 2 SPDIF PA18 NC 7 PA19 NC PA20 NC 7 PA21 NC

PIN	Define	CFG	Function
PC0	NWE	2/3	
PC1	NALE	2/3	
PC2	NCLE	2/3	
PC3	NCE1	2/3	
PC4	NCE0	2	
PC5	NRE	2/3	
PC6	NRB0	2/3	NAND
PC7	NRB1	2	/eMMC
PC8	NDQ0	2/3	/NOR
PC9	NDQ1	2/3	
PC10	NDQ2	2/3	
PC11	NDQ3	2/3	
PC12	NDQ4	2/3	
PC13	NDQ5	2/3	
PC14	NDQ6	2/3	
PC15	NDQ7	2/3	
PC16	NDQS	2/3	

PIN	Define	CFG	Function
PD0	NC	7	
PD1	NC	7	
PD2	NC	7	
PD3	NC	7	
PD4	NC	7	
PD5	NC	7	
PD6	NC	7	
PD7	NC	7	
PD8	NC	7	
PD9	NC	7	
PD10	NC	7	
PD11	NC 🌑	7	
PD12	NC	7	
PD13	NC NC	7	
PD14	NC	7	
PD15	NC	7	
PD16	NC	7	
PD17	NC	7	

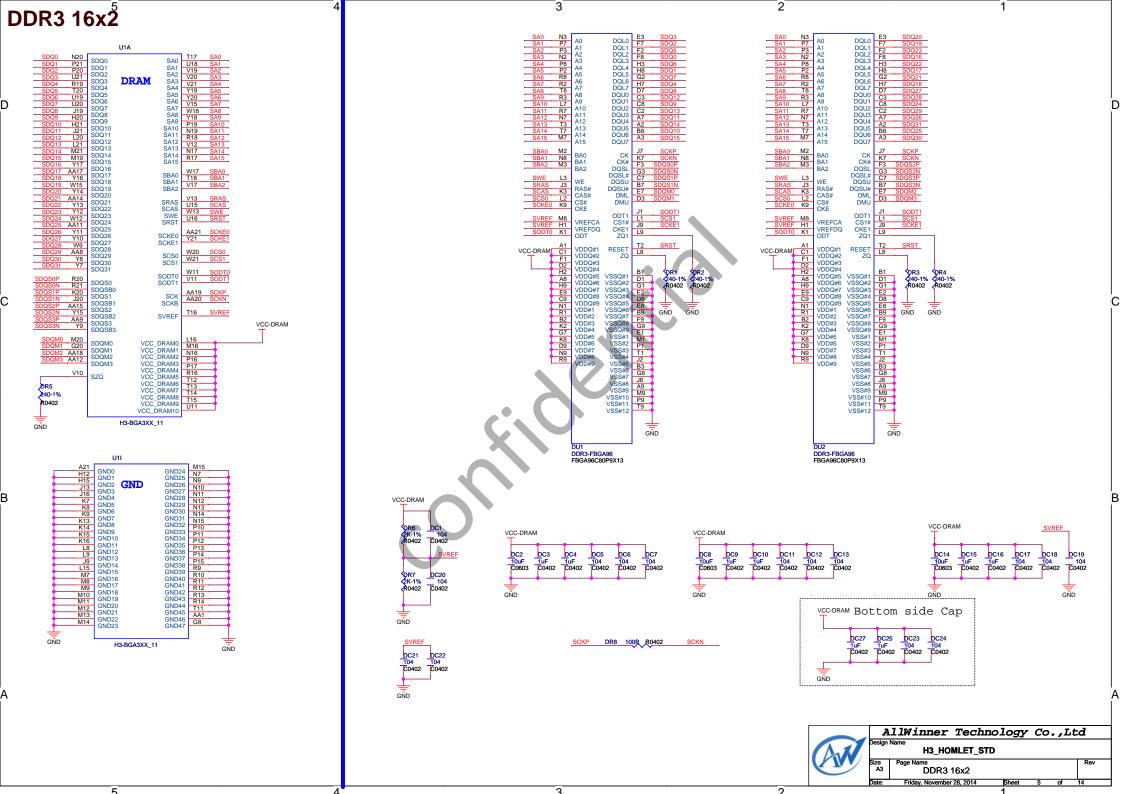
PIN	Define	CFG	Function
PE0	TS-CLK	7	
PE1	TS-ERR	7	
PE2	TS-SYNC	7	
PE3	TS-DVLD	7	
PE4	TS-D0	7	
PE5	TS-D1	7	
PE6	TS-D2	7	
PE7	TS-D3	7	
PE8	TS-D4	7	
PE9	TS-D5	7	
PE10	TS-D6	7	
PE11	TS-D7	7	
PE12	NC	7	
PE13	NC	7	
PE14	NC	7	
PE15	NC	7	

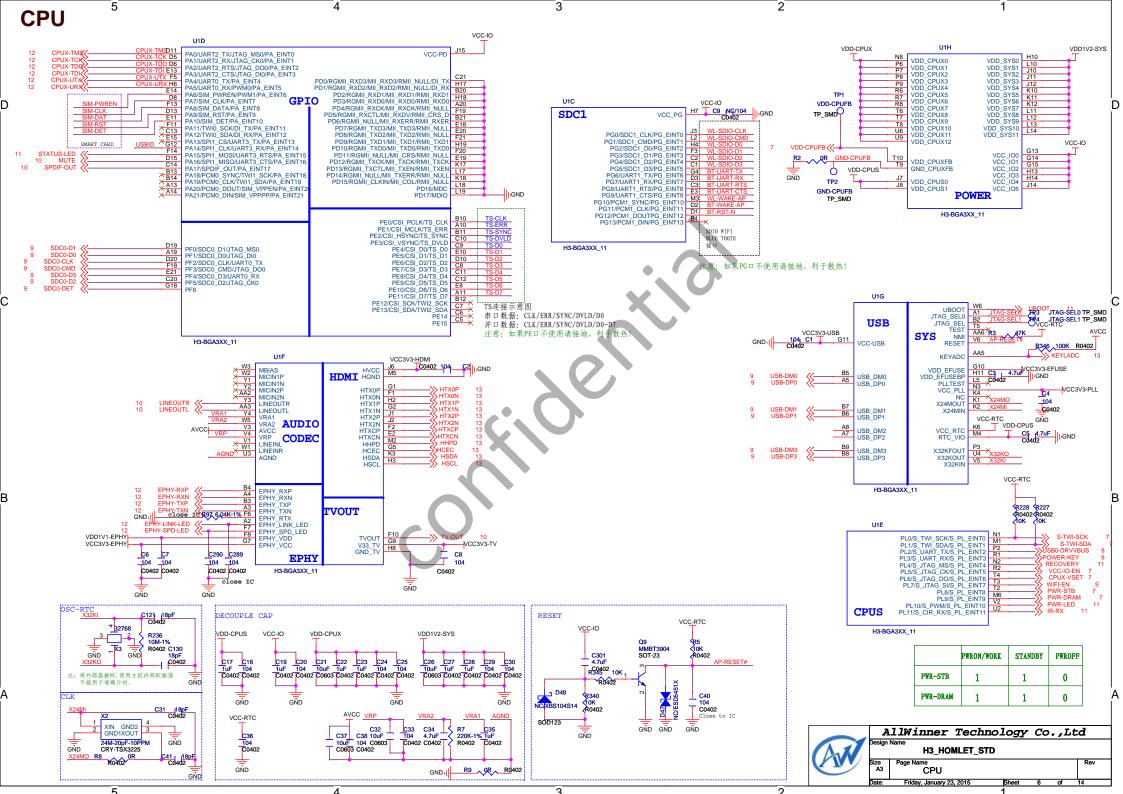
PIN	Define	CFG	Function
PF0	D1	2	
PF1	D0	2	
PF2	CLK	2	CARD0
PF3	CMD	2	
PF4	D3	2	
PF5	D2	2	
PF6	DET	0	

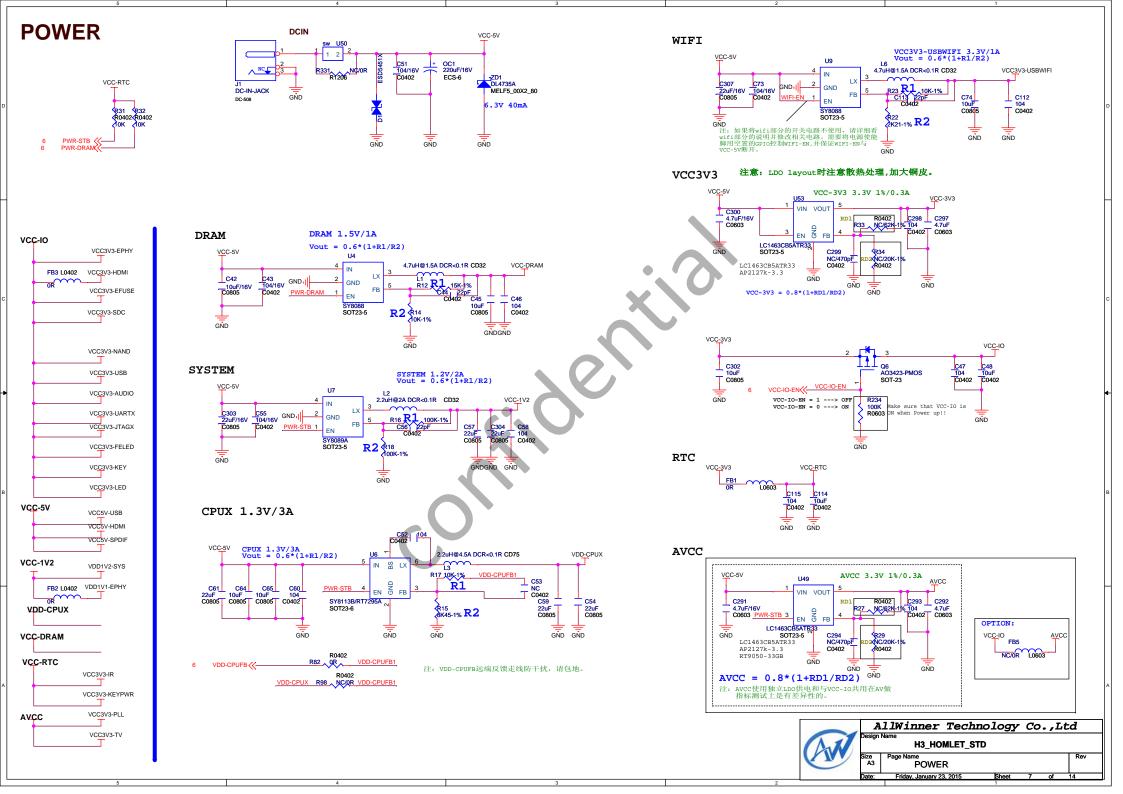
PIN	Define	CFG	Function
PG0	WL-SDIO-CLK	7	
PG1	WL-SDIO-CMD	7	
PG2	WL-SDIO-D0	7	WIFI
PG3	WL-SDIO-D1	7	MILI
PG4	WL-SDIO-D2	7	
PG5	WL-SDIO-D3	7	
PG6	BT-UART-TX	7	
PG7	BT-UART-RX	7	BT
PG8	BT-UART-RTS	7	БІ
PG9	BT-UART-CTS	7	
PG10	WL-WAKE-AP	7	
PG11	BT-WAKE-AP	7	WIFI&BT
PG12	BT-RST-N	7	GPIO
PG13	NC	7	

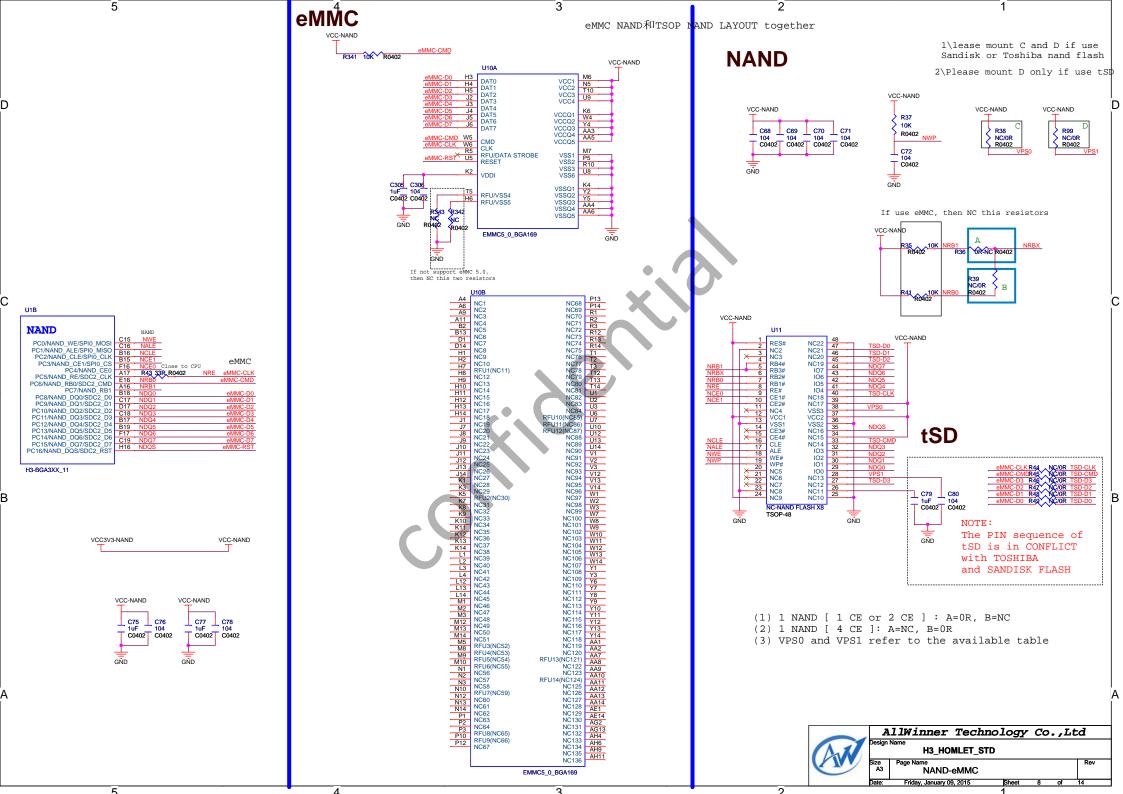
PIN	Define	CFG	Function	
PL0	TWI	2	TWI	
PL1	TWI	2	11/1	
PL2	USB0-DRVVBUS	1	USB	
PL3	USB1-DRVVBUS	1	OSB	
PL4	RECOVERY	0	KEY	
PL5	VCC-IO-EN	1	IO-EN	
PL6	CPUX-VSET	7	CPUX-VSET	
PL7	WIFI-EN	7	WIFI-EN	
PL8	PWR-STB	1		
PL9	PWR-DRAM	1		
PL10	PWR-LED	1		
PL11	IR-RX	2		

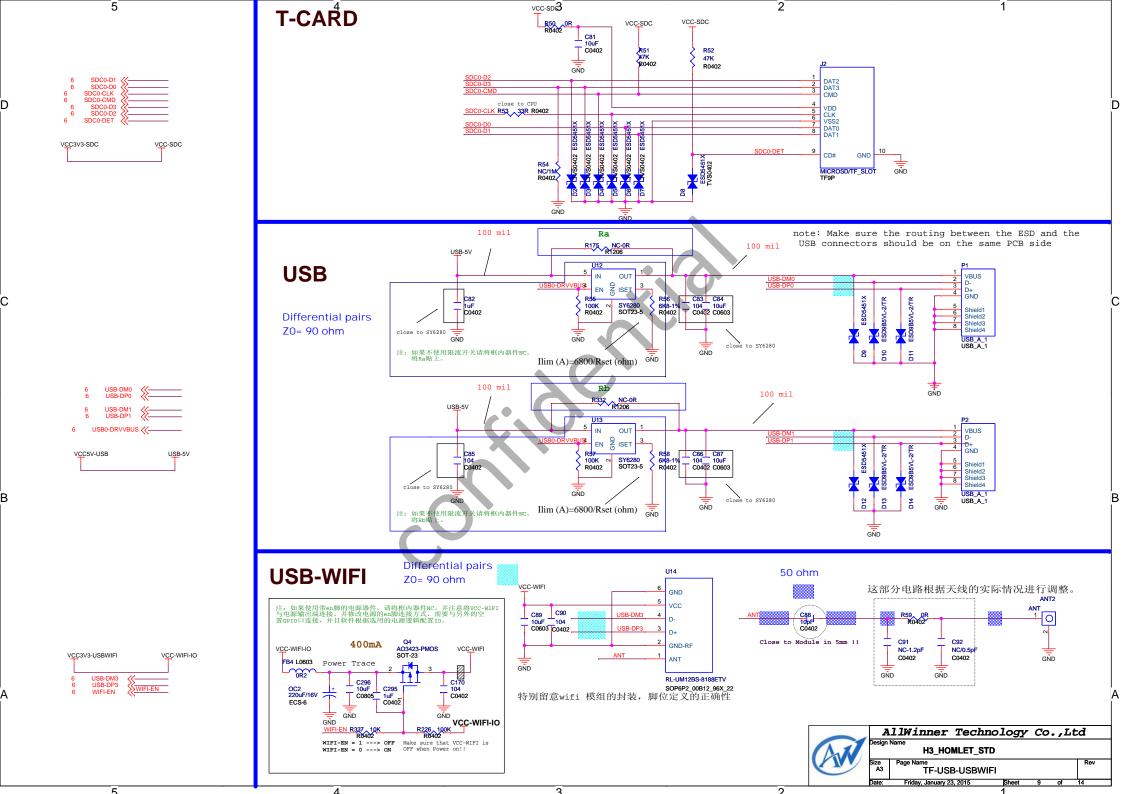
AllWinner Technology Co.,Ltd H3_HOMLET_STD ĞPIO ASSIGNMENT Wednesday, November 26, 2014 Sheet

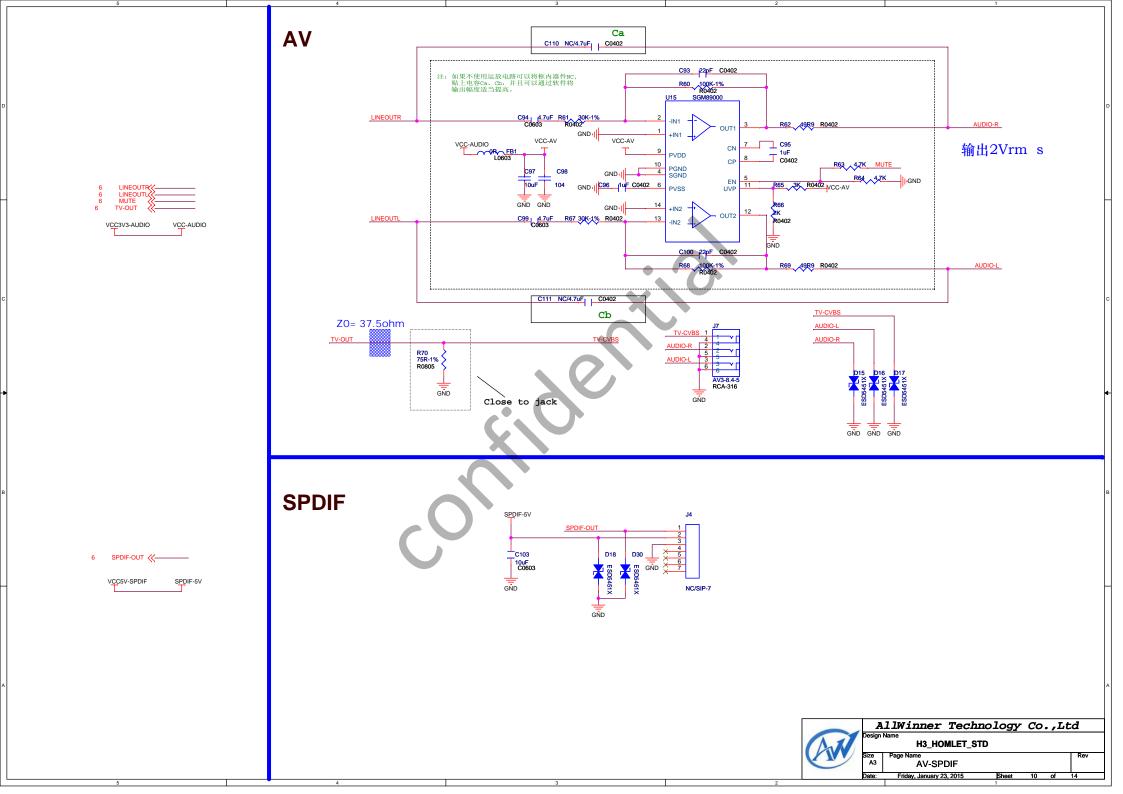


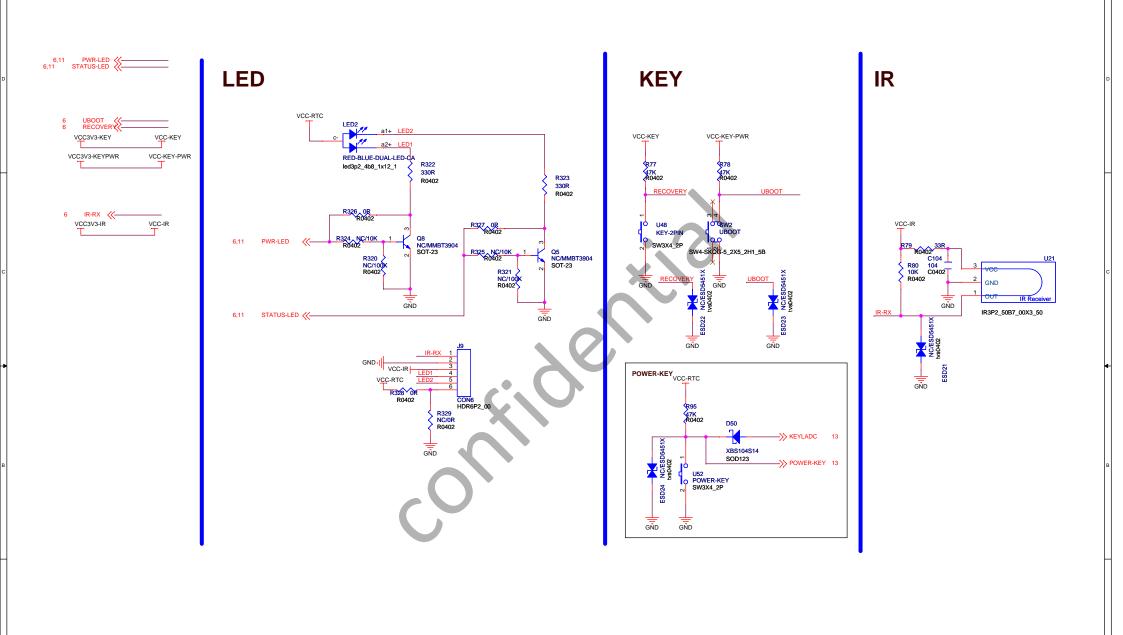


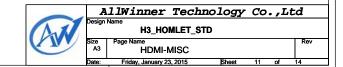


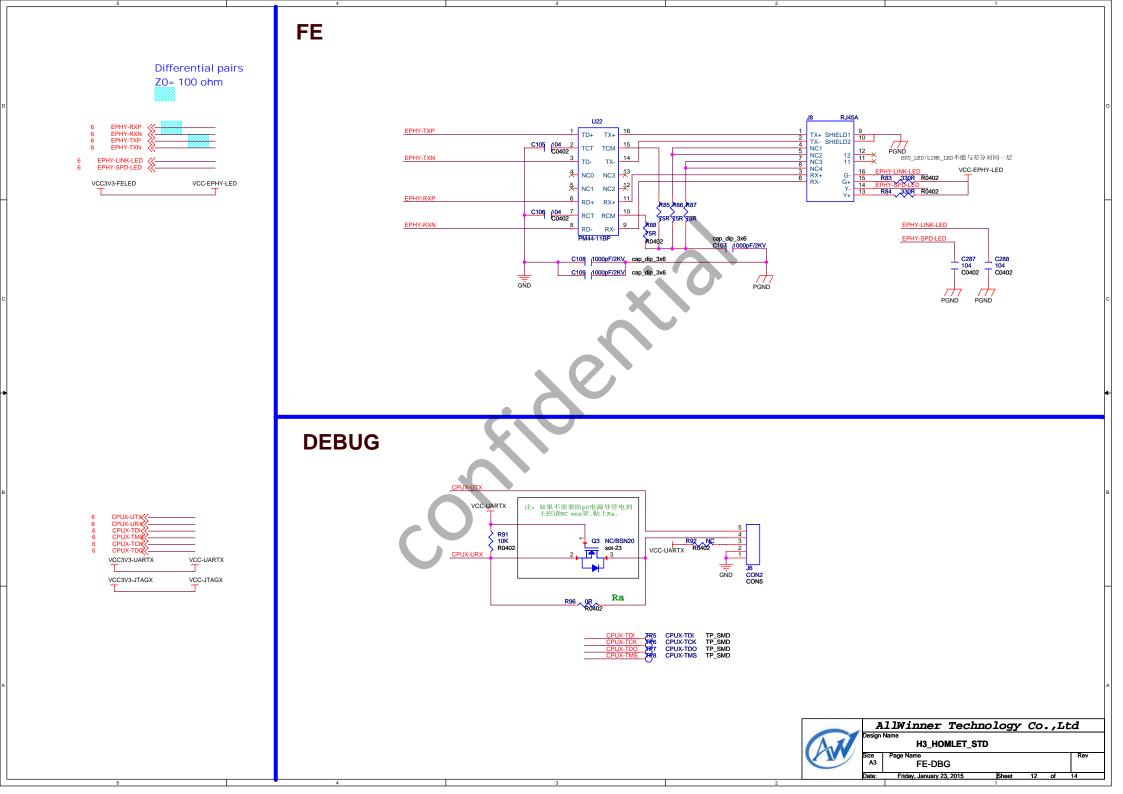




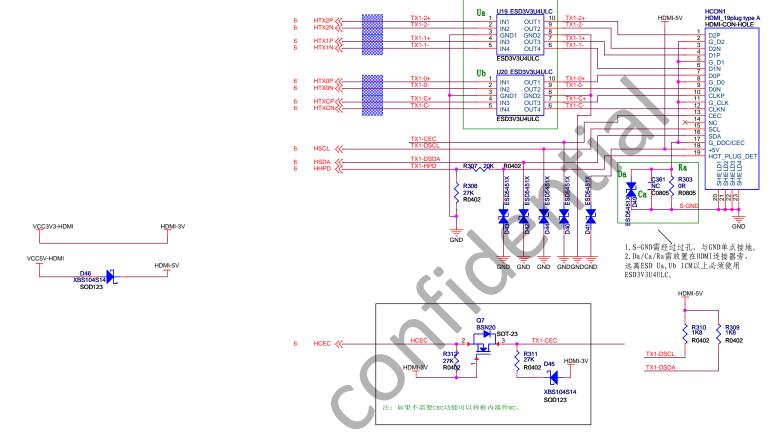


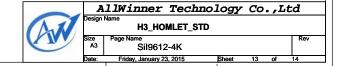






HDMI





HCON1 HDMI_19plug type A HDMI-CON-HOLE

HDMI-5V

