# Thermoelectric $Si_{1-x}Ge_x$ and Ge epitaxial films on Si(001) with controlled composition and strain for group IV element-based thermoelectric generators

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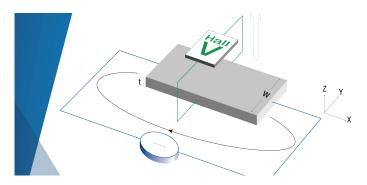
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# **ABSTRACT**

This study presents the material design of  $\mathrm{Si}_{1-x}\mathrm{Ge}_x$  epitaxial films/Si for thin film thermoelectric generators (TFTEGs) by investigating their thermoelectric properties. The thermoelectric films composed of group-IV elements are advantageous due to their compatibility with the Si process. We fabricated  $\mathrm{Si}_{1-x}\mathrm{Ge}_x$  epitaxial films with various controlled x values and strains using various growth methods. Ge epitaxial films without strains exhibited the highest thermoelectric power factor ( $\sim$ 47  $\mu\mathrm{W}$  cm $^{-1}$  K $^{-2}$ ) among various strain-controlled  $\mathrm{Si}_{1-x}\mathrm{Ge}_x$  ( $x\neq 1$ ) epitaxial films, which is higher at room temperature than SiGe alloy-based bulks ever reported. On the other hand, strained  $\mathrm{Si}_{1-x}\mathrm{Ge}_x$  epitaxial films showed an ultralow thermal conductivity of  $\sim$ 2 W m $^{-1}$  K $^{-1}$ , which is close to the value for amorphous Si. In addition to strained SiGe films with the ultralow thermal conductivity, unstrained Ge films with a high thermoelectric power factor can also be used for future TFTEGs by applying a nanostructuring technique. A preliminary TFTEG of Ge epitaxial films was realized, which generated a maximum power of  $\sim$ 0.10  $\mu\mathrm{W}$  cm $^{-2}$  under a temperature difference of 20 K. This demonstrates that epitaxial films composed of group-IV semiconductors are promising materials for TFTEG applications.

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Thermoelectric generators (TEGs), which enable us to reuse a vast amount of waste heat, are attracting great interest as one of the sustainable energy sources. The thermoelectric (TE) conversion efficiency of the materials is evaluated by the dimensionless figure-of-merit  $ZT (=S^2\sigma T/\kappa)$ , where S is the Seebeck coefficient,  $\sigma$  is the electrical conductivity, T is the absolute temperature, and  $\kappa$  is the thermal conductivity. Considerable effort has been made on developing high ZT materials, anamely, the simultaneous realization of high power factor  $S^2\sigma$  and low  $\kappa$ . A large temperature difference  $\Delta T$  and high  $S^2\sigma$  are important for obtaining a large output power from the thin film TEG (TFTEG). To obtain a sufficient  $\Delta T$ , there were a lot of pronounced reports about the  $\kappa$  reduction of TE films by nanostructuring with almost no degradation of high  $S^2\sigma$ , for example, through the fabrication of holes or nanodots in the films using the Si process technique. Some sufficient  $S^2\sigma$  are gaining importance

when they are compatible with the Si process technique to use the nanostructuring technique for  $\kappa$  reduction.

Group-IV semiconductors, such as Si, are suitable for the industry because of their eco-friendly and non-toxic properties. In the TE research field, there have been a lot of studies about bulk  $\mathrm{Si}_{1-x}\mathrm{Ge}_x^{8,9,27-29}$  and polycrystalline  $\mathrm{Si}_{1-x}\mathrm{Ge}_x$  films.  $^{4,12,14}$   $\mathrm{Si}_{1-x}\mathrm{Ge}_x$  ( $0 < x \le 1$ ) TE films on Si substrates are compatible with Si technology,  $^{17,22-26}$  which has aided the realization of various digital devices, where the carrier mobility  $\mu$  was effectively controlled by varying x and the hetero-epitaxial strain. There are many TE studies on Si-based superlattice films.  $^{18,30-32}$  On the other hand, the "Si $_{1-x}\mathrm{Ge}_x$  epitaxial films" on Si substrates, which can give basic character depending on x and strain, have not been experimentally investigated in terms of "TE properties" except for our previous work about p-type Ge epitaxial films; <sup>33</sup> despite their advantages, a lot of information exists about group-IV semiconductors, their good properties

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(high S and  $\mu$ ), and their compatibility with industry. The epitaxial films have intermixed layers, strains, and dislocations at the hetero-interfaces between the  $\mathrm{Si}_{1-x}\mathrm{Ge}_x$  films and  $\mathrm{Si}$  substrates, which influence carrier and phonon transports related to the TE properties. Moreover, the Ge composition x has an impact on the electronic states of  $\mathrm{Si}_{1-x}\mathrm{Ge}_x$ . The TE properties of epitaxial films are not sufficiently simple to be predicted in the same manner as bulk  $\mathrm{Si}_{1-x}\mathrm{Ge}_x$ . For group IV element-based TFTEGs, it is necessary to clarify the TE properties of  $\mathrm{Si}_{1-x}\mathrm{Ge}$  epitaxial films depending on the strain and composition.

In this study, we establish a material design of  $\mathrm{Si}_{1-x}\mathrm{Ge}_x$  epitaxial films/Si for TFTEG applications.  $\mathrm{Si}_{1-x}\mathrm{Ge}_x$  epitaxial films with various controlled x values and strain are fabricated using two kinds of growth methods. Ge epitaxial films exhibit the highest  $S^2\sigma$  ( $\sim$ 47  $\mu\mathrm{W}$  cm $^{-1}$  K $^{-2}$ ) among the strain-controlled SiGe epitaxial films, which is higher than those of the n-type SiGe-alloy-based bulks that have been ever reported at room temperature.  $^{34}$  On the other hand,  $\mathrm{Si}_{0.7}\mathrm{Ge}_{0.3}$  films display ultralow  $\kappa$  ( $\sim$ 2 W m $^{-1}$  K $^{-1}$ ). Thus, SiGe epitaxial films are suitable due to the ultralow  $\kappa$ , and Ge epitaxial films with higher  $S^2\sigma$  can also be a material candidate for TFTEG applications by applying the nanostructuring technique in the future to generate sufficient  $\Delta T$ . The preliminary TFTEG of the Ge epitaxial film is realized and generates a maximum power of  $\sim$ 0.10  $\mu\mathrm{W}$  cm $^{-2}$  under a  $\Delta T$  value of 20 K. This demonstrates that  $\mathrm{Si}_{1-x}\mathrm{Ge}_x$  epitaxial films/Si can be promising TFTEG materials.

Strained and unstrained Si<sub>1-x</sub>Ge<sub>x</sub> epitaxial films were fabricated on Si substrates using one-step and two-step growth, respectively, in the following manner. 35,36 Chemically cleaned undoped Si(001) substrates (electrical resistivity:  $>1000 \Omega$  cm) were introduced into a molecular beam epitaxy (MBE) chamber at a base pressure of  $\sim 1 \times 10^{-8}$  Pa, equipped with Knudsen cells for Si and Ge. After degassing the substrates at 550 °C for ~5 h, 50 nm Si buffer layers were formed on the Si substrates at 500 °C to obtain the clean Si(001)–(2  $\times$  1) reconstructed surfaces. In the case of one-step growth [without the low-temperature (LT)-grown  $Si_{1-x}Ge_x$  buffer layer], the  $Si_{1-x}Ge_x$  films (x = 0.3, 0.5) were directly grown on the Si buffer layers at 400 °C. In the case of two-step growth (with the LT-grown  $Si_{1-x}Ge_x$ buffer layer), the  $Si_{1-x}Ge_x$  buffer layers with a thickness of 50 nm were grown on the Si buffer layers at a LT of 100 °C. These LT-grown Si<sub>1-x</sub>Ge<sub>x</sub> buffer layers work as strain-relaxation layers between the  $Si_{1-x}Ge_x$  films and the Si substrates. Subsequently, the  $Si_{1-x}Ge_x$  films (x = 0.3, 1) were grown at 400 °C on the LT-grown  $Si_{1-x}Ge_x$  buffer layers. The samples fabricated using the one-step and two-step growth methods are referred to as one-step and two-step Si<sub>1-x</sub>Ge<sub>x</sub> samples, respectively. The two-step  $Si_{1-x}Ge_x$  samples were expected to be strain-relaxed, while the one-step growth samples were considered to be strained. For n-type doping, ion implantation was carried out using a phosphorous ion beam with ion kinetic energies of 25–30 keV. The dose was in the range of  $1\times 10^{14}$ – $1\times 10^{15}$  cm $^{-2}$ . To electrically activate phosphorous atoms, the doped samples were annealed at 400 °C ( $x\!=\!1$ ) or 500 °C ( $x\!=\!0.3,\,0.5$ ) for 10 min at a N2 flow rate of  $\sim\!0.5$  l min $^{-1}$ . The thickness of the doped layer (DL) (carrier conducting layer) was defined as the ion implantation depth estimated from the stopping and range of ions in matter (SRIM) calculation. The fabricated samples are summarized in Table I, where the film thickness in the case of two-step samples does not include the thickness of the LT-grown Si $_{1-x}$ Ge $_x$  buffer layers.

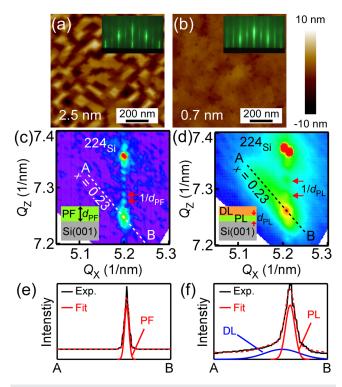
In situ reflection high-energy electron diffraction (RHEED) observations were performed with a 20 keV electron beam incident in the  $\langle 110\rangle_{\rm Si}$  direction. Atomic force microscopy (AFM) observations were performed with the Si cantilevers. Reciprocal space maps (RSMs) were obtained by ex situ x-ray diffraction (XRD) measurements with a Cu K $\alpha$  line (wavelength: 0.15418 nm). The carrier concentration  $n,\,\sigma,$  and S values were measured in the in-plane direction using Hall effect measurements, the van der Pauw method, and the Seebeck coefficient measurement system (ADVANCE-RIKO ZEM-3), respectively. The  $\kappa$  values in the cross-plane direction were measured at room temperature using the front-heating/front-detection time domain thermore-flectance (TDTR) method. Mo transducer films with thicknesses of  $\sim 150$  nm and  $\sim 100$  nm were deposited on the undoped and doped samples, respectively, for detecting the thermore-flectance signal.

The unileg-type TFTEG was fabricated using the Ge sample. Ge stripe structures with dimensions of  $4\,\mathrm{mm}\times450\,\mu\mathrm{m}\times100\,\mathrm{nm}$  were fabricated using photolithography and dry etching using CH<sub>4</sub>, O<sub>2</sub>, and He. The stripe interval was designed to be  $\sim\!150\,\mu\mathrm{m}$ . To connect the Ge stripe structures electrically in series, Al electrodes with a width of  $100\,\mu\mathrm{m}$  and a thickness of 150 nm were fabricated by photolithography and electron beam deposition. During the measurement of the TFTEG characteristics near room temperature,  $\Delta T$  between the hot and cold sides was created by Peltier devices and evaluated using an InSb detector (InfraScope MWIR temperature mapping microscope, Quantum Focus Instruments Co.).  $^{37}$ 

Figure 1 shows the AFM images and RHEED patterns (insets) of the one-step [Fig. 1(a)] and two-step Si<sub>0.7</sub>Ge<sub>0.3</sub> samples with a thickness of 150 nm [Fig. 1(b)], which are typical surface morphologies and diffraction patterns of the one-step and two-step samples. In all the samples, RHEED patterns with  $(2 \times 1)$  surface reconstruction were observed, indicating that all the Si<sub>1-x</sub>Ge<sub>x</sub> samples were epitaxially grown on Si(001) substrates. However, the surface morphologies of

**TABLE I.** Film thickness and doped layer (DL) thickness estimated from the SRIM calculation, Ge composition x, and in-plane strain  $\varepsilon_{ij}$  of the DLs measured from the RSMs of the doped samples.

Sample name	Target film thickness (nm)	Measured film thickness (nm)	DL thickness (nm)	Target x	Measured x	$\varepsilon_{//}$ of the DL (sample with n)
One-step Si <sub>0.7</sub> Ge <sub>0.3</sub> sample (100 nm)	100	92	78	0.3	0.23	$-0.67\% (\sim 4.1 \times 10^{19} \mathrm{cm}^{-3})$
One-step Si <sub>0.7</sub> Ge <sub>0.3</sub> sample (150 nm)	150	137	91	0.3	0.26	$-0.23\% (\sim 6.7 \times 10^{19} \mathrm{cm}^{-3})$
Two-step Si <sub>0.7</sub> Ge <sub>0.3</sub> sample	150	130	91	0.3	0.25	$-0.05\% (\sim 8.4 \times 10^{19} \mathrm{cm}^{-3})$
One-step Si <sub>0.5</sub> Ge <sub>0.5</sub> sample	100	100	78	0.5	0.38	$-0.02\% (\sim 8.9 \times 10^{19} \mathrm{cm}^{-3})$
Two-step Ge sample	100	79	71	1.0	1.00	$-0.01\% (\sim 2.8 \times 10^{19} \mathrm{cm}^{-3})$



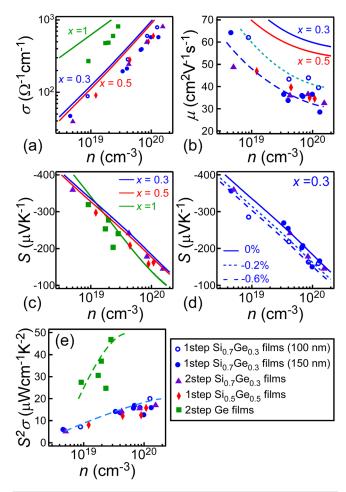
**FIG. 1.** AFM images of (a) one-step  $Si_{0.7}Ge_{0.3}$  sample (150 nm) and (b) two-step  $Si_{0.7}Ge_{0.3}$  sample. RMS values of surface morphologies are described. The insets show the corresponding RHEED patterns. (c) and (d) RSMs of the (c) as-grown and (d) doped one-step  $Si_{0.7}Ge_{0.3}$  samples (100 nm). (e) and (f) Profiles along the dashed lines from A to B in (c) and (d), respectively.

the one-step and two-step  $\mathrm{Si}_{1-x}\mathrm{Ge}_x$  samples seemed to be quite different. AFM observation indicated that the root mean square (RMS) values of the surface morphologies are 2.5 nm for the one-step  $\mathrm{Si}_{0.7}\mathrm{Ge}_{0.3}$  sample and 0.7 nm for the two-step  $\mathrm{Si}_{0.7}\mathrm{Ge}_{0.3}$  sample, which are consistent with the spotty and streaky diffraction spots in RHEED patterns, respectively. This result of surface roughness implies the film strain difference between the one-step and two-step samples.

Figures 1(c) and 1(d) show the RSMs of the as-grown (undoped) and doped ( $n \sim 4.1 \times 10^{19} \, \text{cm}^{-3}$ ) one-step  $\text{Si}_{0.7}\text{Ge}_{0.3}$  samples with a thickness of 100 nm as typical examples. Pendellösung fringes were observed between the 224<sub>Si</sub> spots from the Si substrate and the 224<sub>SiGe</sub> spots from the Si<sub>0.7</sub>Ge<sub>0.3</sub> film, indicating pseudomorphic growth. The profiles crossing the 224<sub>SiGe</sub> spot peaks were acquired along the dashed lines corresponding to an x value of 0.23 in Figs. 1(c) and 1(d), as shown in Figs. 1(e) and 1(f), respectively. In the as-grown sample, one sharp peak fitted with one Gaussian function was observed as shown in Fig. 1(e), and the Pendellösung fringe separation in Fig. 1(c) corresponds to the thickness of the pseudomorphic film (PF) of  $\sim$ 100 nm  $(d_{PF})$ . However, in the case of the doped samples, the  $224_{SiGe}$  diffraction peak was fitted with two Gaussian functions: one broad and one sharp [Fig. 1(f)]. The sharp peak position in Fig. 1(f) is similar to that of the PF in Fig. 1(e). From these results, it was found that the doped sample has the two layers [the doped layer (DL) and pseudomorphic layer (PL)] corresponding to two diffraction peaks because the DL was formed by ion implantation to the as-grown PF ( $\sim$ 100 nm), as shown in the schematic in the insets in Figs. 1(c) and 1(d). Actually, the fringe separation was changed by ion implantation and corresponds to a smaller thickness ( $\sim$ 38 nm) of the PL ( $d_{\rm PL}$ ) [Fig. 1(d)]. This revealed that the DL with some strain relaxation formed by ion implantation has a thickness of  $\sim$ 62 nm, which is approximately equal to the ion implantation depth. Using the broad fitted Gaussian curve of the DL [Fig. 1(f)], its x and in-plane strain ( $\varepsilon_{l/l}$ ) were estimated to be 0.23 and  $\sim$ -0.67%, respectively. The analysis results and the RSMs of the other samples are summarized in Table I and in Figs. S1 and S2 in the supplementary material, respectively. It was found that the thicker one-step samples (150 nm thickness) are relatively strain-relaxed and the two-step samples and higher x samples were almost completely strain-relaxed. These results demonstrated that strained and unstrained Si<sub>1-x</sub>Ge<sub>x</sub> films were well grown on Si substrates epitaxially.

We measured the TE properties of the  $Si_{1-x}Ge_x$  samples with various x and  $\varepsilon_{//}$  values at room temperature. Figure 2(a) shows the values of  $\sigma$  as a function of n. Each sample exhibited a lower  $\sigma$  value than each bulk [the semi-empirical curves of bulk materials (see the supplementary material, Sec. II) indicated by the solid curves in Figs. 2(a) and 2(b)]. 38,39 The degradation of  $\sigma$  from the bulk values could be attributed to large amounts of defects originating from growth temperatures lower than bulk growth cases (~800–1200 °C). As shown in Fig. 2(a), Ge samples exhibited higher  $\sigma$  values than those of the Si<sub>0.7</sub>Ge<sub>0.3</sub> and Si<sub>0.5</sub>Ge<sub>0.5</sub> samples. This is because the conductivity effective mass  $(m_c)$  of Ge (0.12  $m_0$ , where  $m_0$  is the free electron mass) is smaller than those of  $Si_{0.7}Ge_{0.3}$  and  $Si_{0.5}Ge_{0.5}$  (0.26 $m_0$ ), <sup>40</sup> in addition to the alloy scattering of electrons in Si<sub>0.7</sub>Ge<sub>0.3</sub> and Si<sub>0.5</sub>Ge<sub>0.5</sub> samples. The  $\mu$  values of  $Si_{1-x}Ge_x$  samples  $(x \neq 1)$  are plotted as a function of n, as shown in Fig. 2(b). The strained one-step Si<sub>0.7</sub>Ge<sub>0.3</sub> samples exhibited higher  $\mu$  values than the other samples with less strain. In general, the strain causes degeneracy lifting from sixfold to two- and fourfold degeneracies in the Si-Ge system. 40,41 In the case of compressively strained  $Si_{1-x}Ge_x$  (x < 0.85), carriers predominantly transport in the fourfold degenerate bands with smaller  $m_c$ , positioning at lower energy levels. This small  $m_c$  explains the higher  $\mu$  values of the onestep Si<sub>0.7</sub>Ge<sub>0.3</sub> samples (100 nm). These results highlight that in terms of  $\sigma$ , the Ge samples and the compressively strained Si<sub>1-x</sub>Ge<sub>x</sub> samples with small x are better owing to the smaller  $m_c$ .

To investigate the compositional effect on the S values, the n–Sdata of the less-strained samples with various x values (0.3, 0.5, and 1) are plotted, namely, unstrained two-step  $Si_{1-x}Ge_x$  (x = 0.3, 1) and one-step  $Si_{1-x}Ge_x$  (x = 0.5) samples. In the region  $n > 1 \times 10^{19}$  cm<sup>-3</sup>, there was a significant difference in the S values: Si<sub>0.7</sub>Ge<sub>0.3</sub> (triangles)  $> Si_{0.5}Ge_{0.5}$  (rhombi) > Ge (squares). The theoretical S curves were computed based on the Boltzmann transport equation under the relax-18,42 where two carrier ation time and effective mass approximation, <sup>16</sup> scattering processes were assumed: ionized impurity scattering (for all samples) and alloy scattering (not for Ge samples). The relaxation time ( $\tau$ ) of the ionized impurity scattering process<sup>43</sup> is described as  $16\pi\alpha^2(2m_d)^{1/2}E^{3/2}/q^4n$ , where q is the elementary charge, E is the carrier energy,  $\alpha$  is the permittivity, and  $m_d$  is the density-of-state (DOS) effective mass.  $\tau$  for the alloy scattering process<sup>44</sup> is described as 64/  $[3\pi^2x(1-x)a^3U_0D(E)]$ , where a is the lattice constant,  $U_0$  is the alloy scattering potential (0.8 eV for Si-Ge alloy), D(E) is the DOS of the conduction band, and  $\hbar$  is the Dirac constant. D(E) is given by a simple parabolic band model as follows:  $D(E) = (2m_d^3)^{1/2} E^{1/2} / \pi^2 \hbar^3$ . Considering the influence of the band structure on the S value, we



**FIG. 2.** (a)–(e) *n* dependences of (a) *σ*, (b) *μ*, (c) and (d) *S*, and (e)  $S^2\sigma$  at room temperature. The solid curves in (a) and (b) are the semi-empirical curves of  $Si_{1-x}Ge_x$  (x=1,0.3, and 0.5). The dotted and dashed curves in (b) are given as guides to the eye of the strained one-step  $Si_{0.7}Ge_{0.3}$  samples (100 nm) (dotted) and the other less-strained  $Si_{0.7}Ge_{0.3}$  samples (dashed). The solid lines in (c) are the theoretical *S* curves of almost-unstrained  $Si_{1-x}Ge_x$  films with various *x* values (0.3, 0.5, and 1). The solid, dotted, and dashed curves in (d) are the theoretical *S* curves of  $Si_{0.7}Ge_{0.3}$  films with various strains ( $ε_{ij}=0\%$ , -0.2%, and -0.6%, respectively). The theoretical *S* curves were computed based on the Boltzmann transport equation under relaxation time and effective mass approximation. The dashed curves in (e) are given as guides to the eye.

used the following effective density of state ( $N_{\rm C}$ ):  $1.04 \times 10^{19}$  cm<sup>-3</sup> for Ge and  $2.80 \times 10^{19}$  cm<sup>-3</sup> for unstrained Si<sub>0.7</sub>Ge<sub>0.3</sub> and Si<sub>0.5</sub>Ge<sub>0.5</sub>, which are determined by the degree of degeneracy and  $m_{\rm d}$ . The theoretical S curves reproduce the experimental n–S plots well, as shown in Fig. 2(c), although a little smaller S in the case of Si<sub>0.5</sub>Ge<sub>0.5</sub> was observed presumably due to the crystal defects caused by the strain relaxation in the one-step growth. From this computation, the fact that unstrained Si<sub>1-x</sub>Ge $_x$  exhibited higher S values than Ge samples is accounted for by the larger  $N_{\rm C}$  of unstrained Si<sub>1-x</sub>Ge $_x$  than Ge samples.

We investigated the effect of  $\varepsilon_{ll}$  on the S values of the Si<sub>0.7</sub>Ge<sub>0.3</sub> samples, as shown in Fig. 2(d). The almost-unstrained two-step

samples with an  $\varepsilon_{//}$  value of -0.05% (triangles) and less-strained thick samples with an  $\varepsilon_{//}$  value of -0.23% (solid circles) exhibited S values higher than more-strained one-step thin samples with an  $\varepsilon_{ll}$  value of -0.67% (open circles) in Fig. 2(d). To explain this strain-induced S reduction, the theoretical S curves were calculated in a manner similar to those in Fig. 2(c), using individual  $N_{\rm C}$  values:  $2.80 \times 10^{19} \, {\rm cm}^{-3}$  (unstrained),  $2.20 \times 10^{19} \, {\rm cm}^{-3}$  ( $\varepsilon_{//} \sim -0.2\%$ ), and  $1.95 \times 10^{19} \, {\rm cm}^{-3}$  $(\varepsilon_{ll} \sim -0.6\%)$ . In these calculations,  $\varepsilon_{ll} = -0.2\%$  causes S reduction (dotted curve), which cannot explain the experimental results of the samples with  $\varepsilon_{II} = -0.23\%$  (solid circles). It is difficult to predict the effect of strain on TE properties using such a simple calculation. However, the strain-induced S reduction can be easily understood qualitatively because strain lifts the degeneracy of the electronic state near the conduction band minimum resulting in the S decrease. This result demonstrates that we experimentally clarified the actual strain effect in epitaxial films. These results highlight that in terms of S, Si<sub>1-x</sub>Ge<sub>x</sub> samples with smaller x and  $\varepsilon$  values are better due to their larger effective DOS.

Figure 2(e) summarizes the  $S^2\sigma$  values of the samples. The Ge samples exhibited the highest  $S^2\sigma$  values  $(47 \pm 9 \ \mu \text{W cm}^{-1} \ \text{K}^{-2})$ among the  $Si_{1-x}Ge_x$  samples. The  $S^2\sigma$  value of the present Ge epitaxial film is approximately 1.2 times higher than the reported maximum  $S^2\sigma$  values of n-type SiGe-alloy-based bulks<sup>34</sup> and polycrystalline SiGe films. This higher  $S^2\sigma$  value of the Ge samples is due to the higher value of  $\sigma$  arising from the smaller  $m_c$ . On the other hand, for  $Si_{1-x}Ge_x$  samples except for the case of x=1 (i.e., Ge samples), all the samples exhibited almost the same  $S^2\sigma$  values, regardless of x and  $\varepsilon_{I/I}$ values. These similar values result from mixed contributions of some factors, such as the strain (high  $\sigma$  and low S) and crystal defects caused by strain relaxation (low  $\sigma$  and S). Furthermore,  $\sigma$ , S, and  $S^2\sigma$  of the two-step Ge sample with maximum  $S^2\sigma$  ( $n \sim 2.8 \times 10^{19} \, \mathrm{cm}^{-3}$ ) in Fig. 2(e) were measured as a function of T in the range between 300 and 400 K (see Fig. S3 in the supplementary material). The decrease in  $\sigma$  with the T increase indicates the degenerate semiconductor, and S increases in a typical trend. As a result, the maximum  $S^2\sigma$  value was  $\sim$ 57  $\pm$  11  $\mu$ W cm<sup>-1</sup> K<sup>-2</sup> at 400 K. This demonstrates the prospect of the Ge thin films for TEG use in this temperature range near room temperature.

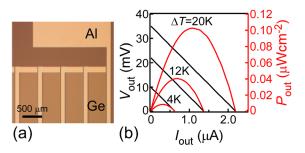
The cross-plane  $\kappa$  values were measured using TDTR (see the supplementary material, Sec. IV). 45 The undoped one-step Si<sub>0.7</sub>Ge<sub>0.3</sub> sample (100 nm) exhibited a lower  $\kappa$  value of  $\sim$ 1.8  $\pm$  0.7 W m<sup>-1</sup> K<sup>-1</sup> to the phonon alloy scattering than the undoped two-step Ge sample  $(25 \pm 8 \,\mathrm{W m^{-1} K^{-1}})$ . Next, to investigate the doping effect, the  $\kappa$  values of the doped one-step Si<sub>0.7</sub>Ge<sub>0.3</sub> sample (100 nm) and the doped two-step Ge sample were measured to be  ${\sim}1.9\pm0.8$  and  $\sim$ 6.5  $\pm$  1.1 W m<sup>-1</sup> K<sup>-1</sup>, respectively. The lattice thermal conductivities ( $\kappa_{lat}$ ) were found to be  $\sim$ 1.6 W m<sup>-1</sup> K<sup>-1</sup> in the doped one-step Si<sub>0.7</sub>Ge<sub>0.3</sub> sample (100 nm) and  $\sim$ 6.1 W m<sup>-1</sup> K<sup>-1</sup> in the doped twostep Ge sample by subtracting the electronic thermal conductivity estimated from the Wiedemann- Franz law with the Lorenz number of 2.44  $\times$  10<sup>-8</sup>  $\Omega$  W K<sup>-2</sup>. The similarity between  $\kappa$  and  $\kappa_{lat}$  indicates that the main heat carrier is a phonon. In the case of the Ge films, the P doping reduced  $\kappa_{lat}$  more largely ( $\sim$ 6.1 W m<sup>-1</sup> K<sup>-1</sup>) than the Ga doping  $(\sim 15 \text{ W m}^{-1} \text{ K}^{-1})^{33}$  even though the dopant concentration is almost the same  $(1-2 \times 10^{20} \, \text{cm}^{-3})$ . This supposedly originates from the large mass difference between Ge and P compared to that between Ge and Ga. On the other hand, in the case of the  $Si_{0.7}Ge_{0.3}$  films, almost no reduction of  $\kappa_{lat}$  was observed upon

doping. This is presumably because phonons are already scattered by the atomic mass difference (alloy scattering) thoroughly before introducing dopants as the atomic mass difference for phonon scattering. In addition, we roughly estimated the in-plane  $\kappa$  values to calculate the values of ZT. It is reported that the in-plane and crossplane  $\kappa$  values were measured to be two times different in the polycrystalline  $\mathrm{Si_{0.7}Ge_{0.3}}$  film, where the composing grains have a lateral size of  $\sim$ 270 nm and a vertical thickness of  $\sim$ 1.2  $\mu$ m. <sup>14</sup> Considering the relationship between the domain shape anisotropy and the  $\kappa$  anisotropy (two times different values), the in-plane  $\kappa$  values of the present SiGe and Ge epitaxial films were assumed to be approximately two times larger than the cross-plane  $\kappa$ . Thus, using the estimated in-plane  $\kappa$  and maximum  $S^2\sigma$  in Fig. 2(e), the in-plane ZT values of the SiGe and Ge epitaxial films were expected to be  $\sim$ 0.15 and  $\sim$ 0.11 at room temperature, respectively.

Here, it should be noted that the  $\kappa$  values of the epitaxial  $\mathrm{Si}_{1-x}\mathrm{Ge}_x$  films ( $\sim$ 1.8  $\pm$  0.7 W m<sup>-1</sup> K<sup>-1</sup> for undoped films) are much lower than those of bulk  $\mathrm{Si}_{0.7}\mathrm{Ge}_{0.3}$  ( $\sim$ 8 W m<sup>-1</sup> K<sup>-1</sup>)<sup>28</sup> and bulk Ge ( $\sim$ 60 W m<sup>-1</sup> K<sup>-1</sup>)<sup>27</sup> and close to the value of amorphous Si ( $\sim$ 1-2 W m<sup>-1</sup> K<sup>-1</sup>)<sup>46-48</sup> although this sample is simply an epitaxial film without nanostructures. Thus, in terms of the  $\kappa$  value,  $\mathrm{Si}_{1-x}\mathrm{Ge}_x$  films are more advantageous than Ge films. Therefore, in the situations where  $\Delta T$  is variable, such as TFTEG for the internet of human,  $\mathrm{Si}_{1-x}\mathrm{Ge}_x$  samples with ultralow  $\kappa$  are better for TFTEG use, while high power factor Ge films can also be advantageous for TFTEG applications by applying the nanostructuring technique.

We fabricated a preliminary TFTEG using a two-step Ge sample and investigated its operation under a constant  $\Delta T$  situation. As shown in the photograph of the Ge film TEG [Fig. 3(a)], the TFTEG composed of 10 stripe structures was well fabricated. Figure 3(b) shows the output voltage ( $V_{\rm out}$ ) and output power ( $P_{\rm out}$ ) as a function of the output current ( $I_{\rm out}$ ) under  $\Delta T$  values of 4, 12, and 20 K, indicating that  $P_{\rm out}$  is roughly proportional to  $\Delta T^2$ . The preliminary TFTEG generated a maximum  $P_{\rm out}$  value of  $\sim$ 0.10  $\mu$ W cm $^{-2}$  when applying a  $\Delta T$  value of 20 K, demonstrating the operation of the Ge film TEG.

In conclusion, we presented the material design of  $\mathrm{Si}_{1-x}\mathrm{Ge}_x$  epitaxial films/Si for the TFTEG by investigating their TE properties. We fabricated  $\mathrm{Si}_{1-x}\mathrm{Ge}_x$  epitaxial films with various x values and strain using two types of growth methods. Ge films exhibited the highest  $S^2\sigma$  of  $\sim$ 47  $\mu\mathrm{W}$  cm $^{-1}$  K $^{-2}$  among  $\mathrm{Si}_{1-x}\mathrm{Ge}_x$  epitaxial films, which is higher than the reported maximum  $S^2\sigma$  values of n-type SiGe alloys.  $^{34}$   $\mathrm{Si}_{0.7}\mathrm{Ge}_{0.3}$  films exhibited the ultralow  $\kappa$  of  $\sim$ 2 W m $^{-1}$  K $^{-1}$  even without nanostructures. It was found that  $\mathrm{Si}_{1-x}\mathrm{Ge}_x$  epitaxial films with



**FIG. 3.** (a) Photograph of the TFTEG fabricated using a Ge epitaxial film. (b)  $V_{\rm out}$  and  $P_{\rm out}$  of the TFTEG as a function of  $I_{\rm out}$  under  $\Delta T = 4$ , 12, and 20 K.

ultralow  $\kappa$  are suitable for TFTEGs under changeable  $\Delta T$  situations, while Ge epitaxial films with higher  $S^2\sigma$  are also suitable after nanostructuring. We tested the operation of a preliminary TFTEG using a Ge film with a higher  $S^2\sigma$  and demonstrated that  $\mathrm{Si}_{1-x}\mathrm{Ge}_x$  epitaxial TE films/Si can be promising TE materials suitable for industrial applications.

See the supplementary material for reciprocal lattice space mapping of  $\mathrm{Si}_{1-x}\mathrm{Ge}_x$  films, semi-empirical electrical conductivity and mobility of bulk  $\mathrm{Si}_{1-x}\mathrm{Ge}_x$  and Ge, temperature dependence of thermoelectric properties, and TDTR measurements and analysis.

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## **DATA AVAILABILITY**

The data that support the findings of this study are available within the article and its supplementary material.

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