

UNISONIC TECHNOLOGIES CO., LTD

LM358

LINEAR INTEGRATED CIRCUIT

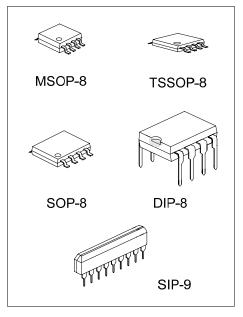
DUAL OPERATIONAL AMPLIFIER

■ DESCRIPTION

The UTC LM358 consists of two independent high gain, internally frequency compensated operational amplifier. It can be operated from a single power supply and also split power supplies.

■ FFATURES

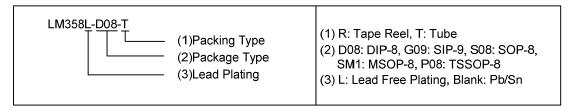
- *Internally frequency compensated for unity gain.
- *Wide power supply range 3V 32V.
- *Input common-mode voltage range include ground.
- *Large DC voltage gain.



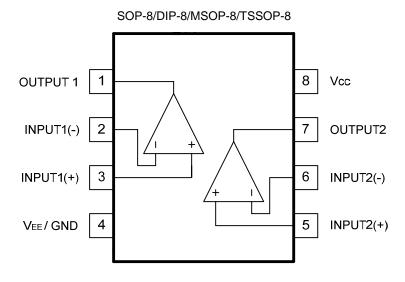
*Pb-free plating product number: LM358L

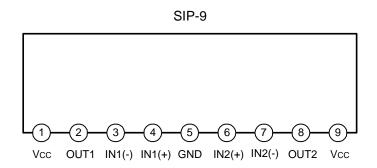
ORDERING INFORMATION

| Ordering Number | | Dookogo | Dooking | |
|-----------------|-------------------|---------|-----------|--|
| Normal | Lead Free Plating | Package | Packing | |
| LM358-D08-T | LM358L-D08-T | DIP-8 | Tube | |
| LM358-G09-T | LM358L-G09-T | SIP-9 | Tube | |
| LM358-P08-R | LM358L-P08-R | TSSOP-8 | Tape Reel | |
| LM358-P08-T | LM358L-P08-T | TSSOP-8 | Tube | |
| LM358-S08-R | LM358L-S08-R | SOP-8 | Tape Reel | |
| LM358-S08-T | LM358L-S08-T | SOP-8 | Tube | |
| LM358-SM1-R | LM358L-SM1-R | MSOP-8 | Tape Reel | |
| LM358-SM1-T | LM358L-SM1-T | MSOP-8 | Tube | |

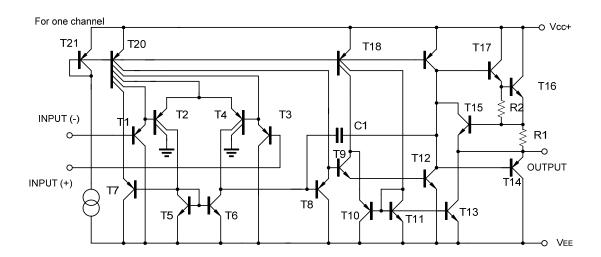


■ PIN DESCRIPTION





■ BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS

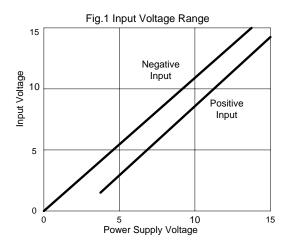
| PARAMETER | | SYMBOL RATINGS | | UNIT | |
|----------------------------|----------------|------------------|--------------------|------|--|
| Supply Voltage | | Vcc | ±16 or 32 | V | |
| Differential Input Voltage | | $V_{I(DIFF)}$ | ±32 | V | |
| Input Voltage | | VI | -0.3 ~ +32 | V | |
| Output Short to Ground | | | Continuous | | |
| | SIP-9 | | 600 | | |
| Dower Dissipation | DIP-8 | P _D | 500 | mW | |
| Power Dissipation | SOP-8 | | 280 | | |
| | TSSOP-8/MSOP-8 | | 200 | | |
| Junction Temperature | | TJ | +125 | °C | |
| Operating Temperature | | T _{OPR} | 0 ~ +70 | °C | |
| Storage Temperature | | T _{STG} | -65 ~ + 150 | °C | |

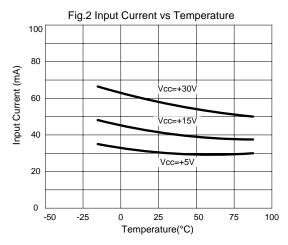
Note Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

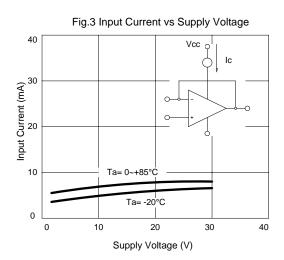
■ ELECTRICAL CHARACTERISTICS (V_{CC}=5.0V, V_{EE}=GND, Ta=25°C, unless otherwise specified)

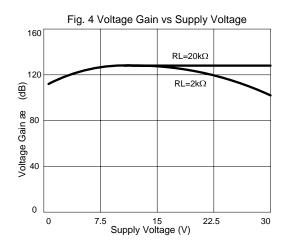
| PARAMETER | SYMBOL | TEST CONDITIONS | MIN | TYP | MAX | UNIT |
|---------------------------------|----------------------|--|-----|-----|----------------------|------|
| Input Offset Voltage | V _{I(OFF)} | $V_{CM}=0V$ to $V_{CC}-1.5V$ $V_{O(P)}=1.4V$, $R_S=0\Omega$ | | 2.9 | 7.0 | mV |
| Input Common Mode Voltage | V _{I(CM)} | V _{CC} =30V | 0 | | V _{CC} -1.5 | V |
| Differential Input Voltage | V _{I(DIFF)} | | | | V_{CC} | V |
| Output Voltage Swing | V_{OH} | V_{CC} =30V, R_L =2K Ω | 26 | | | V |
| | | V_{CC} =30V, R_L =10K Ω | 27 | 28 | | V |
| | V _{OL} | V_{CC} =5 V , $R_L \ge 10K\Omega$ | | 5 | 20 | mV |
| Large Signal Voltage Gain | G∨ | V_{CC} =15V, $R_L \ge 2K\Omega$ $V_{O(P)}$ =1V ~ 11V | 25 | 100 | | V/mV |
| Power Supply Current | I _{CC} | R _L =∞, V _{CC} =30V | | 0.8 | 2.0 | mΑ |
| | | R _L =∞, Full Temperature Range | | 0.5 | 1.2 | mΑ |
| Input Offset Current | I _{I(OFF)} | | | 5 | 50 | nA |
| Input Bias Current | I _{I(BIAS)} | | | 45 | 250 | nA |
| Short Circuit Current to Ground | I _{SC} | | | 40 | 60 | mΑ |
| Output Current | I _{SOURCE} | V _I (+)=1V, V _I (-)=0V V _{CC} =15V, V _{O(P)} =2V | 10 | 30 | | mA |
| | I _{SINK} | V _I (+)=0V, V _I (-)=1V V _{CC} =15V, V _{O(P)} =2V | 10 | 15 | | mA |
| | | V _I (+)=0V, V _I (-)=1V V _{CC} =15V, V _{O(P)} =200mV | 12 | 100 | | mA |
| Common Mode Rejection Ratio | CMRR | | 65 | 80 | | dB |
| Power Supply Rejection Ratio | PSRR | | 65 | 100 | | dB |
| Channel Separation | CS | f=1KHZ ~ 20KHZ | | 120 | | dB |

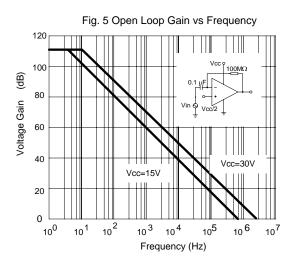
■ TYPICAL CHARACTERISTICS

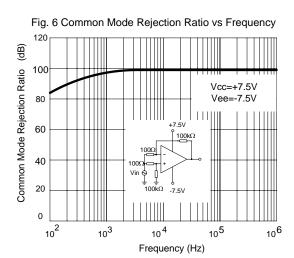












■ TYPICAL CHARACTERISTICS(Cont.)

Fig. 7 Voltage Follower Pulse Response

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Fig. 8 Voltage Follower Response (Small Signal)

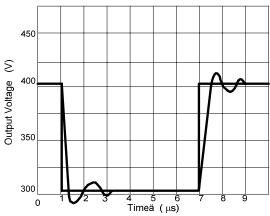


Fig. 9 Gain vs Large Signal Frequency

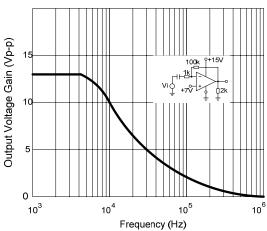


Fig. 10 Output Current Sinking vs Output Voltage

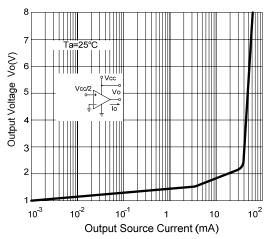


Fig. 11 Output Sink Current vs Output Voltage

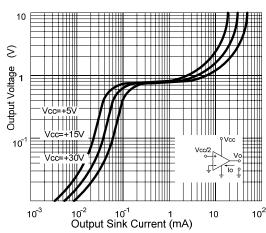
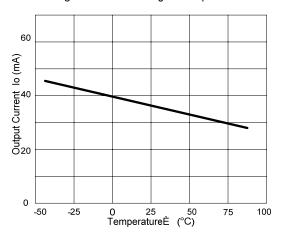


Fig.12 Current Limiting vs Temperature



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