

# K M Indhumukhi

[indhukm333@gmail.com](mailto:indhukm333@gmail.com) | +91 7899060883

Portfolio - <https://indhumukhi-km.github.io/I-Portfolio/>

## OBJECTIVE/SUMMARY

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Graduate with hands-on experience gained from a comprehensive internship, eager to leverage technical proficiency and problem-solving abilities in a reputable IT industry. Passionate about applying technical skills, problem-solving abilities, and creativity to diverse projects and roles. Eager to join a dynamic team and contribute effectively to organizational growth. Committed to continuous learning and delivering high-quality results.

## PROFESSIONAL EXPERIENCE

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### Hrlabs

Bangalore, Karnataka.

June 2024 – October 2024

- *Frontend developer Inter*
  - Leveraged HTML, CSS and React as primary tools to create compelling real-time static websites.
  - Demonstrated proficiency in React by developing and implementing reusable components, ensuring code consistency, maintainability, and scalability across the project.
  - Created a library of reusable UI components, such as buttons, dropdowns, and form elements, streamlining the development process and maintaining a cohesive design language.
  - Received positive feedback from senior developers for attention to detail and commitment to producing high-quality code.
  - Actively participated in team meetings and brainstorming sessions, contributing innovative ideas for improving user experience and interface design.

## EDUCATION

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### Annamacharya Institute of Technology and Sciences

Rajampet, India.

- Bachelor of Technology (B. Tech)
    - Electronic And Communication Engineering - CGPA: 7.87
- August 2019 – May 2023

### Andhra Pradesh Residential Junior College

Banavasi (Kurnool dist.), India

- Intermediate(12<sup>th</sup>)
    - CGPA: 9.3
- June 2017 - March 2019

### MJP AP BC Residential School

Tekulodu (Anantapuram dist.), India

- SSC (10<sup>TH</sup>)
    - CGPA: 9.0
- June 2016 - April 2017

## SKILLS

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- **Programming Languages:** Python, Java.
- **Web Technologies:** HTML5, CSS3, JavaScript, React JS.
- **Database:** MySQL.

## PROJETS

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### Reduction of power and delay in Universal Shift Register using Gate Diffusion Input Technique.

#### (Major-project)

- This project is mainly focused on reducing power in VLSI circuit along with delay Because circuit complexity and fast speed of emerging VLSI circuits results in high power consumption Therefore reducing power become a key parameter in designing VLSI circuits
- Designed low power Universal shift register using Gate Diffusion Input Technique
- Compared with CMOS Technology which we took as existing technology GDI
- Got power consumption ,delay and number of transistors used less than the existing method CMOS
- Tool used for this project is TANNER TOOL 16.0V

## HRlabspage

- Enhanced user experience and navigation on the hrlabs website by implementing React Router for seamless navigation between different sections and pages, ensuring full responsiveness across all devices.
- Collaborated closely with the design team as a Developer at HRlabs to create a user-friendly and visually appealing website using React.js, HTML, and CSS.
- Played a pivotal role in the development and successful launch of the [hrlabs.in](https://hrlabs.in) website, significantly enhancing the company's online presence and brand awareness in the HR consulting industry.

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## ACHIEVEMENTS/AWARDS

- Served as National Green Corps(NGC) member for the academic year 2015-16.
- Served as School Pupil leader(SPL) for the academic year 2016-17.
- Certified in the “Inter District Tennikoit Championship” [2018].
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## SOFT SKILLS

- Communication
- Teamwork
- Interpersonal Skills
- Adaptability