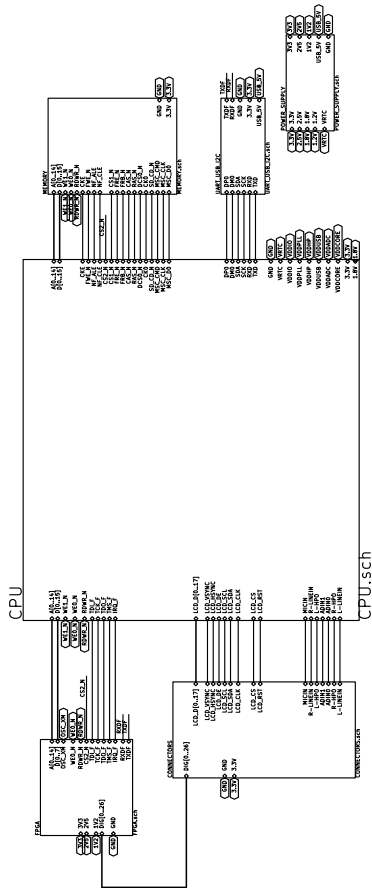


A

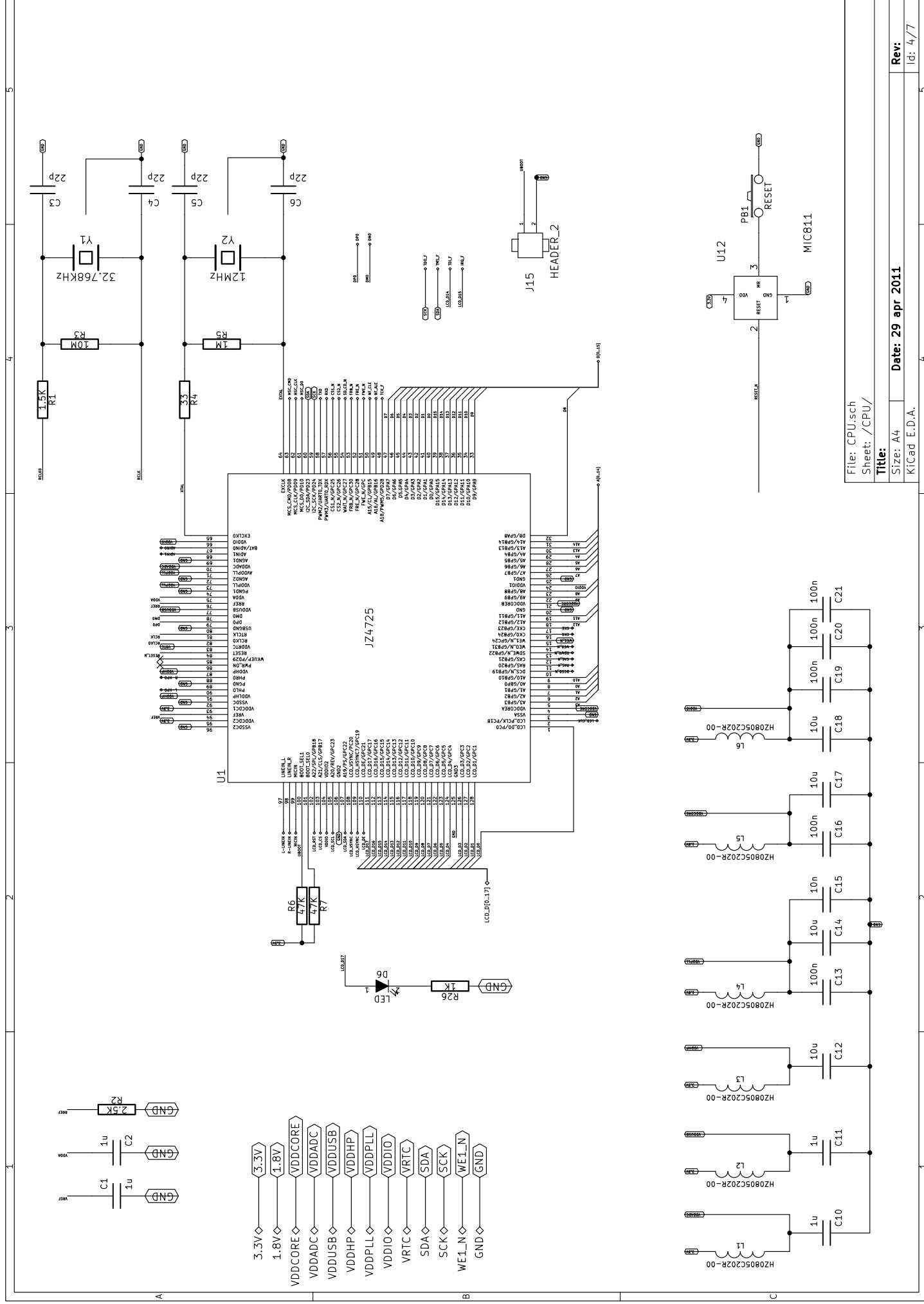
B

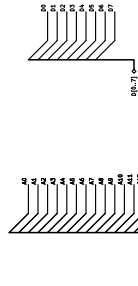
C



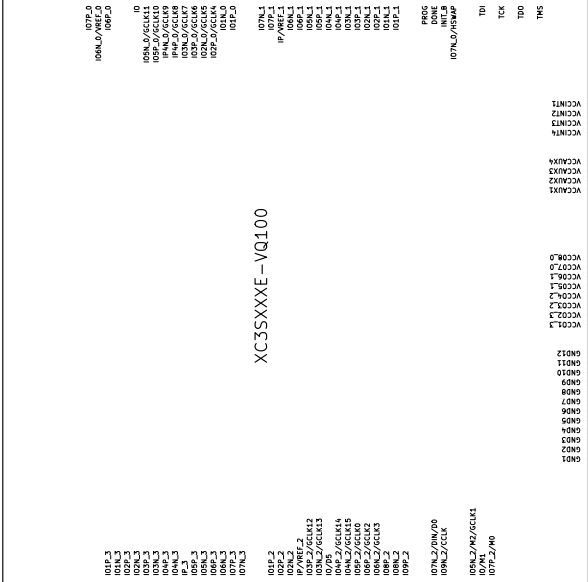
File: SAKC.sch
Sheet: /

Title:		
Size: A4	Date: 29 apr 2011	Rev:
KiCad E.D.A.		Id: 1/7

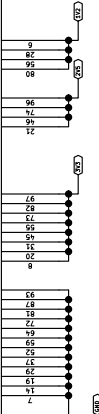




U9



U9

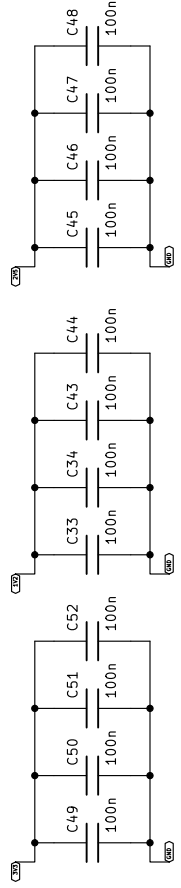
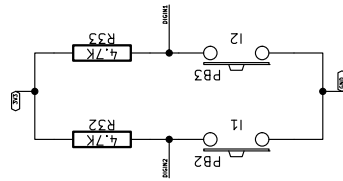
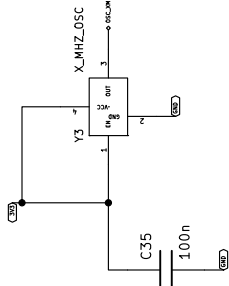
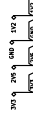


U9

U9

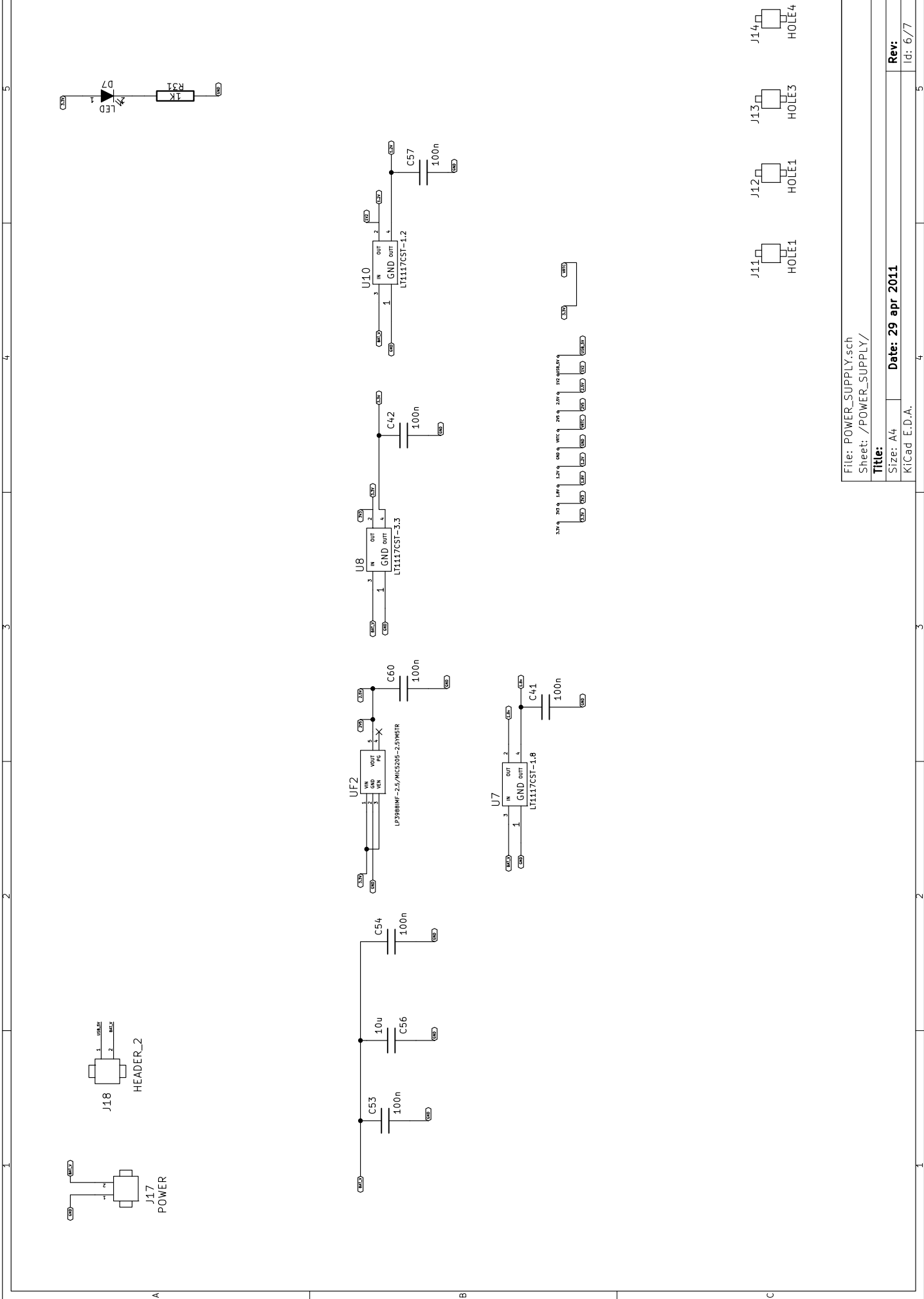
U9

U9



File: FPGA.sch
Sheet: /FPGA/

Title: noname.sch
Size: A4
Date: 29 apr 2011
Rev: Id: 3/7



File: POWER_SUPPLY.sch
Sheet: /POWER_SUPPLY/

Title:		
Size: A4	Date: 29 apr 2011	Rev:
KICad E.D.A.		Id: 6/7

