

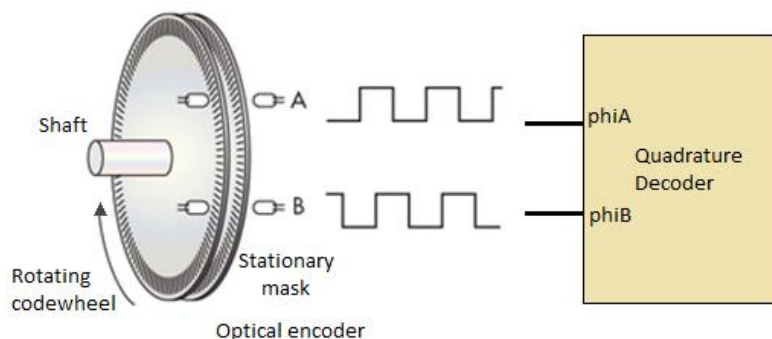
## Objective

This code example demonstrates how to use the TCPWM Component in quadrature decoder mode in PSoC® 6 MCU.

## Overview

This code example shows the TCPWM Component is configured in quadrature decoder mode, to detect the direction of rotation of a shaft. This example emulates an optical encoder (Figure 1) that detects shaft rotation. If the shaft rotates clockwise, **A** leads **B**; if it rotates anti-clockwise, **B** leads **A**. The result is shown using LEDs: for clockwise rotation, a red LED is ON; for anti-clockwise rotation, a green LED is ON. When rotation stops, both LEDs are turned off.

Figure 1. Project Overview



This code example assumes that you are familiar with the PSoC 6 MCU device and the PSoC Creator™ IDE. If you are new to PSoC 6 MCU, see the application note [AN210781 – Getting Started with PSoC 6 MCU with Bluetooth Low Energy \(BLE\) Connectivity](#).

## Requirements

**Tool:** PSoC Creator 4.2

**Programming Language:** C (ARM® GCC 5.4.1, ARM MDK 5.22)

**Associated Parts:** PSoC 6 MCU

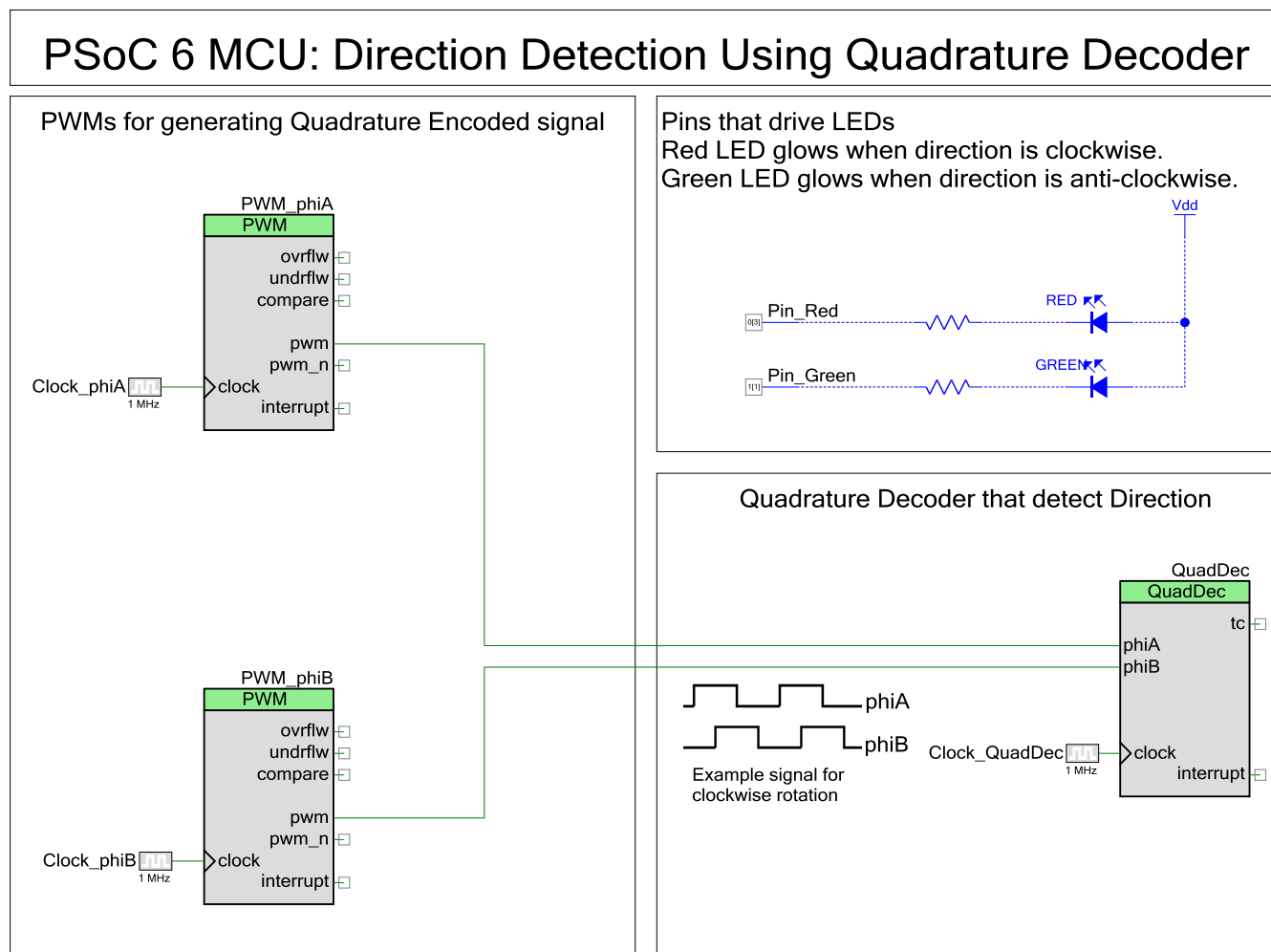
**Related Hardware:** [CY8CKIT-062-BLE PSoC 6 BLE Pioneer Kit](#)

## Design

To emulate the optical encoder, the design uses two PWM Components. Each PWM Component uses a TCPWM block in PWM mode. The output frequency of both PWMs is the same, but they are 90° out of phase. The PWM Components are configured to generate signals at 250 Hz.

To detect the direction of rotation, a Quadrature Decoder Component is used. This Component uses a TCPWM block configured as quadrature decoder. The counter of the quadrature decoder is initialized with a midpoint counter value on an index event. The counter value changes (increases/ decreases) based on the quadrature input signal. A positive edge on phiA increments the counter when phiB is 0 and decrements the counter when phiB is 1. Therefore, if phiA leads phiB, the count value of the Quadrature Decoder increases; if phiB leads phiA, the count value decreases. Based on the current and the previous counter values the direction of rotation is determined. Digital Output Pins drive the LEDs. If the rotation is clockwise, the red LED is ON; if it is counter-clockwise, the green LED is ON.

Figure 2. Project Schematic



## Components

Table 1 lists the PSoC Creator Components used in this example, as well as the hardware resources used by each.

Table 1. PSoC Creator Component

Components	Instance name	Settings (Non-Default)	Hardware Resources
PWM	PWM_phiA PWM_phiB	<b>[General tab]</b> Period:3999u Compare:2000u	2 TCPWM
Quadrature Decoder	QuadDec	<b>[General tab]</b> Quadrature mode: x1	1 TCPWM
Clock	Clock_phiA Clock_phiB Clock_Quaddec	Frequency: 1 MHz	3 peripheral clock dividers
Digital Output Pin	Pin_Red Pin_Green	Default settings	2 GPIO pins

## Design-Wide Resources

Table 2 shows the physical pins used.

Table 2. Pin Names and Locations

Pin Name	Location
Pin_Red	P0[3]
Pin_Green	P1[1]

## Operation

1. Plug the CY8CKIT-062 kit board into your computer's USB port.
2. In the *main\_cm0p.c* file, find the line "`#define ROTATION`" and select direction **CLOCKWISE**, **ANTI\_CLOCKWISE** or, **NO\_ROTATION**.
3. Build the project and program it into the PSoC 6 MCU device. Choose **Debug > Program**. For more information on device programming, see PSoC Creator Help. Flash for both CPUs is programmed in a single program operation.
4. On successful programming, the red LED glows when the direction is clockwise; the green LED glows when the direction is counter-clockwise. Both LEDs are turned off when no rotation is detected.

## Related Documents

Application Notes	
<a href="#">AN210781 – Getting Started with PSoC 6 MCU with Bluetooth Low Energy (BLE) Connectivity</a>	Introduction of PSoC 6 MCU with Bluetooth Low Energy (BLE)
PSoC Creator Component Datasheets	
<a href="#">TCPWM</a>	Supports configuration of the TCPWM hardware for Timer/Counter functionality
<a href="#">Clock</a>	Supports local clock generation
<a href="#">Pins</a>	Supports connection of hardware resources to physical pins
Device Documentation	
<a href="#">PSoC 6 MCU: PSoC 63 with BLE Datasheet</a>	<a href="#">PSoC 6 MCU: PSoC 63 with BLE Architecture Technical Reference Manual</a>
Development Kit (DVK) Documentation	
<a href="#">CY8CKIT-062-BLE Pioneer Kit</a>	

## Document History

Document Title: CE220799 – PSoC 6 MCU: Direction Detection Using Quadrature Decoder

Document Number: 002-20799

Revision	ECN	Orig. of Change	Submission Date	Description of Change
**	5896810	AJYA	09/26/2017	New code example

## Worldwide Sales and Design Support

Cypress maintains a worldwide network of offices, solution centers, manufacturer's representatives, and distributors. To find the office closest to you, visit us at [Cypress Locations](#).

## Products

ARM® Cortex® Microcontrollers	<a href="http://cypress.com/arm">cypress.com/arm</a>
Automotive	<a href="http://cypress.com/automotive">cypress.com/automotive</a>
Clocks & Buffers	<a href="http://cypress.com/clocks">cypress.com/clocks</a>
Interface	<a href="http://cypress.com/interface">cypress.com/interface</a>
Internet of Things	<a href="http://cypress.com/iot">cypress.com/iot</a>
Memory	<a href="http://cypress.com/memory">cypress.com/memory</a>
Microcontrollers	<a href="http://cypress.com/mcu">cypress.com/mcu</a>
PSoC	<a href="http://cypress.com/psoc">cypress.com/psoc</a>
Power Management ICs	<a href="http://cypress.com/pmic">cypress.com/pmic</a>
Touch Sensing	<a href="http://cypress.com/touch">cypress.com/touch</a>
USB Controllers	<a href="http://cypress.com/usb">cypress.com/usb</a>
Wireless Connectivity	<a href="http://cypress.com/wireless">cypress.com/wireless</a>

## PSoC® Solutions

[PSoC 1](#) | [PSoC 3](#) | [PSoC 4](#) | [PSoC 5LP](#) | [PSoC 6](#)

## Cypress Developer Community

[Forums](#) | [WICED IOT Forums](#) | [Projects](#) | [Videos](#) | [Blogs](#) | [Training](#) | [Components](#)

## Technical Support

[cypress.com/support](http://cypress.com/support)

All other trademarks or registered trademarks referenced herein are the property of their respective owners.



© Cypress Semiconductor Corporation, 2017. This document is the property of Cypress Semiconductor Corporation and its subsidiaries, including Spansion LLC ("Cypress"). This document, including any software or firmware included or referenced in this document ("Software"), is owned by Cypress under the intellectual property laws and treaties of the United States and other countries worldwide. Cypress reserves all rights under such laws and treaties and does not, except as specifically stated in this paragraph, grant any license under its patents, copyrights, trademarks, or other intellectual property rights. If the Software is not accompanied by a license agreement and you do not otherwise have a written agreement with Cypress governing the use of the Software, then Cypress hereby grants you a personal, non-exclusive, nontransferable license (without the right to sublicense) (1) under its copyright rights in the Software (a) for Software provided in source code form, to modify and reproduce the Software solely for use with Cypress hardware products, only internally within your organization, and (b) to distribute the Software in binary code form externally to end users (either directly or indirectly through resellers and distributors), solely for use on Cypress hardware product units, and (2) under those claims of Cypress's patents that are infringed by the Software (as provided by Cypress, unmodified) to make, use, distribute, and import the Software solely for use with Cypress hardware products. Any other use, reproduction, modification, translation, or compilation of the Software is prohibited.

TO THE EXTENT PERMITTED BY APPLICABLE LAW, CYPRESS MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARD TO THIS DOCUMENT OR ANY SOFTWARE OR ACCOMPANYING HARDWARE, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE. To the extent permitted by applicable law, Cypress reserves the right to make changes to this document without further notice. Cypress does not assume any liability arising out of the application or use of any product or circuit described in this document. Any information provided in this document, including any sample design information or programming code, is provided only for reference purposes. It is the responsibility of the user of this document to properly design, program, and test the functionality and safety of any application made of this information and any resulting product. Cypress products are not designed, intended, or authorized for use as critical components in systems designed or intended for the operation of weapons, weapons systems, nuclear installations, life-support devices or systems, other medical devices or systems (including resuscitation equipment and surgical implants), pollution control or hazardous substances management, or other uses where the failure of the device or system could cause personal injury, death, or property damage ("Unintended Uses"). A critical component is any component of a device or system whose failure to perform can be reasonably expected to cause the failure of the device or system, or to affect its safety or effectiveness. Cypress is not liable, in whole or in part, and you shall and hereby do release Cypress from any claim, damage, or other liability arising from or related to all Unintended Uses of Cypress products. You shall indemnify and hold Cypress harmless from and against all claims, costs, damages, and other liabilities, including claims for personal injury or death, arising from or related to any Unintended Uses of Cypress products.

Cypress, the Cypress logo, Spansion, the Spansion logo, and combinations thereof, WICED, PSoC, CapSense, EZ-USB, F-RAM, and Traveo are trademarks or registered trademarks of Cypress in the United States and other countries. For a more complete list of Cypress trademarks, visit [cypress.com](http://cypress.com). Other names and brands may be claimed as property of their respective owners.