

Objective

This example demonstrates how to use the printf function with a Serial Communication Block (SCB) based UART in PSoC® 6 MCU.

Overview

This example is designed to redirect the printf function to use the UART API. Project is designed to print number of times, the kit button SW2 is pressed, on the terminal.

Requirements

Tool: PSoC Creator™ 4.2; Peripheral Driver Library (PDL) 3.0.1

Programming Language: C (Arm® GCC 5.4.1 and Arm MDK 5.22)

Associated Parts: All PSoC 6 MCU parts

Related Hardware: CY8CKIT-062-BLE PSoC 6 BLE Pioneer Kit

Hardware Setup

This example uses the kit's default configuration. See the [kit guide](#) to ensure the kit is configured correctly. If the settings are different from the default values, see the 'Selection Switches' table in the [kit guide](#) to reset to the default settings.

[Table 2](#) lists the PSoC Creator pin connection settings required on the CY8CKIT-062-BLE Kit.

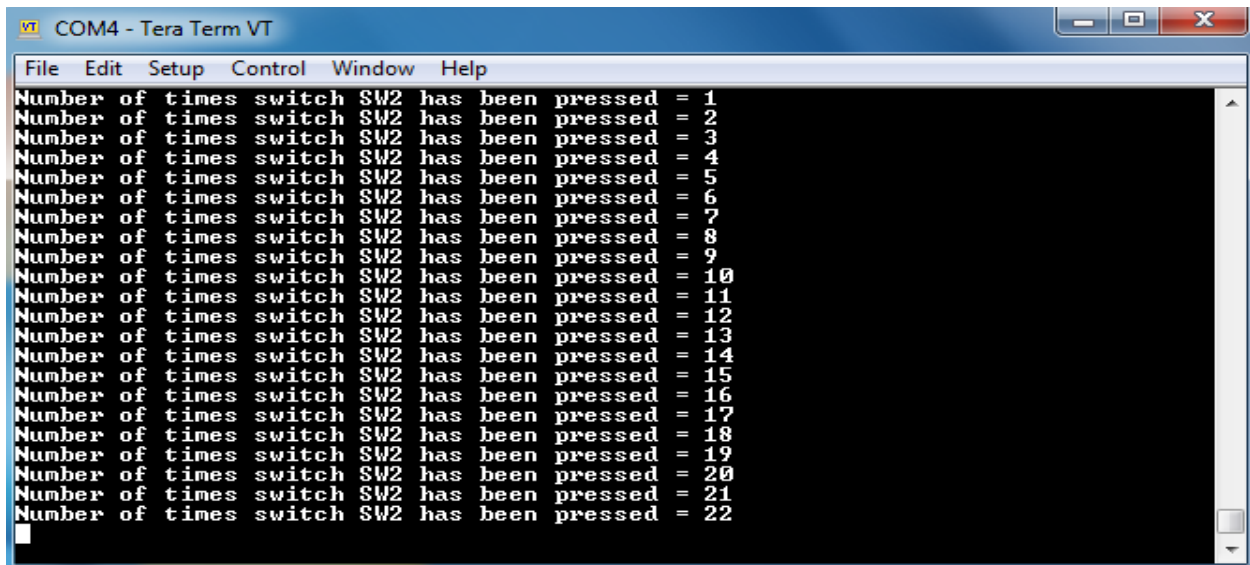
Software Setup

Set up a terminal emulator like Tera Term or PuTTY on your personal computer with baud rate and other settings shown in [Figure 2](#).

Operation

1. Plug the CY8CKIT-062-BLE kit board into your computer's USB port.
2. Build the UART_printf project and program it into the PSoC 6 MCU device. Choose **Debug > Program**. For more information on device programming, see PSoC Creator Help. Flash for both CPUs is programmed in a single program operation.
3. Open a serial port communication program such as Tera Term and select the corresponding COM port. Configure the terminal with baud rate of 115200, data bits of 8, stop bits of 1, and with parity and flow control set to none.
4. Press the SW2 switch on the kit. The terminal will print the number of times the switch SW2 is pressed as shown in [Figure 1](#).

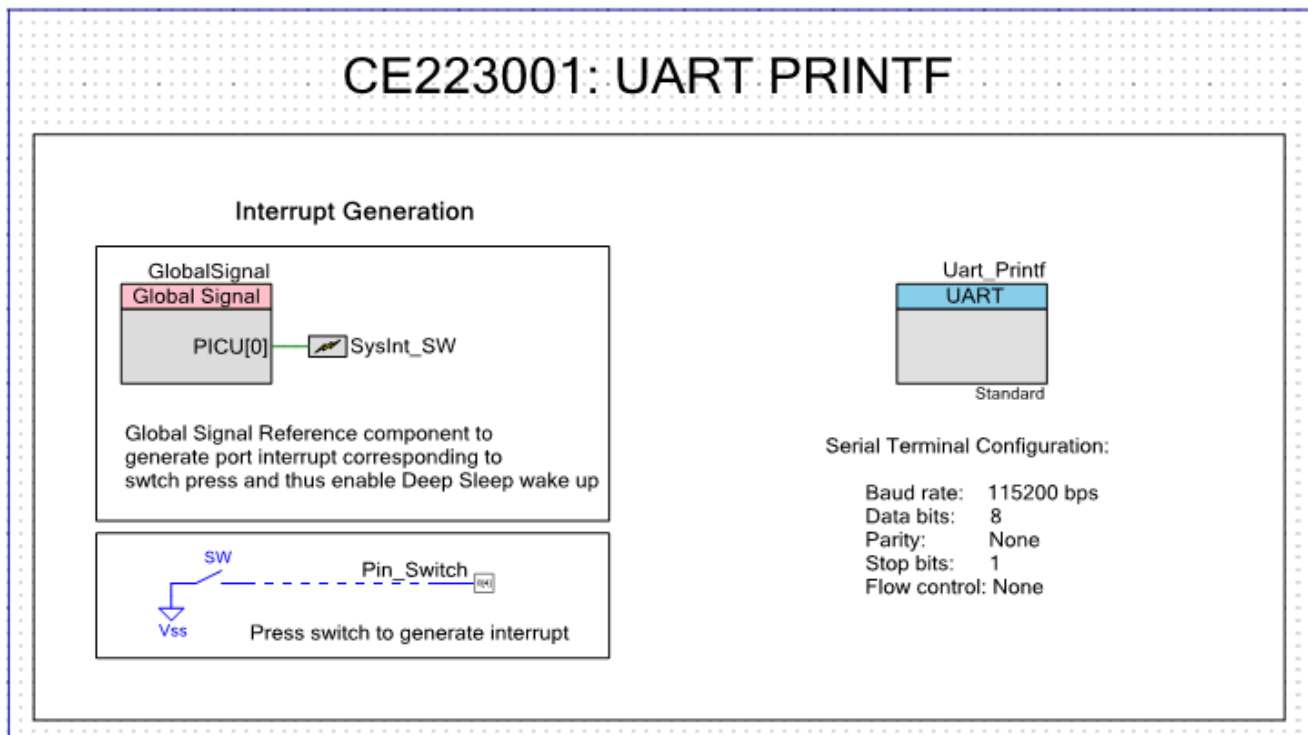
Figure 1. Terminal Prints



Design and Implementation

The design shown in [Figure 2](#) has a UART (Uart_Printf) Component, Global Signal Reference (GlobalSignal) Component, System Interrupt Component (SysInt_Sw), and a GPIO (pin_switch) Component. UART is configured in TX mode to transmit data at baud rate of 115200. GlobalSignal is configured to connect a GPIO (pin_switch) interrupt signal to SysInt_Sw. SysInt_Sw increments the count when pin_switch (SW2 on kit) is pressed. Number of times the kit button SW2 is pressed is printed on the terminal using printf function. Count is reset when its value reaches 65535.

Figure 2. UART printf Example Schematic



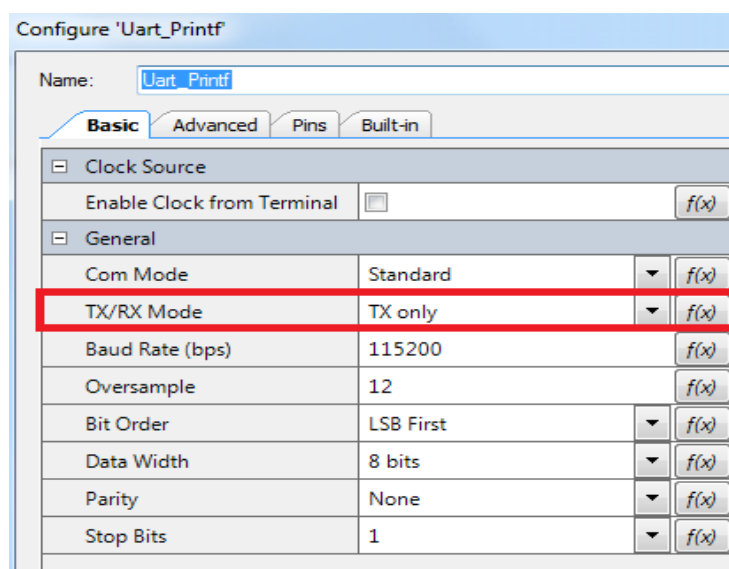
Components and Settings

Table 1 lists the PSoC Creator Components used in this example, how they are used in the design, and the hardware resources used by each Component. Non-default settings for each Component are listed in Table 1.

Table 1. PSoC Creator Components

| Component | Instance Name | Purpose | Non-default Parameter Settings |
|---------------------------------------|---------------|-------------------------------------|--|
| UART(SCB_UART_PDL) | Uart_Printf | To handle UART serial communication | See Figure 3. |
| Global Signal Reference (GSRef) | GlobalSignal | To generate Interrupt | Tab Basic- Global Signal Name: Port Interrupt 0 (PICU[0]). |
| System Interrupt (SysInt) | SysInt_SW | To process Interrupt | Tab Basic- Check Box: Deep Sleep Capable. |
| General Purpose Input / Output (GPIO) | Pin_Switch | To Connect to switch | See Figure 4. |

Figure 3. UART Parameter Settings



Configure 'Uart_Printf'

Name: Uart_Printf

Basic Advanced Pins Built-in

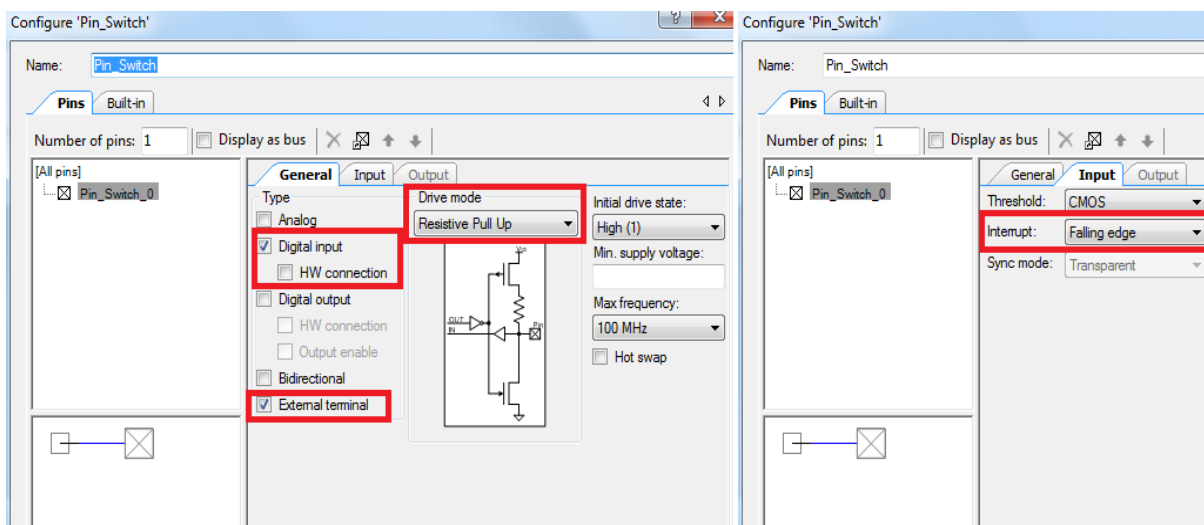
☐ Clock Source

Enable Clock from Terminal ☐ f(x)

☐ General

| | | |
|-------------------|----------------|------|
| Com Mode | Standard | f(x) |
| TX/RX Mode | TX only | f(x) |
| Baud Rate (bps) | 115200 | f(x) |
| Oversample | 12 | f(x) |
| Bit Order | LSB First | f(x) |
| Data Width | 8 bits | f(x) |
| Parity | None | f(x) |
| Stop Bits | 1 | f(x) |

Figure 4. Pin_Switch GPIO Parameter Settings



Design-Wide Resources

Table 2 shows the pin assignment for the code example.

Table 2. Pin Names and Location

| Pin Name | Location |
|----------------|----------|
| Uart_Printf:tx | P5[1] |
| Pin_Switch | P0[4] |

Reusing This Example

This example is designed for the CY8CKIT-062-BLE pioneer kit. To port the design to a different PSoC 6 MCU device, kit, or both, change the target device using the Device Selector and update the pin assignments in the Design Wide Resources Pins settings as needed. For single-core PSoC 6 MCU devices, port the code from *main_cm4.c* to *main.c*.

In some cases, a resource used by a code example (for example, an IP block) may not be supported on another device. In those cases, the example will not work. If you build the code targeted at such a device, you will get errors. See the device datasheet for information on supported devices.

Related Documents

| | |
|---|---|
| Application Notes | |
| AN210781 – Getting Started with PSoC 6 MCU with Bluetooth Low Energy (BLE) Connectivity | Describes PSoC 63 with Bluetooth Low Energy (BLE) Connectivity and how to build your first PSoC Creator project |
| PSoC Creator Component Datasheets | |
| Global Signal Reference | Connections to device global signals |
| System Interrupt | Interrupt vectoring and control |
| General Purpose Input / Output | Supports Analog, Digital I/O and Bidirectional signal types |
| UART | Supports UART communication |
| Device Documentation | |
| PSoC 6 MCU: PSoC 63 with BLE Datasheet | PSoC 6 MCU: PSoC 63 with BLE Architecture Technical Reference Manual |
| Development Kit (DVK) Documentation | |
| CY8CKIT-062-BLE PSoC 6 BLE Pioneer Kit | |

Document History

Document Title: CE223001 – PSoC 6 MCU UART printf

Document Number: 002-23001

| Revision | ECN | Orig. of Change | Submission Date | Description of Change |
|----------|---------|-----------------|-----------------|-----------------------|
| ** | 6104587 | VJYA | 03/23/2018 | New Code Example |

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198 Champion Court
San Jose, CA 95134-1709

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