

# OPTIGA™ Trust M2 ID2

**Product Version: V2**

## About this document

### Scope and purpose

The purpose of this document is to guide a beginner to demonstrate OPTIGA shell application software package with the OPTIGA™ Trust M2 ID2 ESP32-DevKitC V4. The scope is limited to OPTIGA™ Trust M2 ID2 ESP32-DevKitC V4 and its hardware and software components.

### Intended audience

This document addresses: customers, solution providers, porting guide and system integrators.



## Table of Contents

## Table of Contents

About this document.....	1
Table of Contents .....	2
<b>1 Introduction .....</b>	<b>3</b>
1.1 References .....	3
1.2 Abbreviations .....	3
<b>2 OPTIGA™ Trust M2 ID2.....</b>	<b>4</b>
2.1 OPTIGA™ Trust M2 ID2 with ESP32-DevKitC V4 .....	4
2.1.1 Evaluation Kit Components .....	4
2.2 Installed Software Components .....	4
<b>3 System Setup.....</b>	<b>6</b>
3.1 System Overview .....	6
3.2 Hardware Setup.....	7
3.2.1 ESP32-DevKitC V4.....	7
3.2.2 ESP32 DevKitC Adapter for Shield2Go .....	7
3.2.3 Shield2Go Security OPTIGA™ Trust M2 ID2 .....	8
3.3 Software Setup.....	9
3.3.1 Software Components .....	9
3.3.1.1 ESP32-DevKitC V4.....	9
3.3.2 PC Requirements and Configurations .....	9
3.3.2.1 PC Requirement .....	9
<b>4 Shell Application execution using OPTIGA™ Trust M2 ID2 .....</b>	<b>10</b>
4.1 Quick Setup .....	10
4.1.1 Configure and build for ESP32-DevKitC V4.....	11
4.1.1.1 Configuration .....	11
4.1.1.2 Build source code.....	12
4.1.2 Download example hex file to ESP32-DevKitC V4.....	13
4.1.3 Steps to execute optiga_shell_app example .....	14
4.1.4 Logger control for shell application .....	17
<b>5 Troubleshooting .....</b>	<b>19</b>
<b>Revision History .....</b>	<b>20</b>

## Introduction

# 1 Introduction

This document describes how to setup the environment to demonstrate OPTIGA shell application software package with the OPTIGA™ Trust M2 ID2 ESP32-DevKitC V4.

## 1.1 References

**Table 1**      **References**

Definition	Source
[1] ESP32-DevKitC V4_usermanual	<a href="#">espressif</a>
[2] Infineon_I2C_Protocol	<a href="#">Infineon</a>

## 1.2 Abbreviations

**Table 2**      **Abbreviations**

Abbreviation	Definition
API	Application Programming Interface
ESP32	ESP32-DevKitC V4
HW	Hardware
I2C	Inter Integrated Circuit
IoT	Internet of Things
OS	Operating System
PAL	Platform Abstraction Layer
RSA	Rivest-Shamir-Adleman
PC	Personal Computer
RST	Reset
SCL	Serial Clock
SDA	Serial Data
SW	Software
TTL	Transistor Transistor Logic
USB	Universal Serial Bus

## 2 OPTIGA™ Trust M2 ID2

OPTIGA™ Trust M2 ID2 is a security solution with a pre-programmed security controller with wide range of security features.

It supports secure data, key and metadata object update, hibernate and cryptographic toolbox functionalities, secure communication, platform integrity, data store protection and lifecycle management for connected device security.

### 2.1 OPTIGA™ Trust M2 ID2 with ESP32-DevKitC V4

OPTIGA™ Trust M2 ID2 with ESP32-DevKitC V4 is designed to provide all the components required to setup the environment to demonstrate the features of the OPTIGA™ Trust M2 ID2.

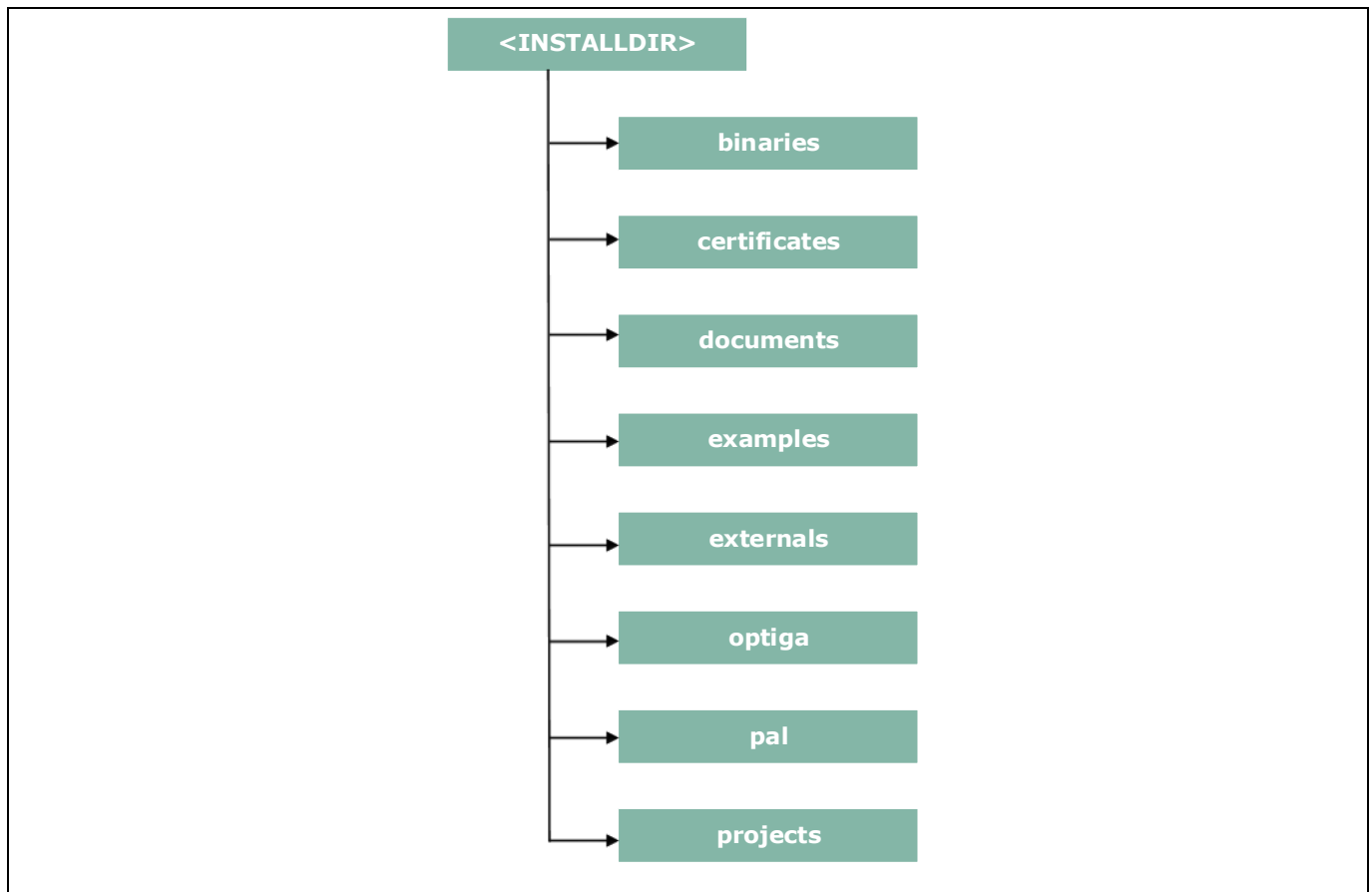
#### 2.1.1 Evaluation Kit Components

**Table 3** Evaluation Kit contents

No.	Item	Description
1	ESP32-DevKitC V4	Hardware Evaluation board for ESP32 microcontroller.
2	ESP32 DevKitC Adapter for Shield2Go	ESP32-DevKitC V4 compatible connector to add Shield2Go board on ESP32-DevKitC V4.
3	OPTIGA™ Trust M2 ID2 Security Shield2Go	ESP32 DevKitC V4 Adapter compatible Shield2Go board contains OPTIGA™ Trust M2 ID2 chip.
4	Micro USB to USB cable	The cable provides DC supply to ESP32-DevKitC V4 and to flash software.

### 2.2 Installed Software Components

The installed directory structure of OPTIGA™ Trust M2 ID2 setup software is shown below:



**Figure 1** Installed directory structure

**<INSTALLDIR>** is the root directory to which the release package contents are extracted. The following section explains the contents of each subdirectory under installed directory:

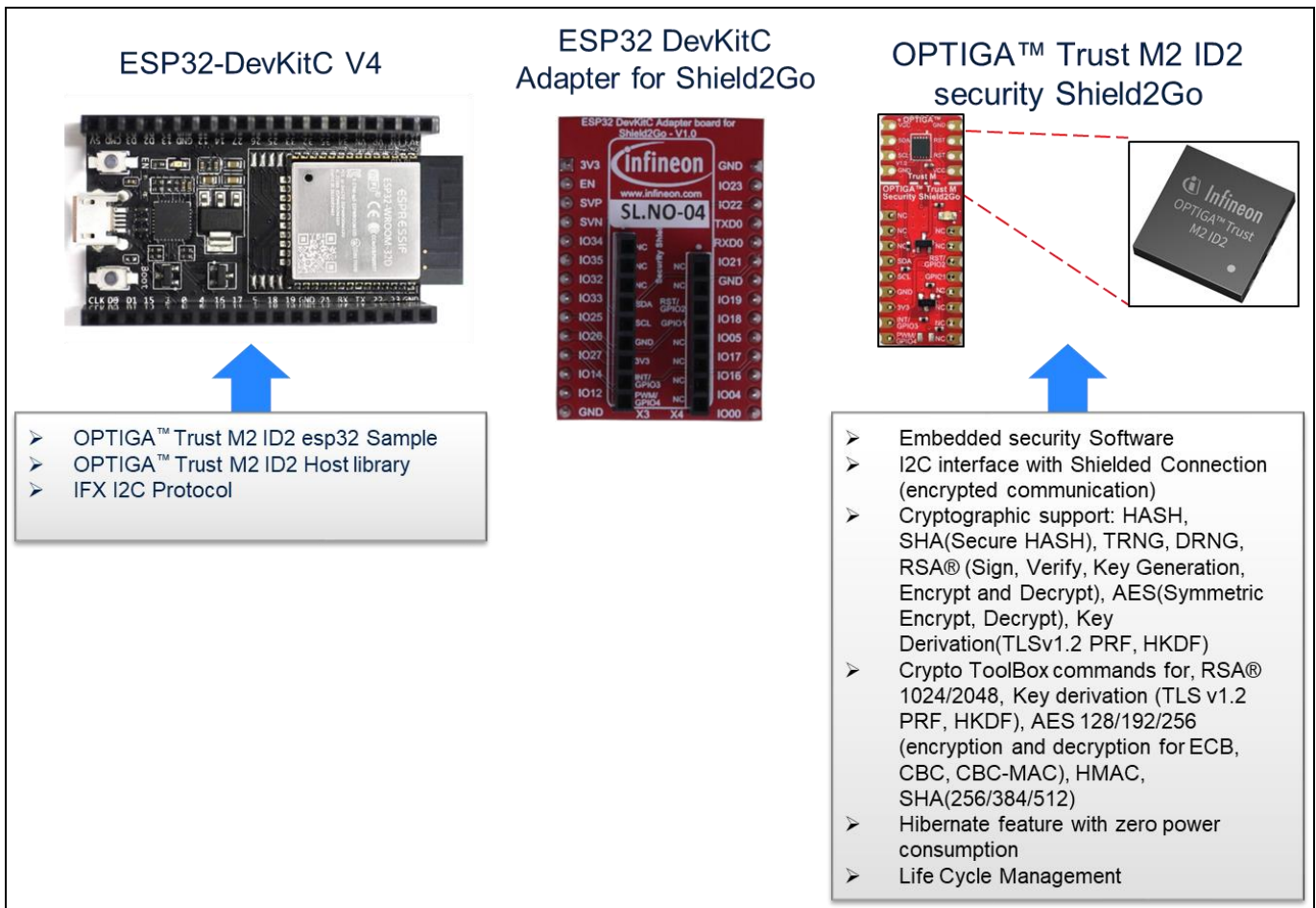
1. **binaries** -- binaries for OPTIGA™ Trust M2 ID2 example application.
2. **certificates** -- Place holder for OPTIGA™ Trust M2 ID2 certificates.
3. **documents** -- Relevant OPTIGA™ Trust M2 ID2 documentation.
4. **examples** -- example use cases for Toolbox features and a tool for generation of manifest and fragment for protected update feature.
5. **externals** -- alios and mbedtls software crypto library.
6. **optiga** -- OPTIGA™ Trust M2 ID2 libraries.
7. **pal** -- PAL for ESP32-DevKitC V4 device and PAL for mbedtls software crypto library.
8. **projects** -- ESP32-DevKitC V4 device example project.

## System Setup

### 3 System Setup

This section explains the basic components required for system setup.

#### 3.1 System Overview



**Figure 2 System Overview**

This system consists of the following components:

1. ESP32-DevKitC V4
  - The ESP32-DevKitC V4 is an evaluation board with ESP32 Microcontroller from espressif. For more information refer document [\[1\]](#).
  - It is used as a reference platform to simulate the Host.
  - It interacts via I2C.
2. ESP32 DevKitC Adapter for Shield2Go
  - It acts as a gateway to add Shield2Go boards onto ESP32-DevKitC V4.
3. OPTIGA™ Trust M2 ID2 Security Shield2Go
  - Shield2Go board contains OPTIGA™ Trust M2 ID2 chip.

The following interface/connection is done among the above components:

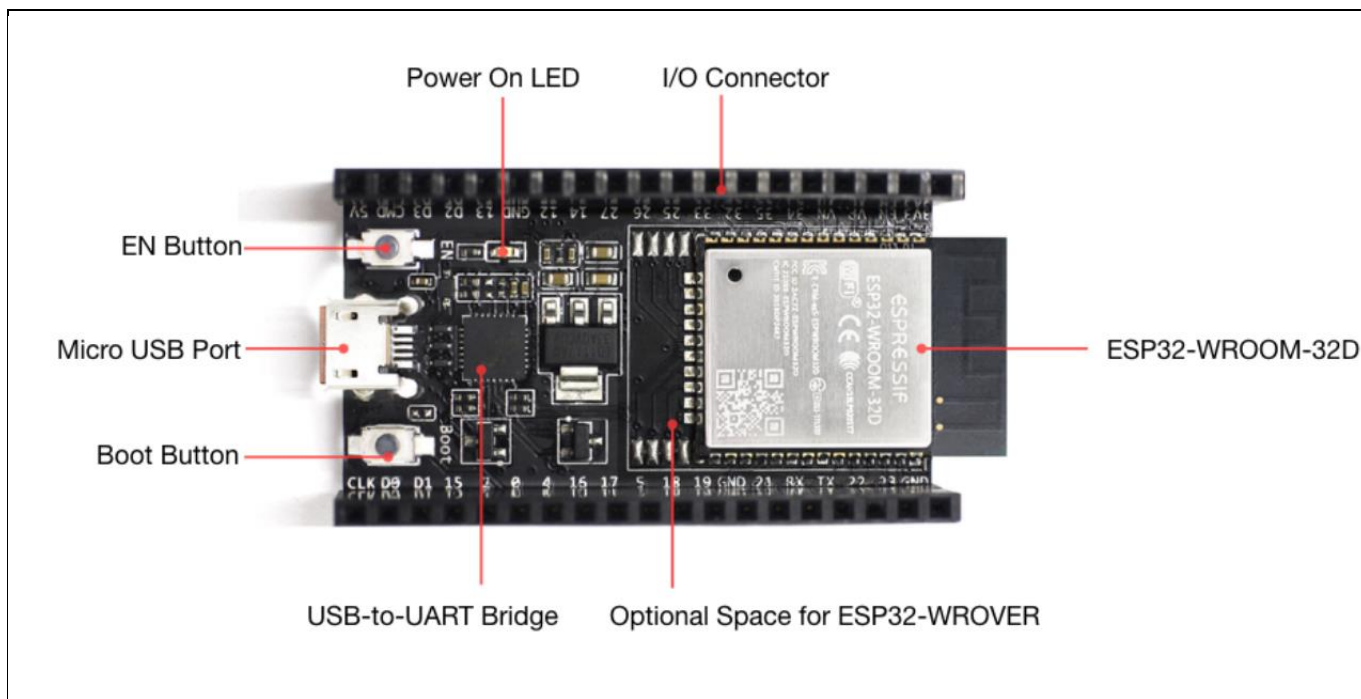
- Micro USB data cable (with Data line) from PC is connected to ESP32-DevKitC V4 to supply power.

## System Setup

### 3.2 Hardware Setup

The hardware required to run OPTIGA™ Trust M2 ID2 setup is described in this section.

#### 3.2.1 ESP32-DevKitC V4



**Figure 3** ESP32-DevKitC V4

Connector supports I2C, reset pin and power supply interfaces among others.

**Table 4** ESP32-DevKitC V4 Pin Information

No.	Description	Pin
1	I2C SCL	IO 22
2	I2C SDA	IO 21
3	RST	IO 25
4	VCC	IO 26
5	GND	GND

For more information about the ESP32 Specification, Architecture and Design/Schematic, refer document [\[1\]](#)

#### 3.2.2 ESP32 DevKitC Adapter for Shield2Go

The ESP32 DevKitC adapter is an evaluation board that allows users to easily combine different Shield2Go boards to ESP compliant ecosystem, for fast evaluation of IoT systems. With its solderless connectors, it allows users to easily stack Shield2Go boards instead of soldering it. The adapter design is derived from ESP32-DevKitC V4 evaluation board.



## System Setup



**Figure 4** ESP32 DevKitC Adapter for Shield2Go

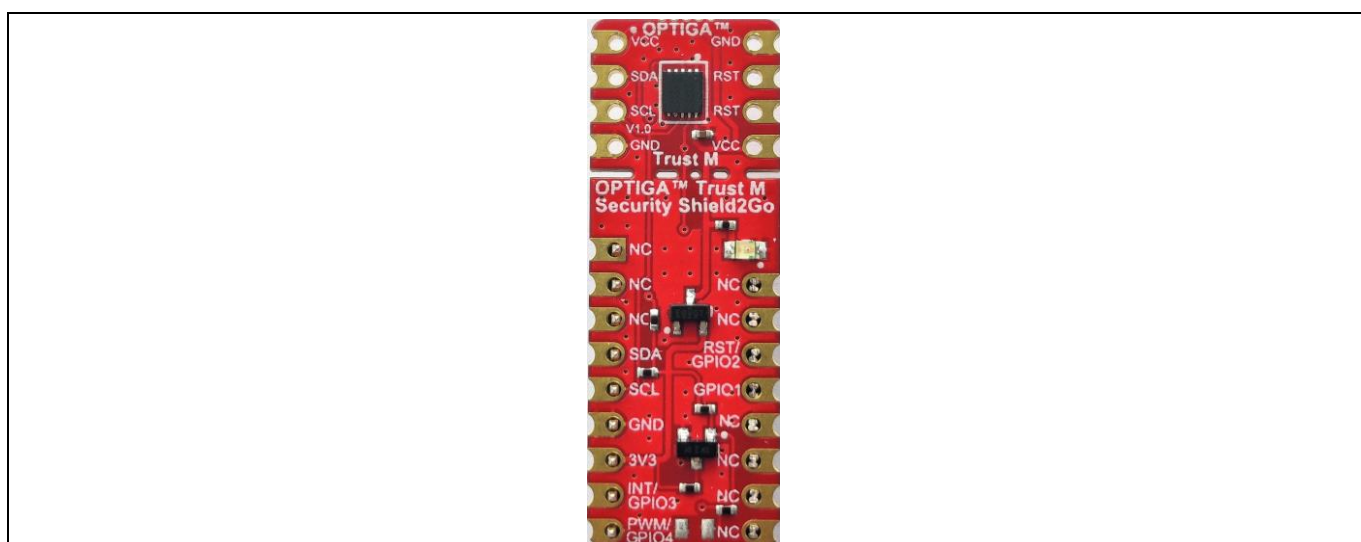
ESP32 DevKitC adapter features are as follows:

- Provide power supply and connectivity for Shield2Go boards.

More information is available at [Infineon website](https://www.infineon.com).

### 3.2.3 Shield2Go Security OPTIGA™ Trust M2 ID2

Shield2Go boards are equipped with featured Infineon ICs and provide a standardized form factor and pin layout, allowing a ‘plug and play’ approach for easy prototyping.



**Figure 5** OPTIGA™ Trust M2 ID2 Shield2Go

The OPTIGA™ Trust M2 ID2 Shield2Go is equipped with OPTIGA™ Trust M2 ID2 security chip. It allows users to develop system solutions by combining Shield2Go with ESP32 DevKitC Adapter for Shield2Go and ESP32.

*Note: Ensure no voltage supplied to any of the pins exceeds the absolute maximum rating of  $V_{cc} + 0.3 V$ .*



## System Setup

### 3.3 Software Setup

This section describes the software used in ESP32 to run the AliOS-Things OPTIGA™ Trust M2 ID2 setup.

#### 3.3.1 Software Components

All the software components required on AliOS-Things for ESP32 are explained in the following sections.

##### 3.3.1.1 ESP32-DevKitC V4

1. OPTIGA™ Trust M2 ID2 Host Library consists of the following:

- Service Layer  
The layers (Util and Crypt) provide APIs to interact with OPTIGA™ for various use-case functionalities.
- Access Layer  
This layer manages the access to the command interface of OPTIGA™ security chip. It also provides the communication interface to the OPTIGA™.
- Platform Abstraction Layer  
This layer provides platform agnostic interfaces for the underlying HW and SW platform functionalities used by OPTIGA™ libraries.
- Platform Layer  
This layer provides the platform specific components and libraries for the supported platforms.

2. I2C Protocol

This is an implementation as per document [\[2\]](#).

3. ESP32 I2C Driver

These are low level I2C device driver for I2C communication from ESP32 to OPTIGA™ Trust M2 ID2 Security chip.

#### 3.3.2 PC Requirements and Configurations

##### 3.3.2.1 PC Requirement

A 32-bit or 64-bit PC with Windows 7/10 Operating System with the below requirements need to be used for setting up ESP32 to run the AliOS-Things using OPTIGA™ Trust M2 ID2 setup:

1. One USB port.
2. Python 2.7.14 version to install AliOS-Thing dependency packages  
Link to download Python 2.7.14: [Download link](#)
3. Git for downloading source code.  
Link to download git: [Download link](#)
4. FTD driver to access ESP32 via COM port.  
Link to download FTD driver: [Download link](#)

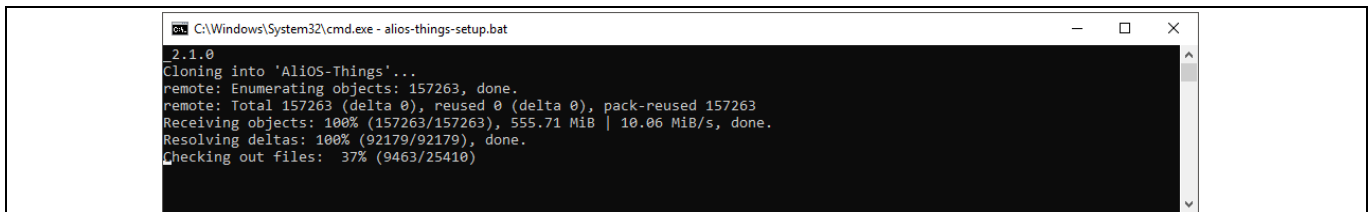
*Note: Add C:\Python27 and C:\Python27\Scripts path to environment variable in the beginning of the environment variable list.*

## Shell Application execution using OPTIGA™ Trust M2 ID2

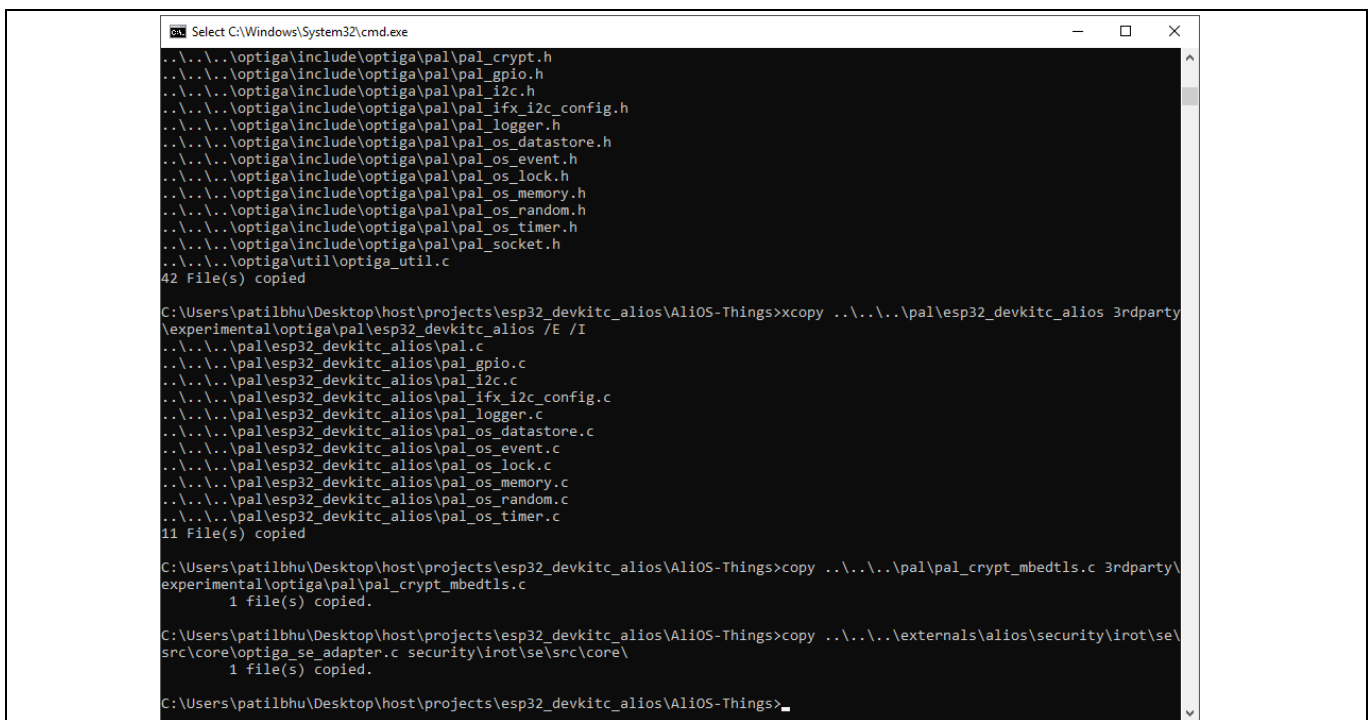
### 4 Shell Application execution using OPTIGA™ Trust M2 ID2

#### 4.1 Quick Setup

1. Navigate to `<INSTALLDIR>/projects/esp32_devkitc_alios` and execute the `alios-things-setup.bat` script.



**Figure 6** Download AliOS-Things source package ongoing



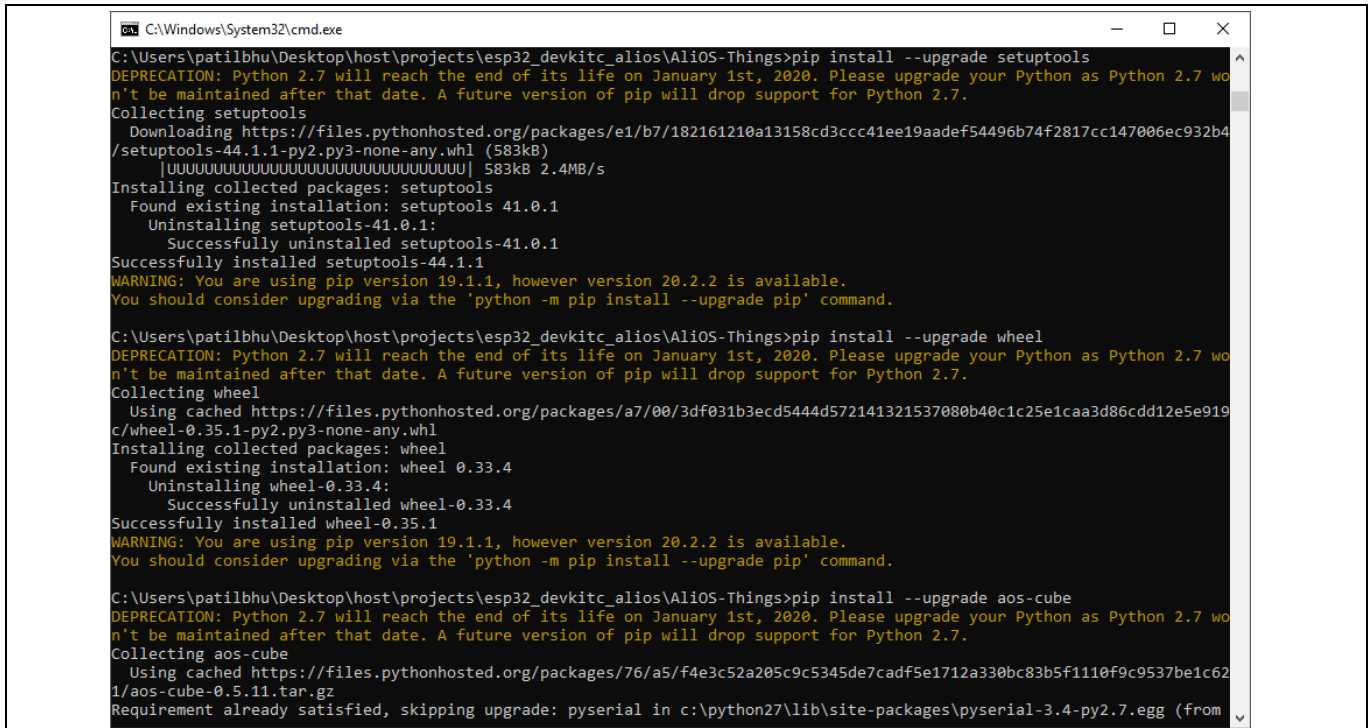
**Figure 7** Download AliOS-Things source package complete

2. A folder with name **AliOS-Things** will appear under `<INSTALLDIR>/projects/esp32_devkitc_alios`.

*Note: Ignore warning: 1 line adds whitespace errors.*

3. Upgrade aos-cube by following the below steps in command line terminal at `<INSTALLDIR>/projects/esp32_devkitc_alios/AliOS-Things`
  - o pip install --upgrade setuptools
  - o pip install --upgrade wheel
  - o pip install --upgrade aos-cube

## Shell Application execution using OPTIGA™ Trust M2 ID2



```

C:\Windows\System32\cmd.exe

C:\Users\patilbhu\Desktop\host\projects\esp32_devkitc_alios\AliOS-Things>pip install --upgrade setuptools
DEPRECATION: Python 2.7 will reach the end of its life on January 1st, 2020. Please upgrade your Python as Python 2.7 wo
n't be maintained after that date. A future version of pip will drop support for Python 2.7.
Collecting setuptools
  Downloading https://files.pythonhosted.org/packages/e1/b7/182161210a13158cd3ccc41ee19aade54496b74f2817cc147006ec932b4
/ setuptools-44.1.1-py2.py3-none-any.whl (583kB)
    [UUUUUUUUUUUUUUUUUUUUUUUUUUUUUUUUUUUU] 583kB 2.4MB/s
Installing collected packages: setuptools
  Found existing installation: setuptools 41.0.1
  Uninstalling setuptools-41.0.1:
    Successfully uninstalled setuptools-41.0.1
Successfully installed setuptools-44.1.1
WARNING: You are using pip version 19.1.1, however version 20.2.2 is available.
You should consider upgrading via the 'python -m pip install --upgrade pip' command.

C:\Users\patilbhu\Desktop\host\projects\esp32_devkitc_alios\AliOS-Things>pip install --upgrade wheel
DEPRECATION: Python 2.7 will reach the end of its life on January 1st, 2020. Please upgrade your Python as Python 2.7 wo
n't be maintained after that date. A future version of pip will drop support for Python 2.7.
Collecting wheel
  Using cached https://files.pythonhosted.org/packages/a7/00/3df031b3ecd5444d572141321537080b40c1c25e1caa3d86cdd12e5e919
c/wheel-0.35.1-py2.py3-none-any.whl
Installing collected packages: wheel
  Found existing installation: wheel 0.33.4
  Uninstalling wheel-0.33.4:
    Successfully uninstalled wheel-0.33.4
Successfully installed wheel-0.35.1
WARNING: You are using pip version 19.1.1, however version 20.2.2 is available.
You should consider upgrading via the 'python -m pip install --upgrade pip' command.

C:\Users\patilbhu\Desktop\host\projects\esp32_devkitc_alios\AliOS-Things>pip install --upgrade aos-cube
DEPRECATION: Python 2.7 will reach the end of its life on January 1st, 2020. Please upgrade your Python as Python 2.7 wo
n't be maintained after that date. A future version of pip will drop support for Python 2.7.
Collecting aos-cube
  Using cached https://files.pythonhosted.org/packages/76/a5/f4e3c52a205c9c5345de7cadf5e1712a330bc83b5f1110f9c9537be1c62
1/aos-cube-0.5.11.tar.gz
Requirement already satisfied, skipping upgrade: pyserial in c:\python27\lib\site-packages\pyserial-3.4-py2.7.egg (from

```

**Figure 8** aos-cube upgrade

### 4.1.1 Configure and build for ESP32-DevKitC V4

This section describes how to configure and build optiga\_shell\_app in AliOS-Things source code for ESP32.

*Note: To use customize Device name and secret please refer this [section](#).*

#### 4.1.1.1 Configuration

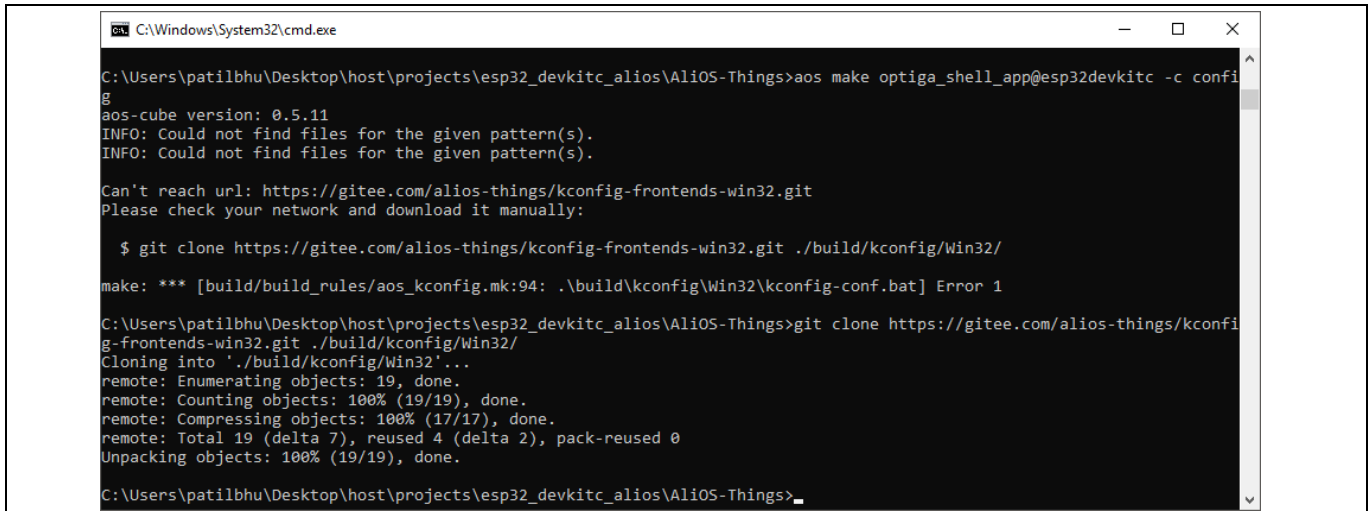
1. Execute below command to configure the optiga\_shell\_app example

- aos make optiga\_shell\_app@esp32devkitc -c config

*Note: git clone config in case of error, shown in Figure 9*

- Execute below command in the terminal
- git clone <https://gitee.com/alios-things/kconfig-frontends-win32.git> ./build/kconfig/Win32/

## Shell Application execution using OPTIGA™ Trust M2 ID2



```
C:\Windows\System32\cmd.exe
C:\Users\patilbhu\Desktop\host\projects\esp32_devkitc_alios\AliOS-Things>aos make optiga_shell_app@esp32devkitc -c config
aos-cube version: 0.5.11
INFO: Could not find files for the given pattern(s).
INFO: Could not find files for the given pattern(s).

Can't reach url: https://gitee.com/alios-things/kconfig-frontends-win32.git
Please check your network and download it manually:

$ git clone https://gitee.com/alios-things/kconfig-frontends-win32.git ./build/kconfig/Win32/

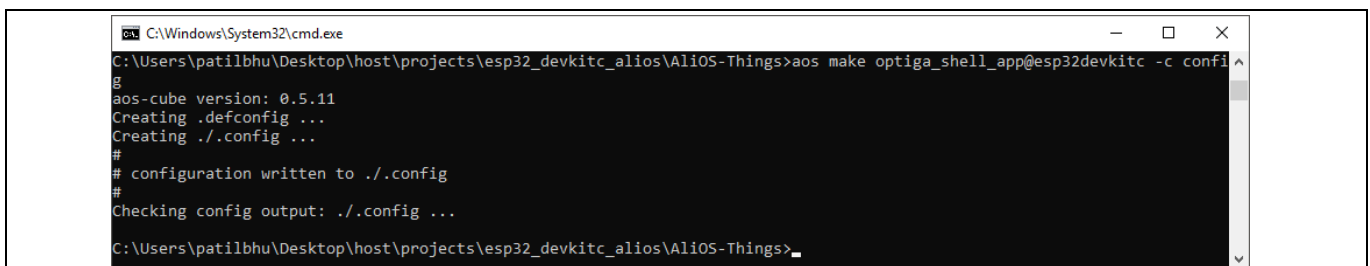
make: *** [build/build_rules/aos_kconfig.mk:94: .\build\kconfig\Win32\kconfig-conf.bat] Error 1

C:\Users\patilbhu\Desktop\host\projects\esp32_devkitc_alios\AliOS-Things>git clone https://gitee.com/alios-things/kconfig-frontends-win32.git ./build/kconfig/Win32/
Cloning into './build/kconfig/Win32'...
remote: Enumerating objects: 19, done.
remote: Counting objects: 100% (19/19), done.
remote: Compressing objects: 100% (17/17), done.
remote: Total 19 (delta 7), reused 4 (delta 2), pack-reused 0
Unpacking objects: 100% (19/19), done.

C:\Users\patilbhu\Desktop\host\projects\esp32_devkitc_alios\AliOS-Things>
```

**Figure 9** Error while configuring AliOS-Things

- Rerun `aos make optiga_shell_app@esp32devkitc -c config`



```
C:\Windows\System32\cmd.exe
C:\Users\patilbhu\Desktop\host\projects\esp32_devkitc_alios\AliOS-Things>aos make optiga_shell_app@esp32devkitc -c config
aos-cube version: 0.5.11
Creating .defconfig ...
Creating .config ...
# configuration written to ./config
#
Checking config output: ./config ...

C:\Users\patilbhu\Desktop\host\projects\esp32_devkitc_alios\AliOS-Things>
```

**Figure 10** AliOS-Things config complete

### 4.1.1.2 Build source code

1. To build source code execute below command
  - `aos make`

## Shell Application execution using OPTIGA™ Trust M2 ID2

```

C:\Windows\System32\cmd.exe
Making out/optiga_shell_app@esp32devkitc/libraries/vfs.a
Making out/optiga_shell_app@esp32devkitc/libraries/optiga.a
Making optiga_shell_app@esp32devkitc.elf
Making optiga_shell_app@esp32devkitc.bin
Making optiga_shell_app@esp32devkitc.hex

AOS MEMORY MAP
=====
| MODULE | ROM | RAM |
=====
| arch_xtensa_lx6 | 4664 | 2088 |
| board_esp32devkitc | 311 | 0 |
| debug | 5315 | 16 |
| kernel_init | 114 | 0 |
| kv | 3399 | 24 |
| libbootloader_support | 1698 | 0 |
| libc | 59263 | 0 |
| libcoexist | 3716 | 1471 |
| libcontainer | 2634 | 0 |
| libcore | 1213 | 5 |
| libdriver | 13145 | 141 |
| libesp32 | 31523 | 4420 |
| libgcc | 5211 | 0 |
| libhal | 547 | 0 |
| liblog | 884 | 276 |
| libm | 92 | 0 |
| libmesh | 55 | 2 |
| libnet80211 | 125138 | 9366 |
| libnewlib | 3137 | 424 |
| libnvs_flash | 11806 | 32 |
| libphy | 34793 | 2194 |
| libpp | 62940 | 6510 |
| librtc | 879 | 4 |
| libsoc | 7026 | 8 |
| libspi_flash | 6138 | 349 |
| libstdc++\+-cache-workaround | 96 | 4 |
| libvfs | 3602 | 335 |
| libwpa | 23277 | 682 |
| libwpa2 | 0 | 1 |
| libwpa_supplicant | 15202 | 0 |
| libwps | 0 | 1 |
| lwip | 52251 | 1164 |
| mcu_esp32 | 20577 | 574 |
| netmgr | 285 | 8 |
| optiga | 108968 | 12450 |
| optiga_shell_app | 36 | 0 |
| osal_aos | 1376 | 0 |
| rhino | 24452 | 9888 |
| ulog | 325 | 14 |
| vfs | 1514 | 1105 |
| yloop | 2118 | 32 |
| *fill* | 4737 | 244 |
=====
| TOTAL (bytes) | 644457 | 53832 |
=====

esptool.py v2.6
653888
e98c8260c5d60f988d95e46286d390d4
Build complete: optiga_shell_app@esp32devkitc

C:\Users\patilbhu\Desktop\host\projects\esp32_devkitc_alios\AliOS-Things>

```

**Figure 11 Build source code**

### 4.1.2 Download example hex file to ESP32-DevKitC V4

1. Execute below command to flash the generated HEX file

- aos upload optiga\_shell\_app@esp32devkitc

*Note: Note the COM port asserted after connecting the ESP32-DevKitC board.*

*Disconnect the ESP32-DevKitC board COM port before flashing of hex file.*

## Shell Application execution using OPTIGA™ Trust M2 ID2

```
--- Available ports:
--- 1: COM3          u'Intel(R) Active Management Technology - SOL (COM3)'
--- 2: COM6          u'Silicon Labs CP210x USB to UART Bridge (COM6)'
--- 3: COM25         u'Standard Serial over Bluetooth link (COM25)'
--- 4: COM26         u'Standard Serial over Bluetooth link (COM26)'
--- Enter port index or full name 2
```

Figure 12 Selecting COM port

### 4.1.3 Steps to execute optiga\_shell\_app example

1. Configure COM port with 115200 8N1.

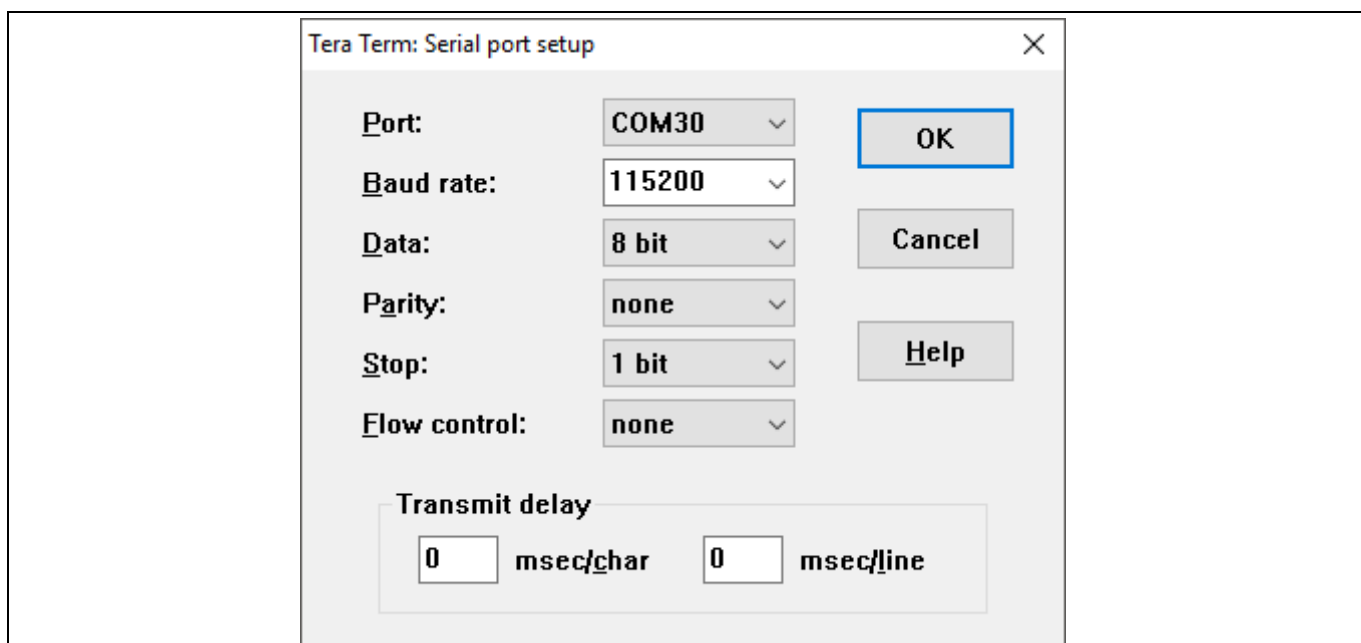
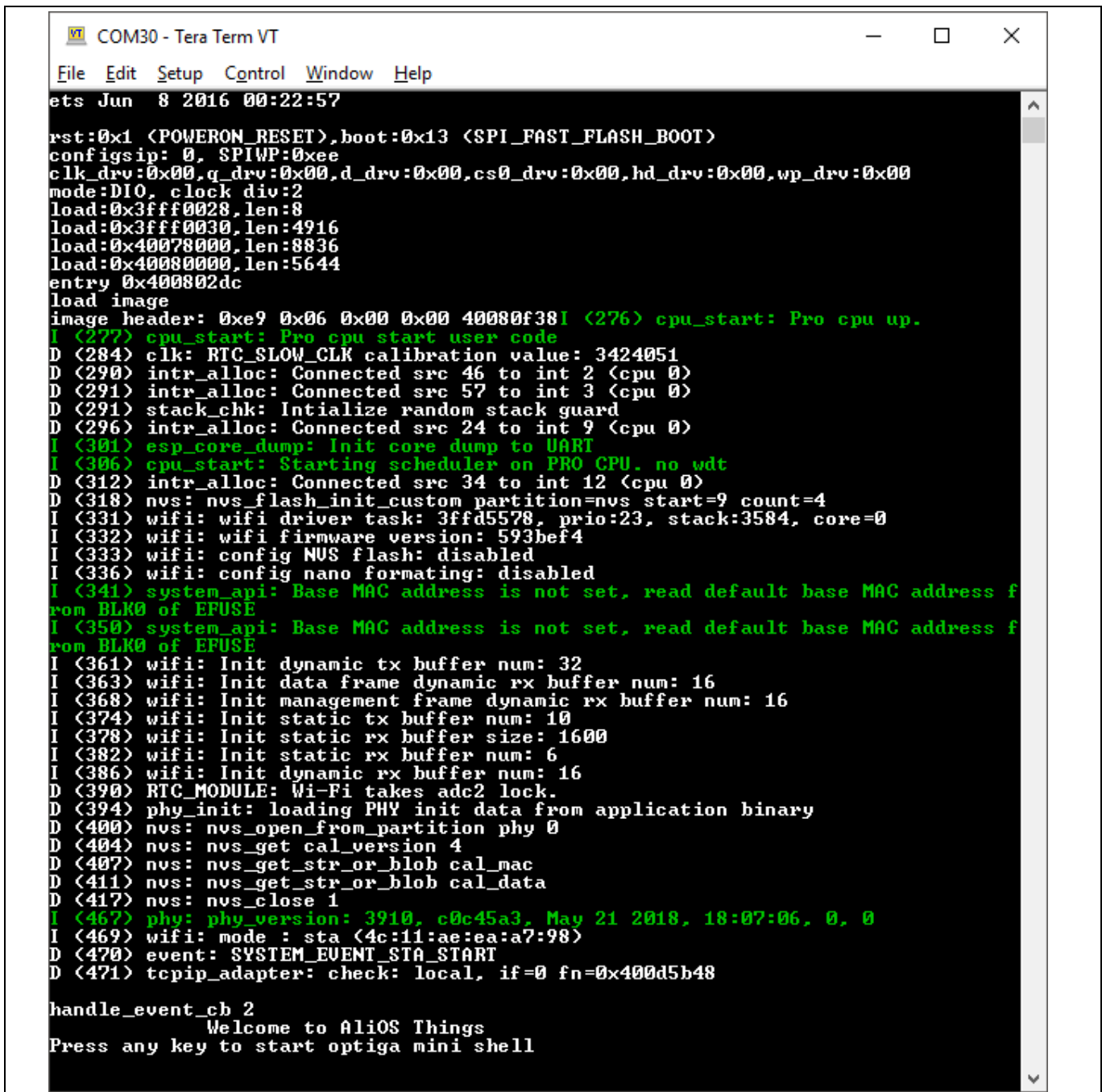


Figure 13 TeraTerm terminal serial configuration

2. After connecting the terminal it will start with boot sequence.

## Shell Application execution using OPTIGA™ Trust M2 ID2



```

COM30 - Tera Term VT
File Edit Setup Control Window Help
ets Jun  8 2016 00:22:57

rst:0x1 (POWERON_RESET),boot:0x13 (SPI_FAST_FLASH_BOOT)
configsip: 0, SPIWP:0xee
clk_drv:0x00,q_drv:0x00,d_drv:0x00,cs0_drv:0x00,hd_drv:0x00,wp_drv:0x00
mode:DIO, clock div:2
load:0x3fff0028,len:8
load:0x3fff0030,len:4916
load:0x40078000,len:8836
load:0x40080000,len:5644
entry 0x400802dc
load image
image header: 0xe9 0x06 0x00 0x00 40080f38I (276) cpu_start: Pro cpu up.
I (277) cpu_start: Pro cpu start user code
D (284) clk: RTC_SLOW_CLK calibration value: 3424051
D (290) intr_alloc: Connected src 46 to int 2 (cpu 0)
D (291) intr_alloc: Connected src 57 to int 3 (cpu 0)
D (291) stack_chk: Initialize random stack guard
D (296) intr_alloc: Connected src 24 to int 9 (cpu 0)
I (301) esp_core_dump: Init core dump to UART
I (306) cpu_start: Starting scheduler on PRO CPU. no wdt
D (312) intr_alloc: Connected src 34 to int 12 (cpu 0)
D (318) nvs: nvs_flash_init_custom partition=nvs start=9 count=4
I (331) wifi: wifi driver task: 3ffd5578, prio:23, stack:3584, core=0
I (332) wifi: wifi firmware version: 593bef4
I (333) wifi: config NUS flash: disabled
I (336) wifi: config nano formatting: disabled
I (341) system_api: Base MAC address is not set, read default base MAC address f
rom BLK0 of EFUSE
I (350) system_api: Base MAC address is not set, read default base MAC address f
rom BLK0 of EFUSE
I (361) wifi: Init dynamic tx buffer num: 32
I (363) wifi: Init data frame dynamic rx buffer num: 16
I (368) wifi: Init management frame dynamic rx buffer num: 16
I (374) wifi: Init static tx buffer num: 10
I (378) wifi: Init static rx buffer size: 1600
I (382) wifi: Init static rx buffer num: 6
I (386) wifi: Init dynamic rx buffer num: 16
D (390) RTC_MODULE: Wi-Fi takes adc2 lock.
D (394) phy_init: loading PHY init data from application binary
D (400) nvs: nvs_open_from_partition phy 0
D (404) nvs: nvs_get_cal_version 4
D (407) nvs: nvs_get_str_or_blob cal_mac
D (411) nvs: nvs_get_str_or_blob cal_data
D (417) nvs: nvs_close 1
I (467) phy: phy_version: 3910, c0c45a3, May 21 2018, 18:07:06, 0, 0
I (469) wifi: mode : sta (4c:11:ae:ea:a7:98)
D (470) event: SYSTEM_EVENT_STA_START
D (471) tcpip_adapter: check: local, if=0 fn=0x400d5b48

handle_event_cb 2
Welcome to AliOS Things
Press any key to start optiga mini shell

```

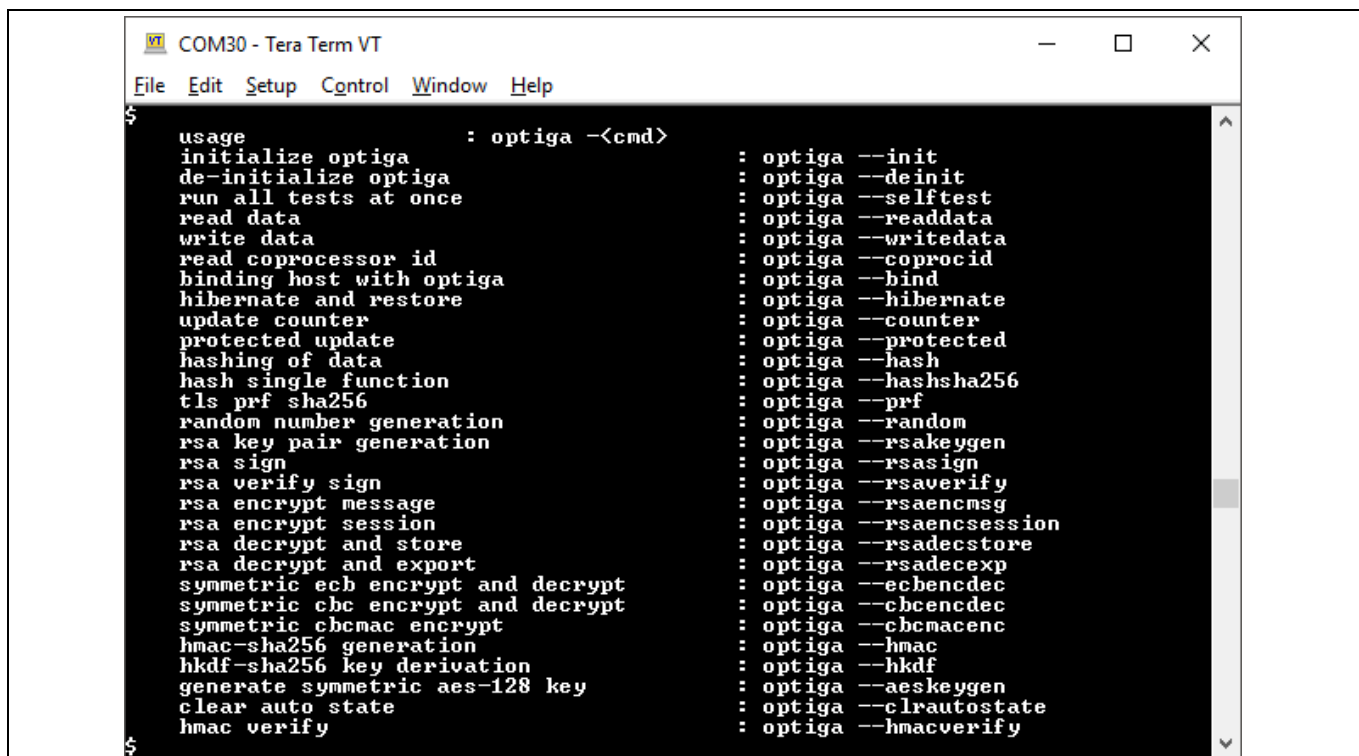
Figure 14 AliOS-Things initial boot sequence

Note: In some cases the boot sequence is not shown, in that case press enter on terminal.

1. Press any key to start the optiga mini shell.



## Shell Application execution using OPTIGA™ Trust M2 ID2



```

COM30 - Tera Term VT
File Edit Setup Control Window Help
$
usage           : optiga -<cmd>
initialize optiga           : optiga --init
de-initialize optiga        : optiga --deinit
run all tests at once      : optiga --selftest
read data                  : optiga --readdata
write data                  : optiga --writedata
read coprocessor id        : optiga --coprocid
binding host with optiga    : optiga --bind
hibernate and restore      : optiga --hibernate
update counter              : optiga --counter
protected update           : optiga --protected
hashing of data             : optiga --hash
hash single function        : optiga --hashsha256
tls prf sha256              : optiga --prf
random number generation    : optiga --random
rsa key pair generation     : optiga --rsakeygen
rsa sign                    : optiga --rsasign
rsa verify sign             : optiga --rsaverify
rsa encrypt message         : optiga --rsaencmsg
rsa encrypt session         : optiga --rsaencsession
rsa decrypt and store       : optiga --rsadecstore
rsa decrypt and export      : optiga --rsadecexp
symmetric ecb encrypt and decrypt : optiga --ecbencdec
symmetric cbc encrypt and decrypt : optiga --cbcencdec
symmetric cbcmac encrypt    : optiga --cbcmacenc
hmac-sha256 generation      : optiga --hmac
hkdf-sha256 key derivation  : optiga --hkdf
generate symmetric aes-128 key : optiga --aeskeygen
clear auto state            : optiga --clrautostate
hmac verify                 : optiga --hmacverify
$

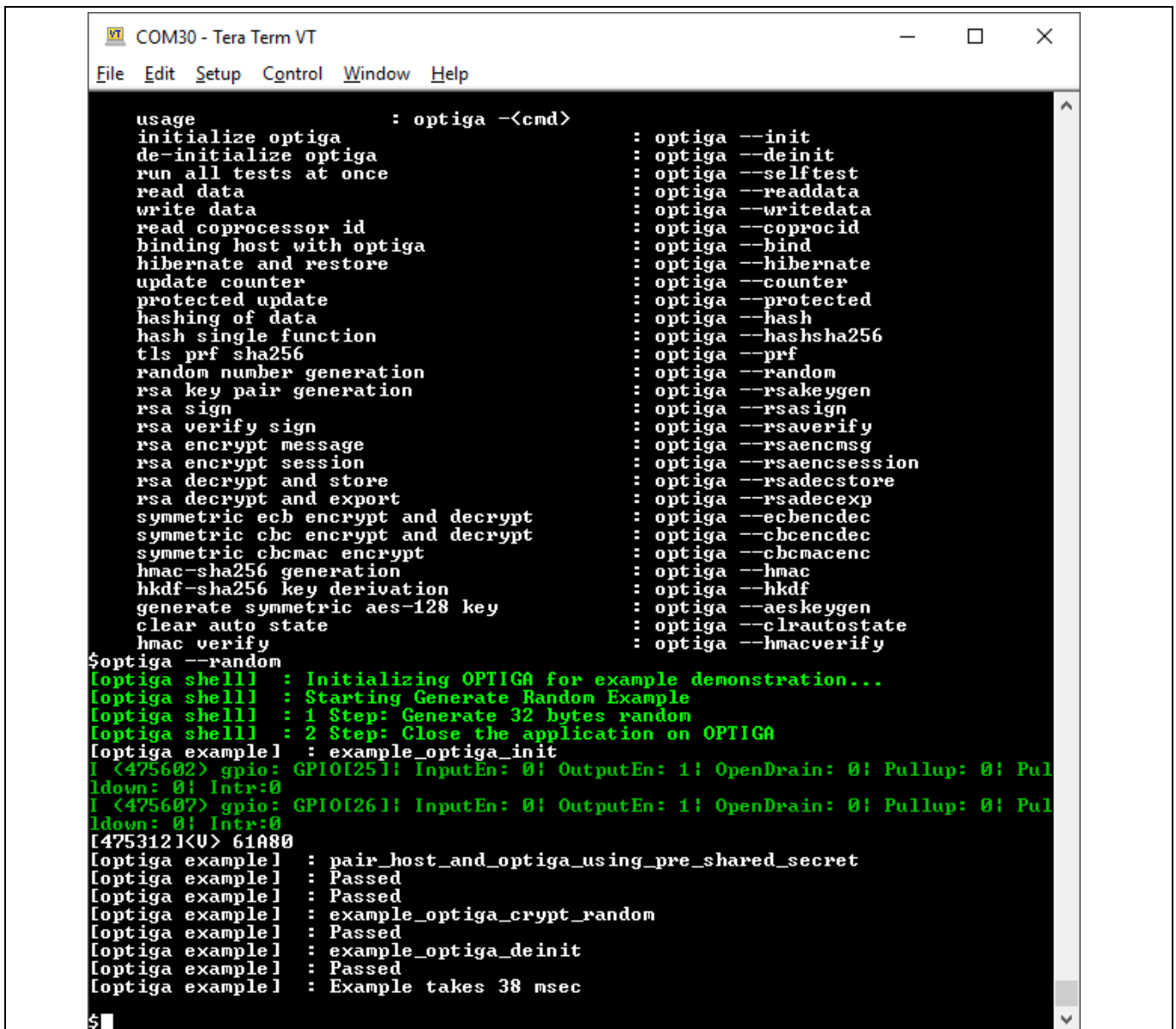
```

**Figure 15** Optiga available commands for shell application

2. Enter optiga command in format of “optiga --<cmd>” for a self-contained example.

*Note:* By default self-contained concept of examples is enabled, where each example performs init & deinit of OPTIGA.

## Shell Application execution using OPTIGA™ Trust M2 ID2



```

usage          : optiga -<cmd>
initialize optiga          : optiga --init
de-initialize optiga       : optiga --deinit
run all tests at once     : optiga --selftest
read data                : optiga --readdata
write data               : optiga --writedata
read coprocessor id      : optiga --coprocid
binding host with optiga  : optiga --bind
hibernate and restore    : optiga --hibernate
update counter           : optiga --counter
protected update         : optiga --protected
hashing of data          : optiga --hash
hash single function     : optiga --hashsha256
tls prf sha256           : optiga --prf
random number generation : optiga --random
rsa key pair generation  : optiga --rsakeygen
rsa sign                 : optiga --rsasign
rsa verify sign          : optiga --rsaverify
rsa encrypt message      : optiga --rsaencmsg
rsa encrypt session      : optiga --rsaencsession
rsa decrypt and store    : optiga --rsadecstore
rsa decrypt and export   : optiga --rsadecexp
symmetric ecb encrypt and decrypt : optiga --ecbencdec
symmetric chc encrypt and decrypt : optiga --chcencdec
symmetric chcmac encrypt : optiga --chcmacenc
hmac-sha256 generation   : optiga --hmac
hkdf-sha256 key derivation : optiga --hkdf
generate symmetric aes-128 key : optiga --aeskeygen
clear auto state         : optiga --clrautostate
hmac verify              : optiga --hmacverify

$optiga --random
[optiga shell] : Initializing OPTIGA for example demonstration...
[optiga shell] : Starting Generate Random Example
[optiga shell] : 1 Step: Generate 32 bytes random
[optiga shell] : 2 Step: Close the application on OPTIGA
[optiga example] : example_optiga_init
I <475602> gpio: GPIO[25]: InputEn: 0! OutputEn: 1! OpenDrain: 0! Pullup: 0! Pul
ldown: 0! Intr:0
I <475607> gpio: GPIO[26]: InputEn: 0! OutputEn: 1! OpenDrain: 0! Pullup: 0! Pul
ldown: 0! Intr:0
[475312]<U> 61080
[optiga example] : pair_host_and_optiga_using_pre_shared_secret
[optiga example] : Passed
[optiga example] : Passed
[optiga example] : example_optiga_crypt_random
[optiga example] : Passed
[optiga example] : example_optiga_deinit
[optiga example] : Passed
[optiga example] : Example takes 38 msec

$

```

**Figure 16 Self-contained optiga random command execution using shell application**

- To execute the example without shielded connection, disable the macro OPTIGA\_COMMS\_SHIELDED\_CONNECTION in file optiga\_lib\_config.h at location `<INSTALLDIR>/projects/esp32_devkitc_alios/AliOS-Thing/3rdparty/experimental/optiga/optiga/include/optiga`

### 4.1.4 Logger control for shell application

By default only logging from example is enabled in the release package.

Further control for OPTIGA™ Trust M2 ID2 host code logging is available in optiga\_lib\_config.h.

The macro OPTIGA\_LIB\_ENABLE\_LOGGING provides complete control to enable/disable logging at host code. In addition, logging at UTIL, CRYPT, CMD and COMMS layer can be controlled using the following macros,

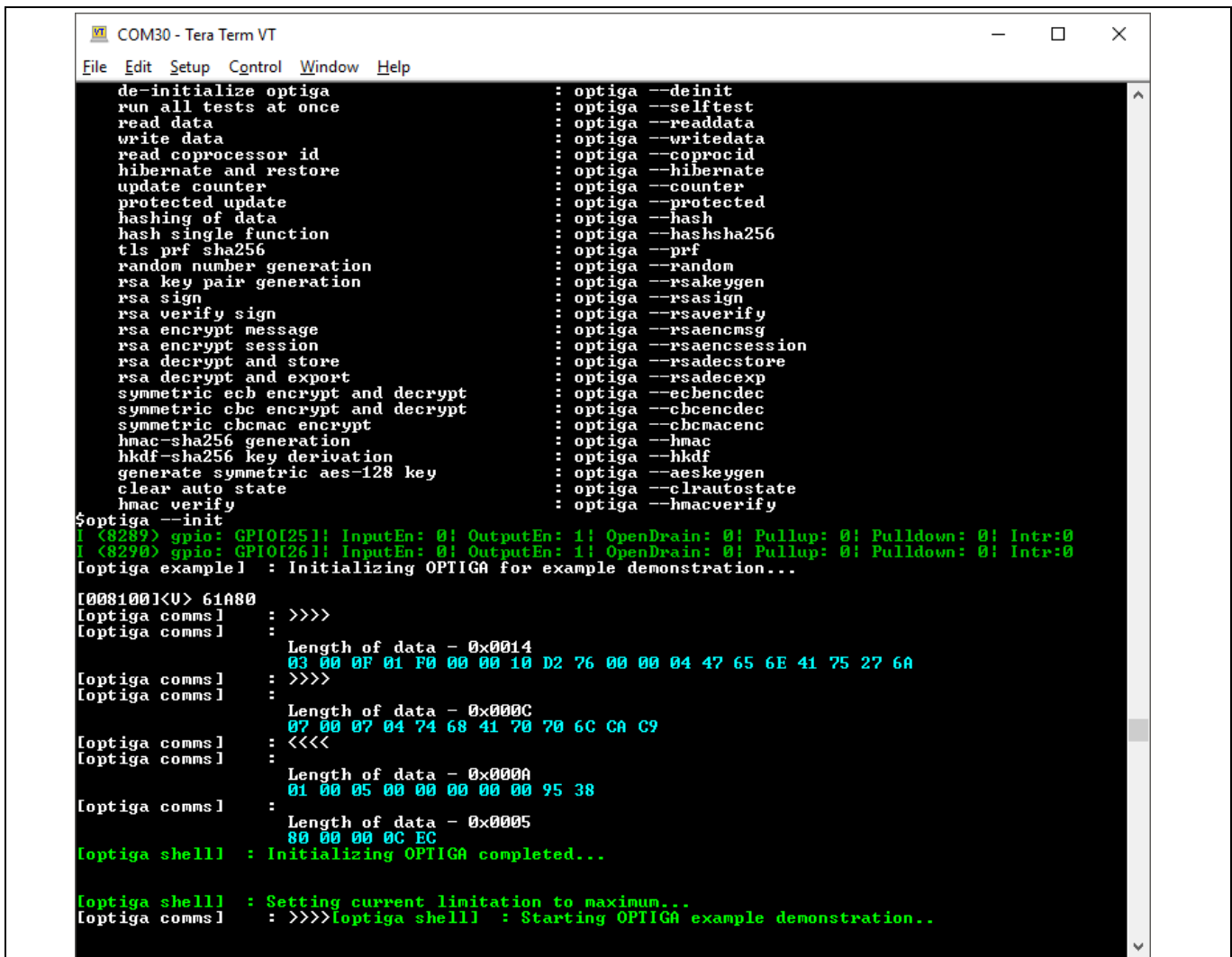
- OPTIGA\_LIB\_ENABLE\_UTIL\_LOGGING

## Shell Application execution using OPTIGA™ Trust M2 ID2

- OPTIGA\_LIB\_ENABLE\_CRYPT\_LOGGING
- OPTIGA\_LIB\_ENABLE\_CMD\_LOGGING
- OPTIGA\_LIB\_ENABLE\_COMMS\_LOGGING

For Example,

1. To enable logging for only COMMS layer, enable OPTIGA\_LIB\_ENABLE\_COMMS\_LOGGING and disable rest all layer macros.
2. Build the project as described in section 4.1.1.2 and execute the example as defined in 4.1.3.



```

COM30 - Tera Term VT
File Edit Setup Control Window Help
de-initialize optiga : optiga --deinit
run all tests at once : optiga --selftest
read data : optiga --readdata
write data : optiga --writedata
read coprocessor id : optiga --coprocid
hibernate and restore : optiga --hibernate
update counter : optiga --counter
protected update : optiga --protected
hashing of data : optiga --hash
hash single function : optiga --hashsha256
tls prf sha256 : optiga --prf
random number generation : optiga --random
rsa key pair generation : optiga --rsakeygen
rsa sign : optiga --rsasign
rsa verify sign : optiga --rsaverify
rsa encrypt message : optiga --rsaencmsg
rsa encrypt session : optiga --rsaencsession
rsa decrypt and store : optiga --rsadecstore
rsa decrypt and export : optiga --rsadecexp
symmetric ecb encrypt and decrypt : optiga --ecbencdec
symmetric cbc encrypt and decrypt : optiga --cbccncdec
symmetric chcmac encrypt : optiga --chcmacenc
hmac-sha256 generation : optiga --hmac
hkdf-sha256 key derivation : optiga --hkdf
generate symmetric aes-128 key : optiga --aeskeygen
clear auto state : optiga --clrautostate
hmac verify : optiga --hmacverify
$optiga --init
I <8289> gpio: GP10[25]! InputEn: 0! OutputEn: 1! OpenDrain: 0! Pullup: 0! Pulldown: 0! Intr:0
I <8290> gpio: GP10[26]! InputEn: 0! OutputEn: 1! OpenDrain: 0! Pullup: 0! Pulldown: 0! Intr:0
[optiga example] : Initializing OPTIGA for example demonstration...

[008100]<U> 61A80
[optiga comms] : >>>>
[optiga comms] :
Length of data - 0x0014
03 00 0F 01 F0 00 00 10 D2 76 00 00 04 47 65 6E 41 75 27 6A
[optiga comms] : >>>>
[optiga comms] :
Length of data - 0x000C
07 00 07 04 74 68 41 70 70 6C CA C9
[optiga comms] : <<<<
[optiga comms] :
Length of data - 0x000A
01 00 05 00 00 00 00 00 95 38
[optiga comms] :
Length of data - 0x0005
80 00 00 0C EC
[optiga shell] : Initializing OPTIGA completed...

[optiga shell] : Setting current limitation to maximum...
[optiga comms] : >>>>[optiga shell] : Starting OPTIGA example demonstration..
  
```

Figure 17 Logging data with only COMMS layer enabled

## Troubleshooting

### 5 Troubleshooting

**Table 5 Troubleshooting**

No	Problem	Reason	Solution
1	Longer <INSTALLDIR> path can cause build failure.	Long path	Extract on desktop.

## Revision History

## Revision History

**Table 6**

<b>Document version</b>	<b>Date of release</b>	<b>Description of changes</b>
2.00	2020-09-09	Initial version

## Trademarks

All referenced product or service names and trademarks are the property of their respective owners.

**Edition 2020-09-09**

### Published by

**Infineon Technologies AG**  
**81726 Munich, Germany**

**© 2020 Infineon Technologies AG.**  
**All Rights Reserved.**

**Do you have a question about this document?**

**Email:**  
[DSSCustomerService@infineon.com](mailto:DSSCustomerService@infineon.com)

**Document reference**

## IMPORTANT NOTICE

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics ("Beschaffenheitsgarantie").

With respect to any examples, hints or any typical values stated herein and/or any information regarding the application of the product, Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation warranties of non-infringement of intellectual property rights of any third party.

In addition, any information given in this document is subject to customer's compliance with its obligations stated in this document and any applicable legal requirements, norms and standards concerning customer's products and any use of the product of Infineon Technologies in customer's applications.

The data contained in this document is exclusively intended for technically trained staff. It is the responsibility of customer's technical departments to evaluate the suitability of the product for the intended application and the completeness of the product information given in this document with respect to such application.

For further information on the product, technology, delivery terms and conditions and prices please contact your nearest Infineon Technologies office ([www.infineon.com](http://www.infineon.com)).

## WARNINGS

Due to technical requirements products may contain dangerous substances. For information on the types in question please contact your nearest Infineon Technologies office.

Except as otherwise explicitly approved by Infineon Technologies in a written document signed by authorized representatives of Infineon Technologies, Infineon Technologies' products may not be used in any applications where a failure of the product or any consequences of the use thereof can reasonably be expected to result in personal injury.