

02_Top_Level.SchDoc

A

B

A

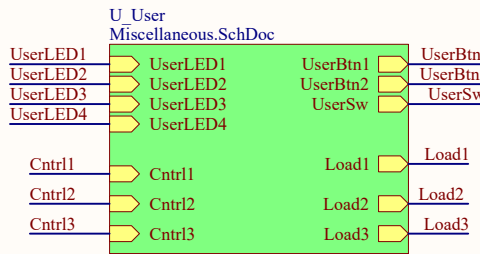
B

C

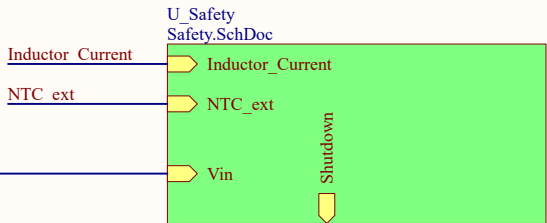
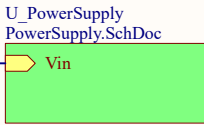
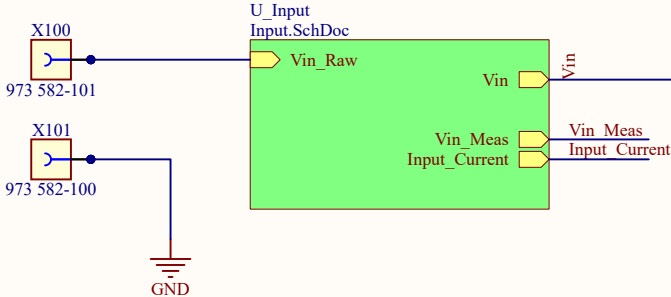
C

D

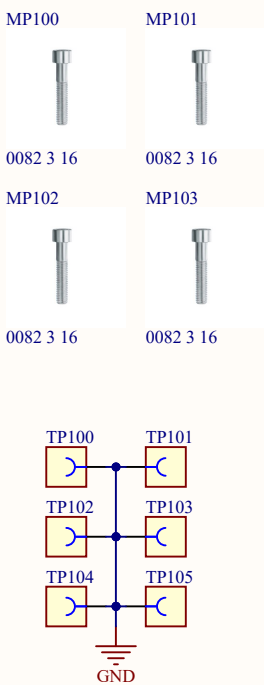
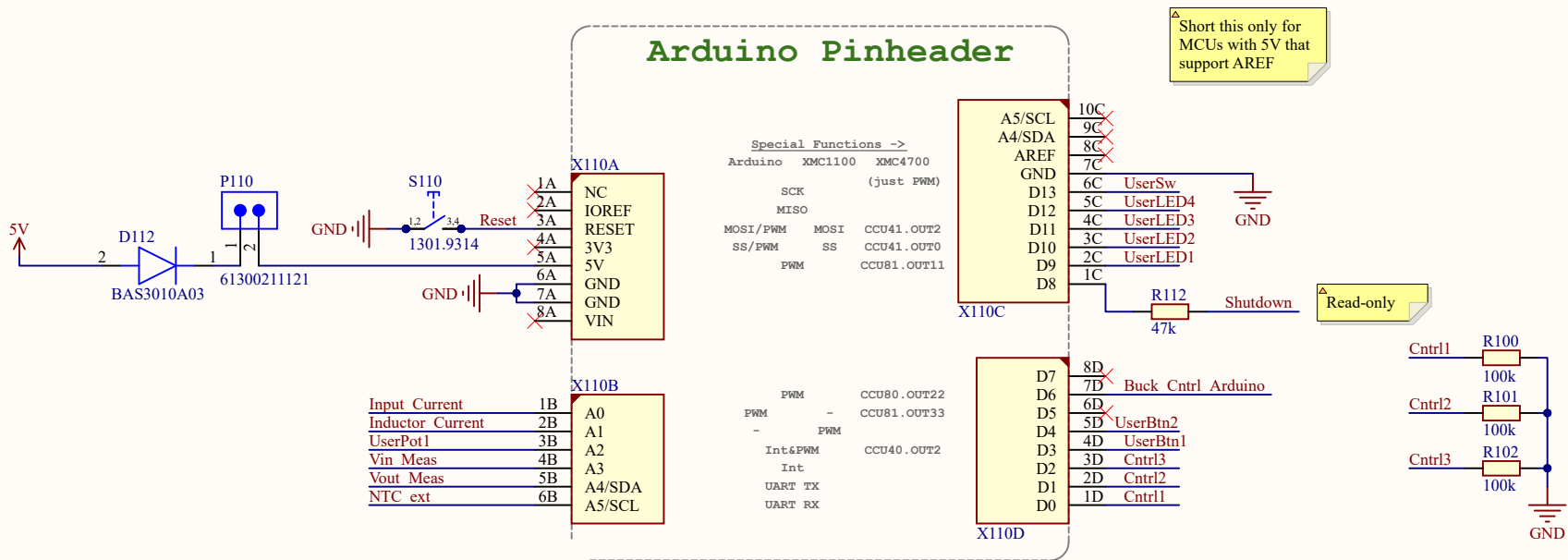
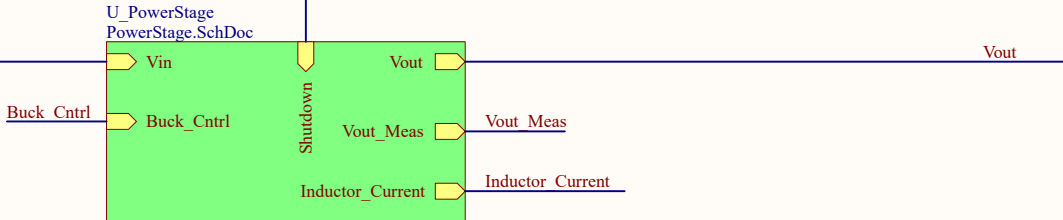
D



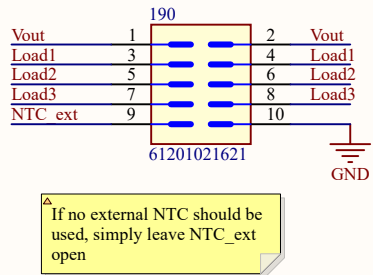
Nominal Input Voltage: 12 - 20 V
Absolute Maximum: -60 V to +60 V



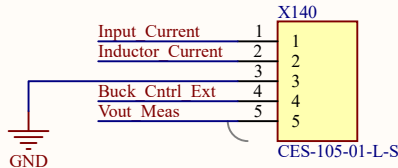
Nominal Output Voltage: 0 - 40 V
(shutdown at 45 V, clamping at 50 V)



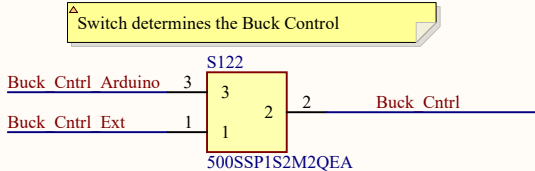
Load resistor connection



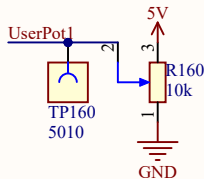
Control Pinheader



Buck Control Switch



User Potentiometer



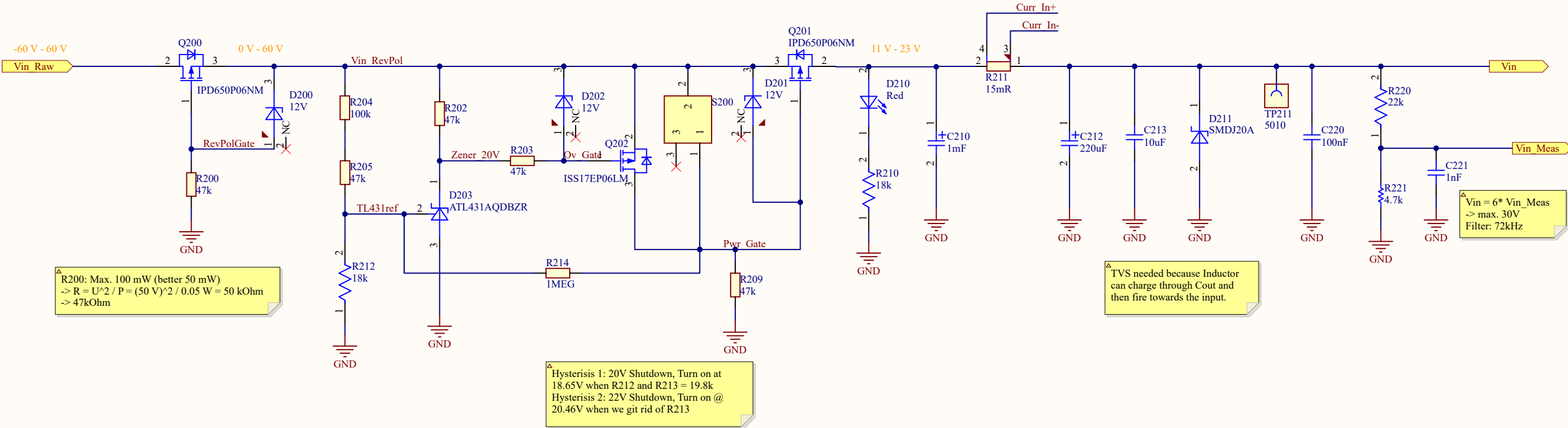
Infineon Technologies AG
IFAG DES SDF SCS EPE
Am Campeon 1-12 - 85579 Neubiberg - Germany

Title Learning Platform for Switching Regulators			
Variant Default_Assembly	Approved <Appr.>		
Size: A3	Document Name 02_Top_Level.SchDoc	Rel. Adrian	Rev. K410
Author: Adrian Keil			
Date: 06-Apr-25 Time: 6:12:18 PM		SVN Revision: Not in version control	
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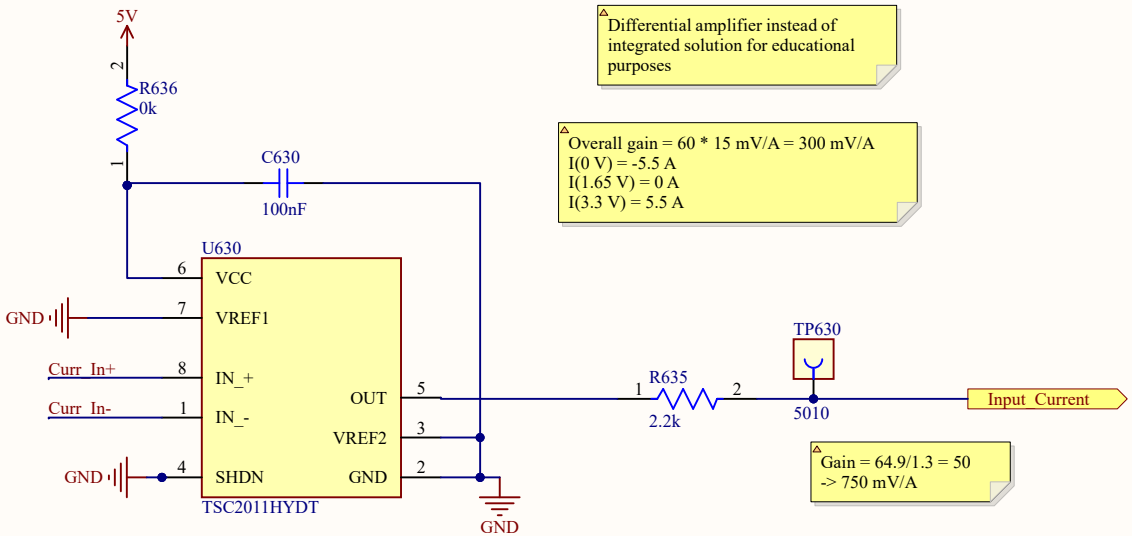
Reverse-Polarity Protection

Over-Voltage Protection

Voltage Measurement

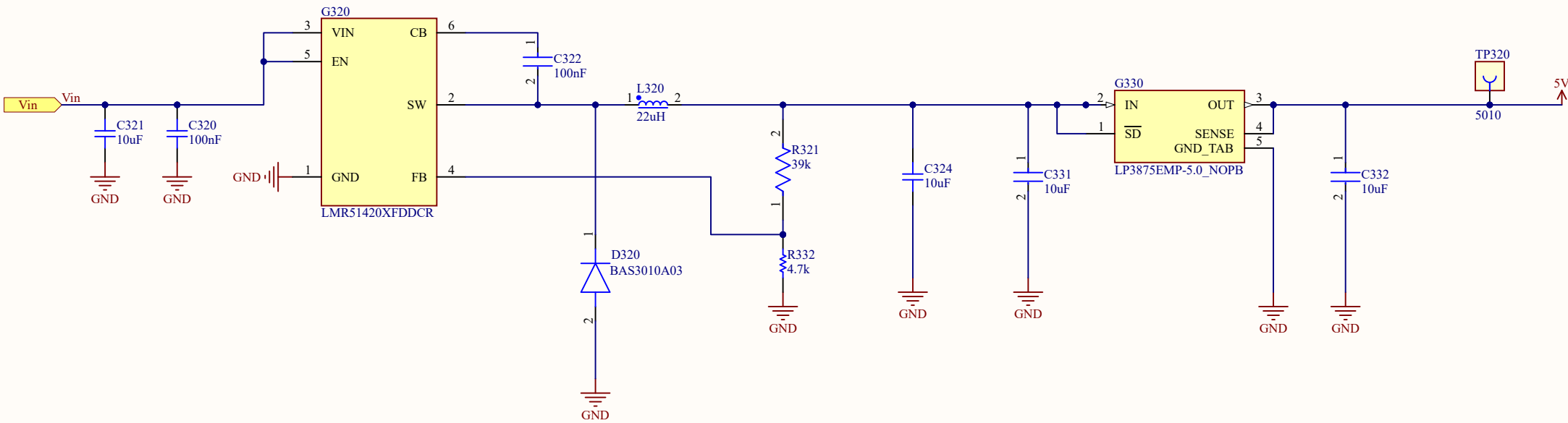


Input Current Measurement



5.6 V Buck Converter

5V LDO

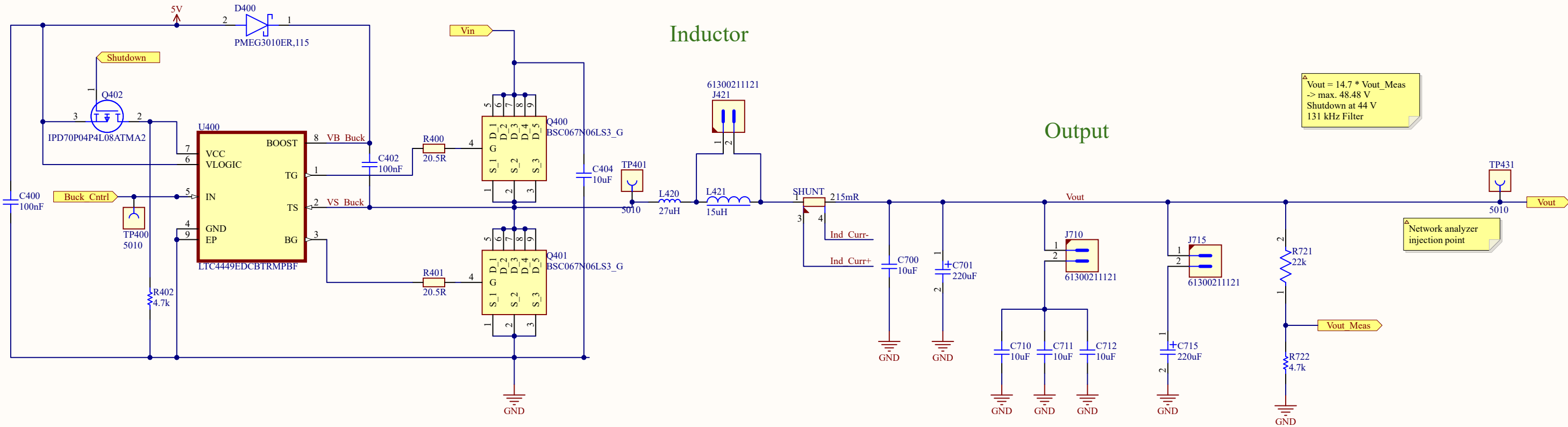


PowerStage.SchDoc

Gate-Driver/ Switching

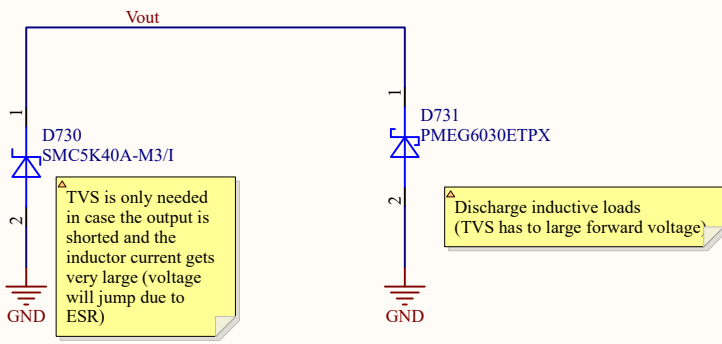
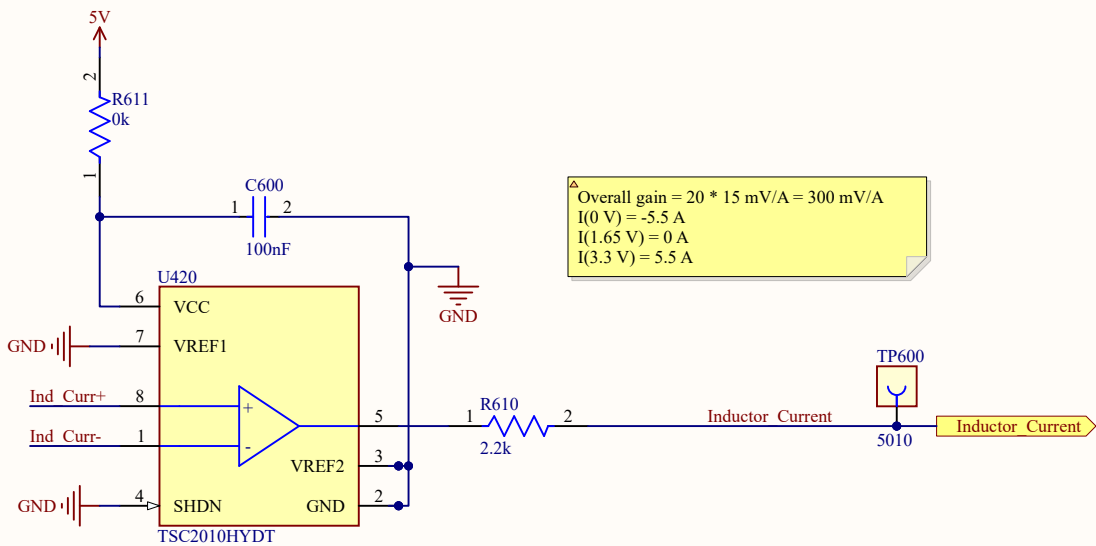
Inductor

Output

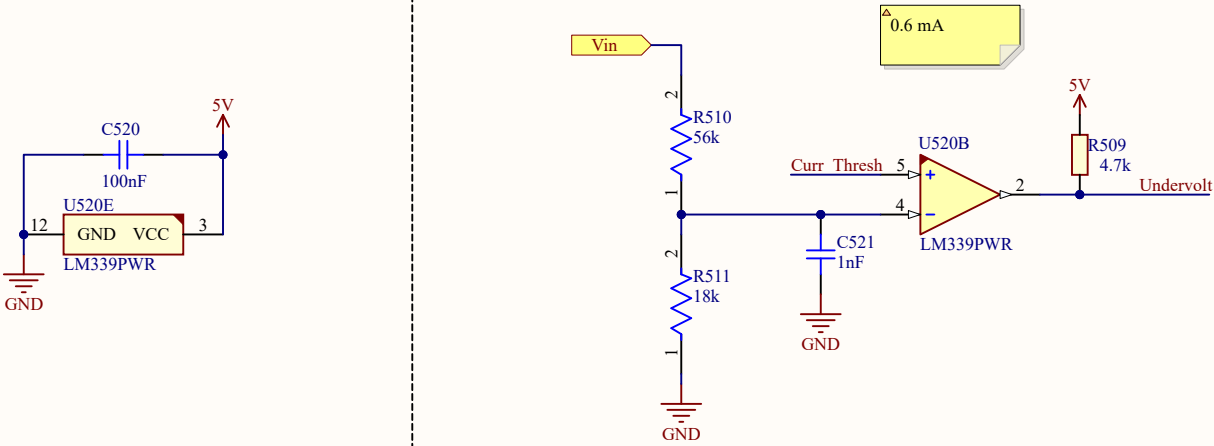


Inductor Current Measurement

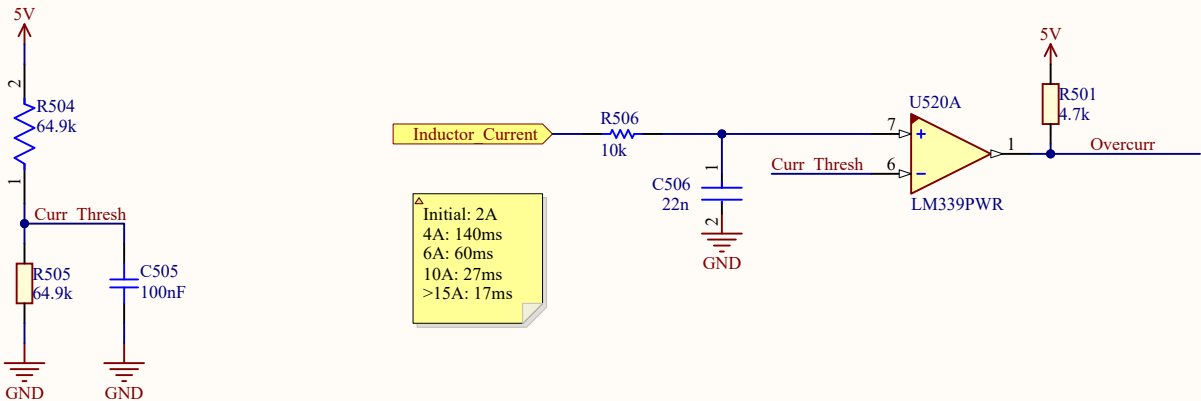
TVS Diodes



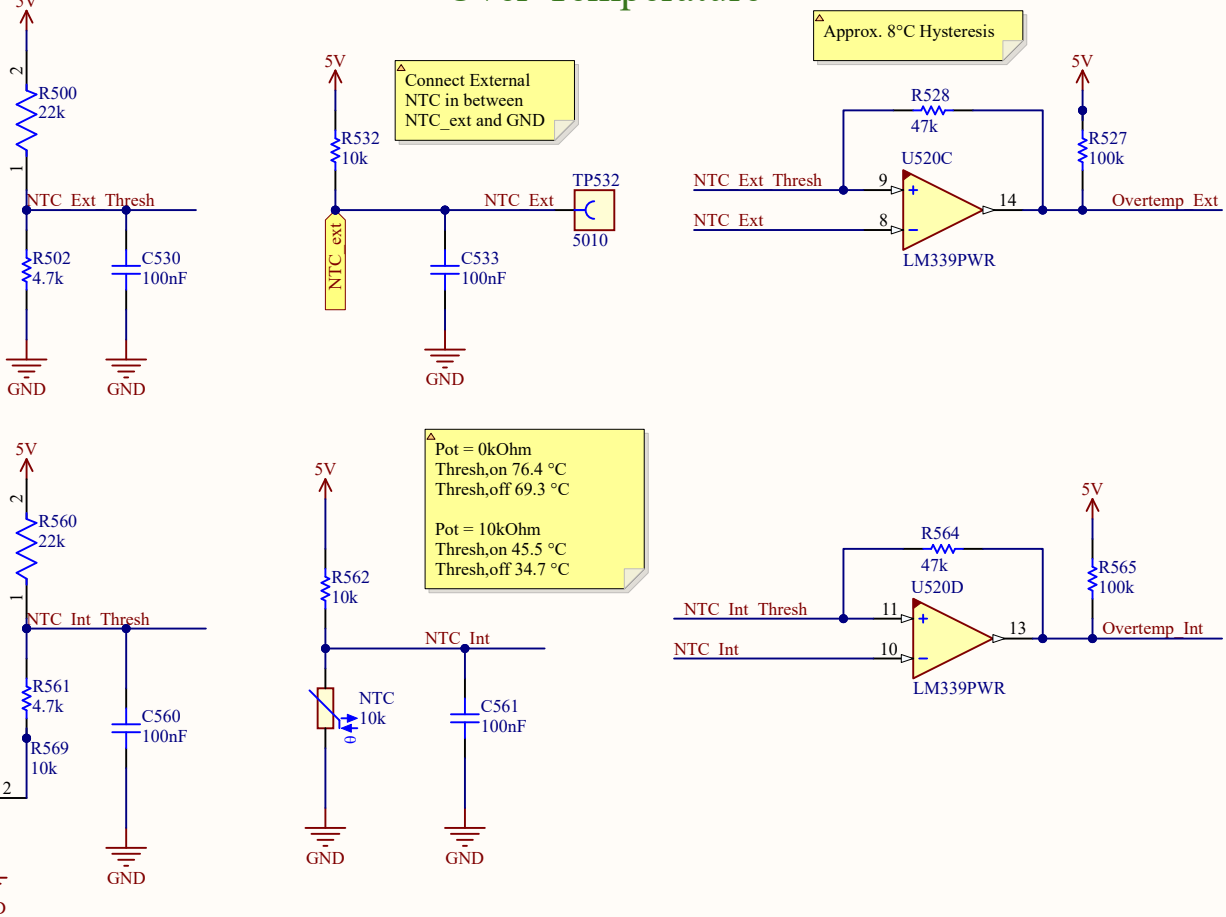
Under-Voltage



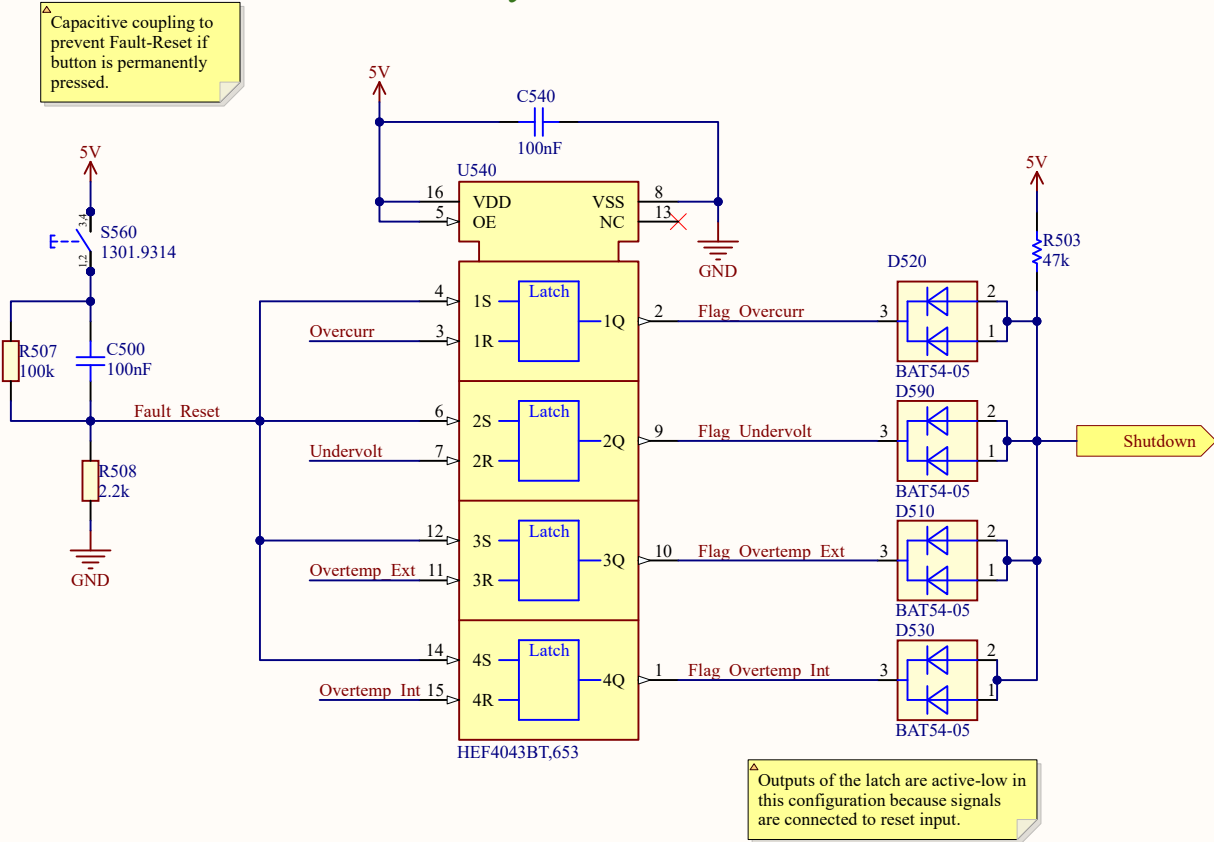
Over-Current



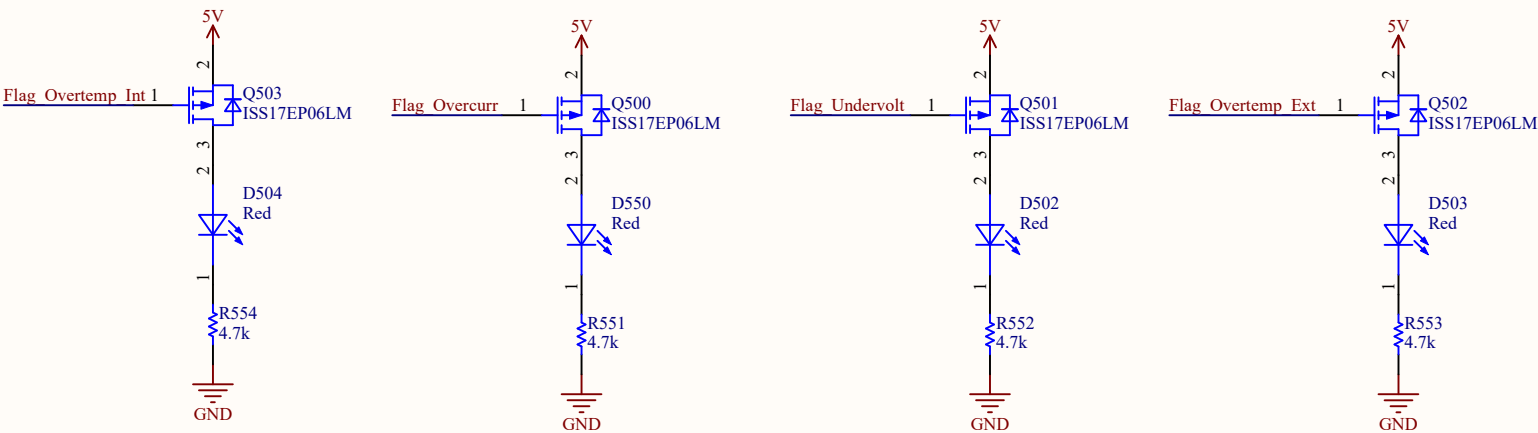
Over-Temperature

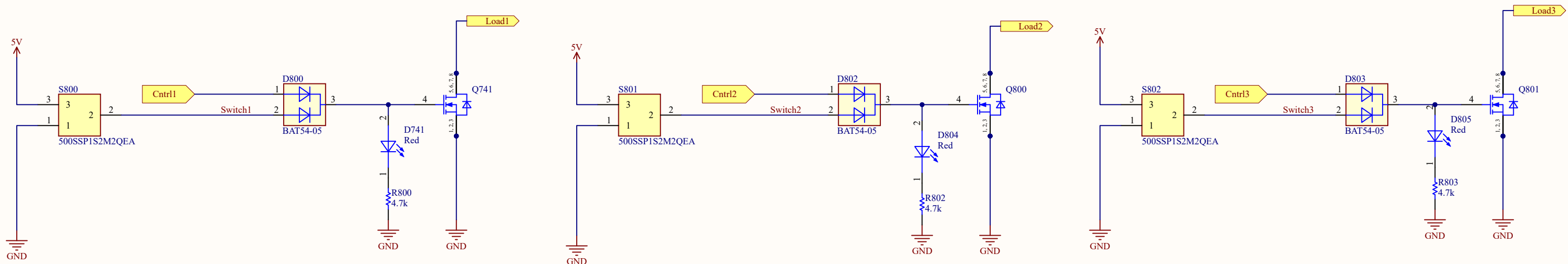


Safety-Latch

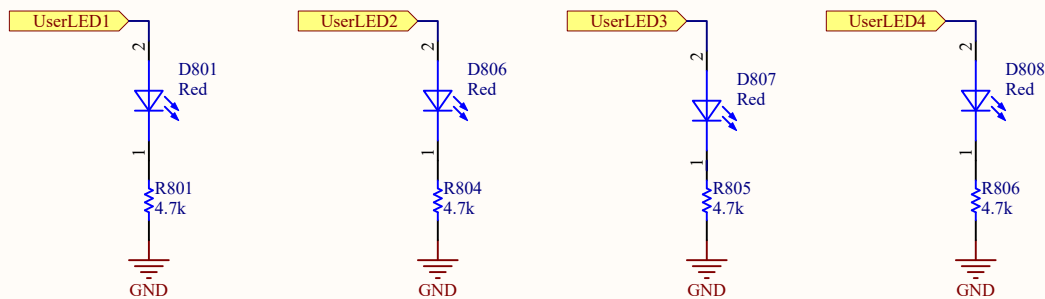


Status-LEDs





User LEDs



User Buttons and Switch

