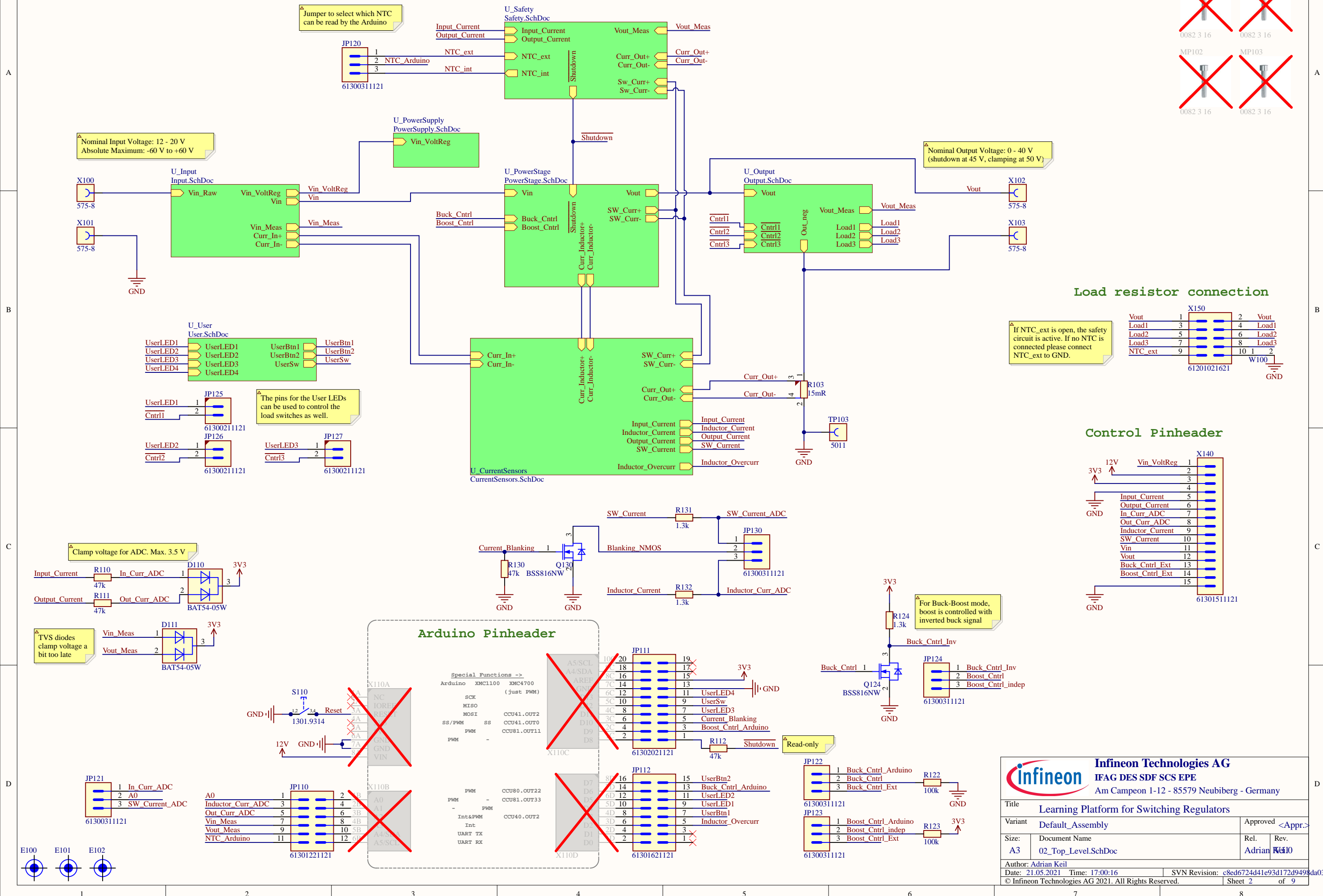




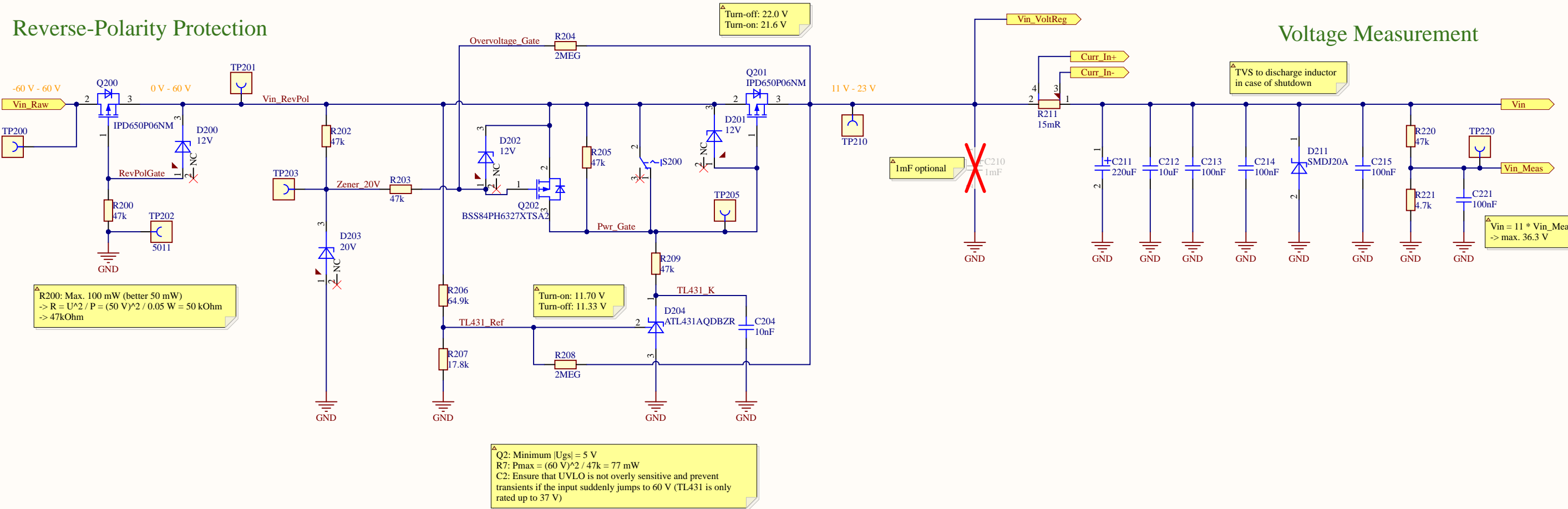
## 02\_Top\_Level.SchDoc



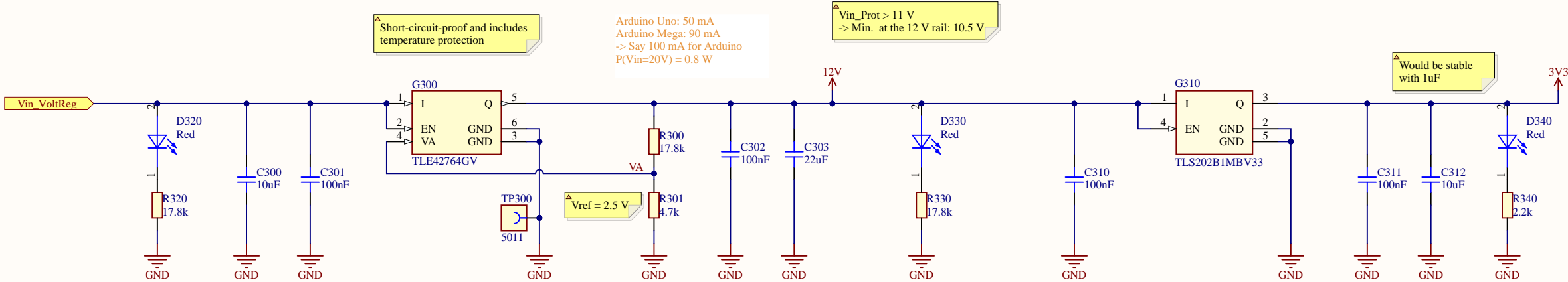
Over-Voltage Protection and UVLO

Reverse-Polarity Protection

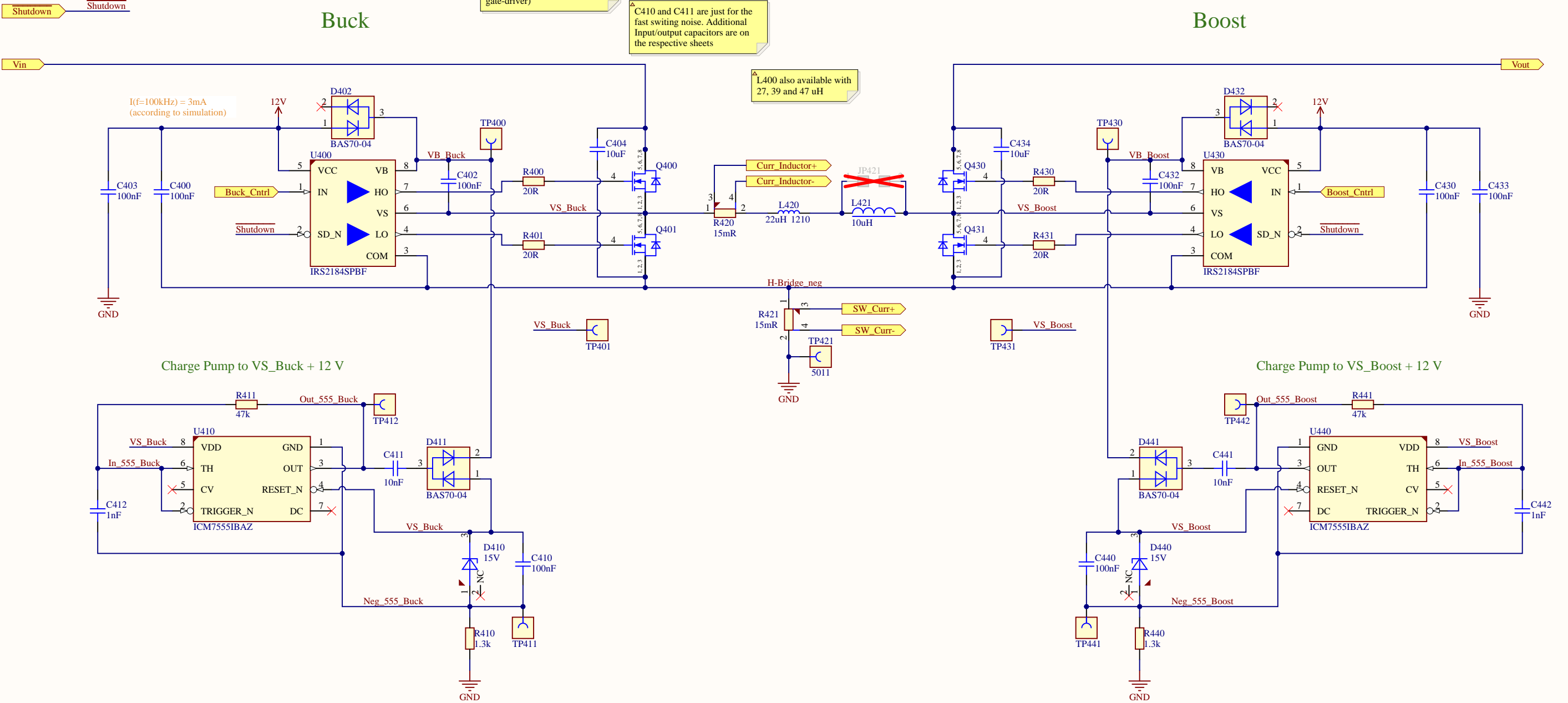
Voltage Measurement



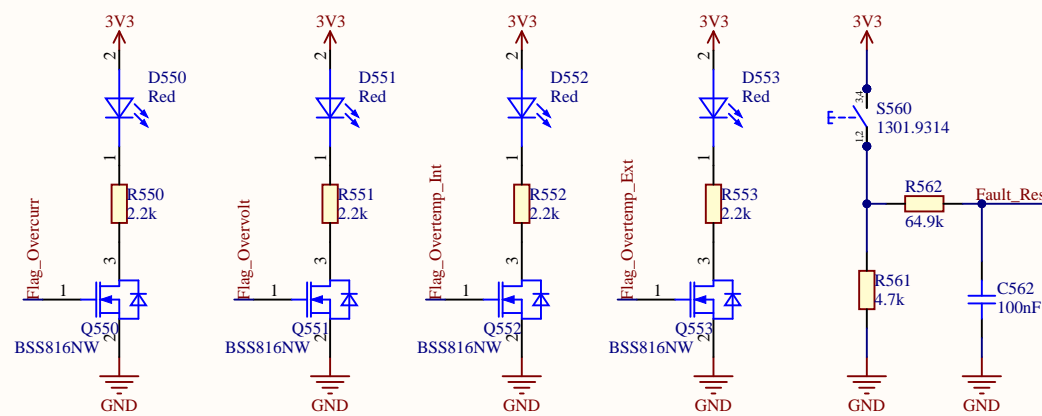
PowerSupply.SchDoc



PowerStage.SchDoc

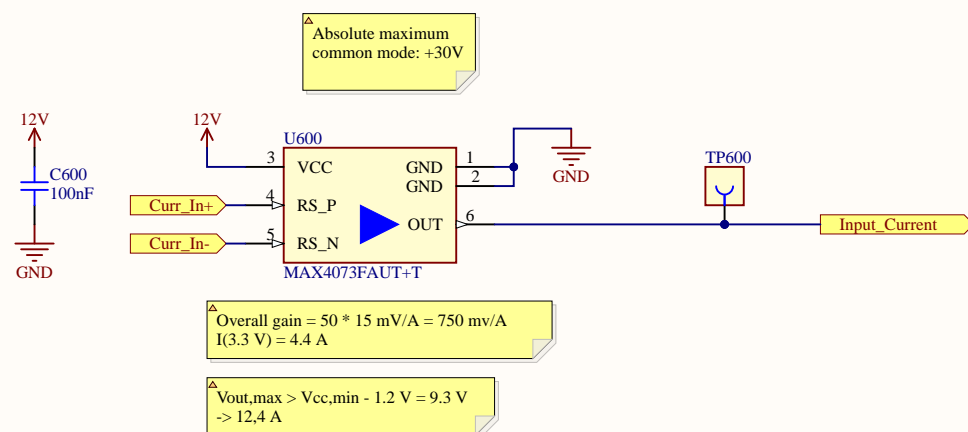


**Safety.SchDoc**



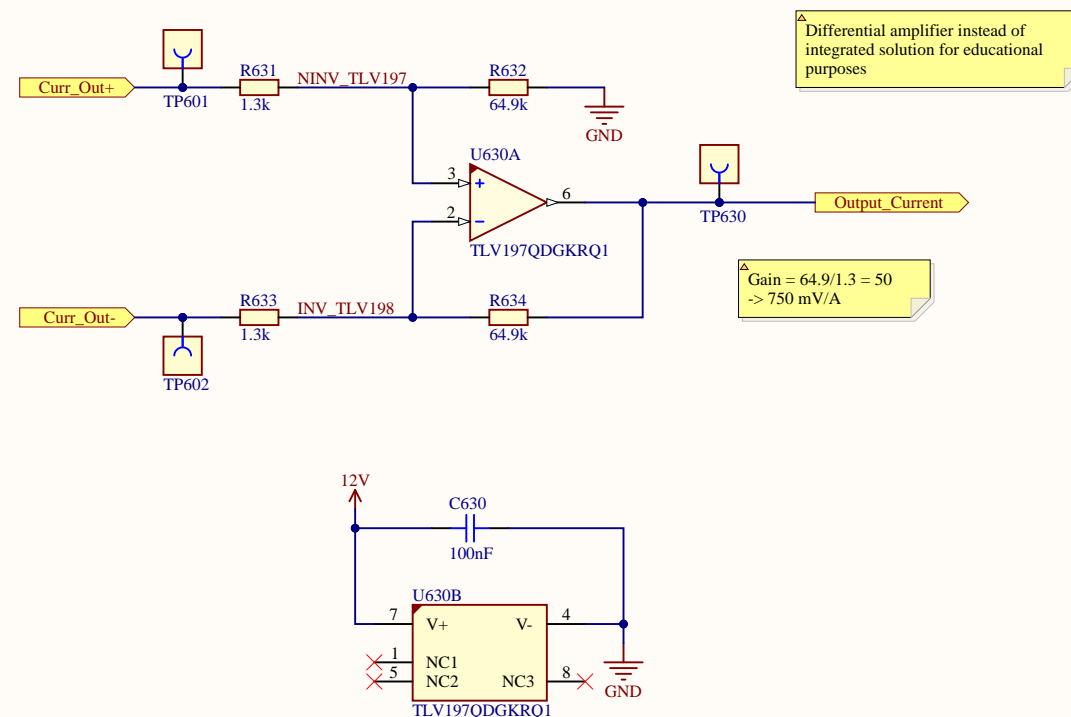
Initial approach of two selectable sense resistors is not worth it (experiment can be easily done in the simulation) and I don't want choices like this in the safety path

## Input Current

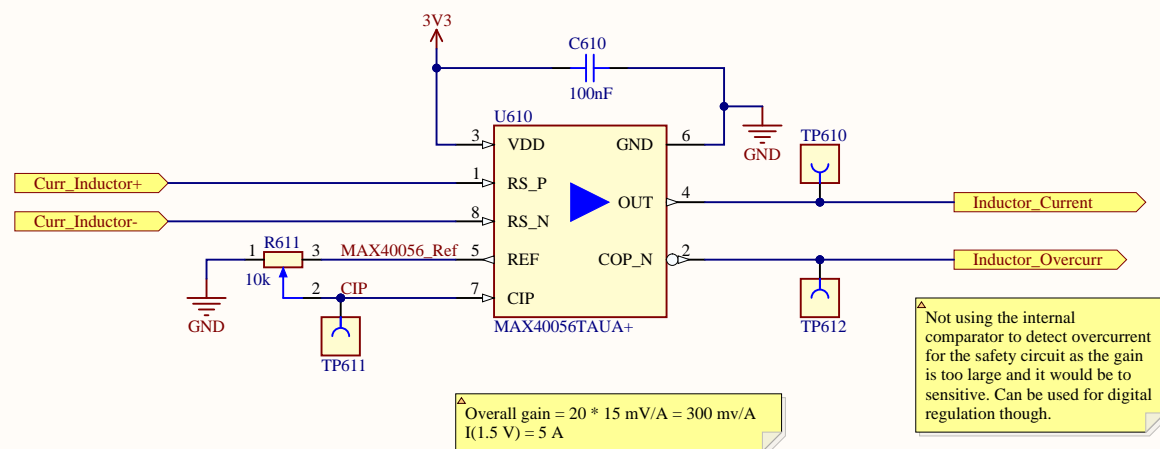


## CurrentSensors.SchDoc

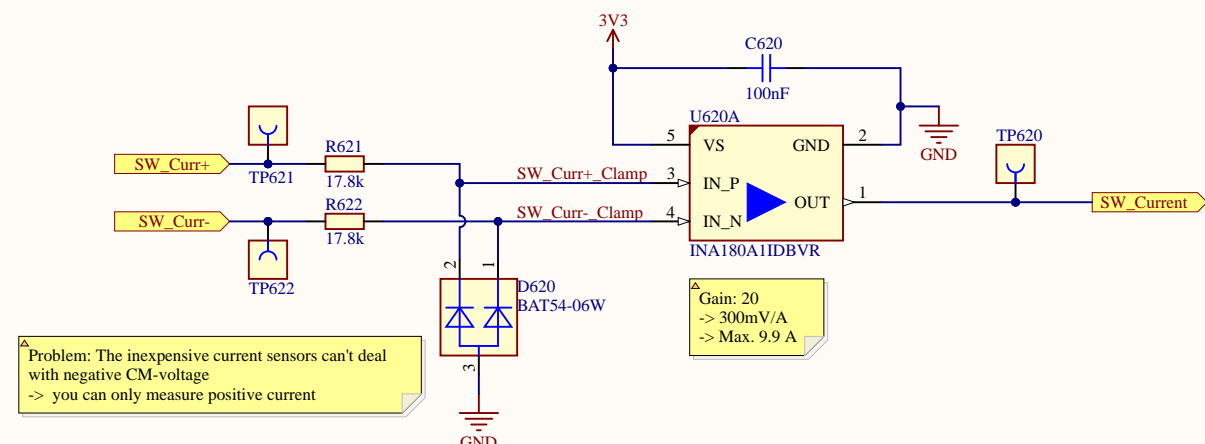
## Output Current



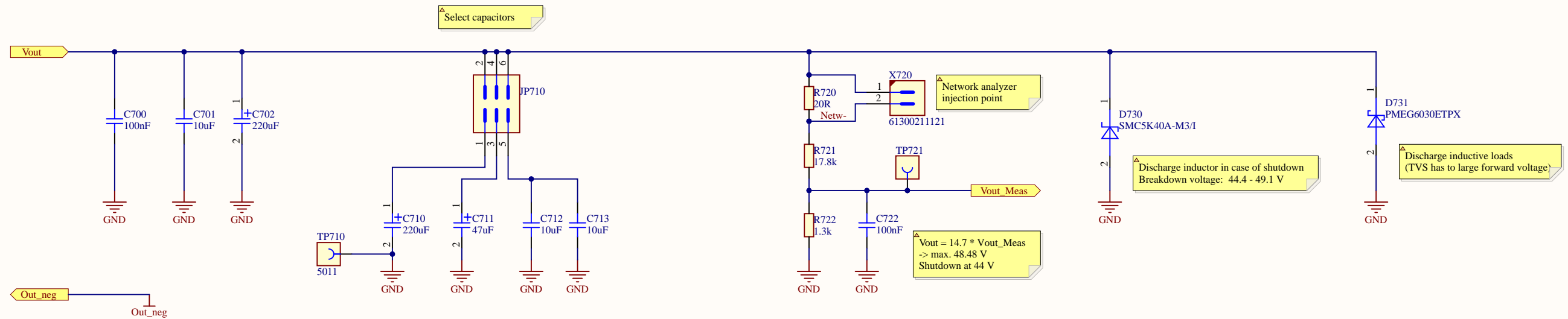
## Inductor Current



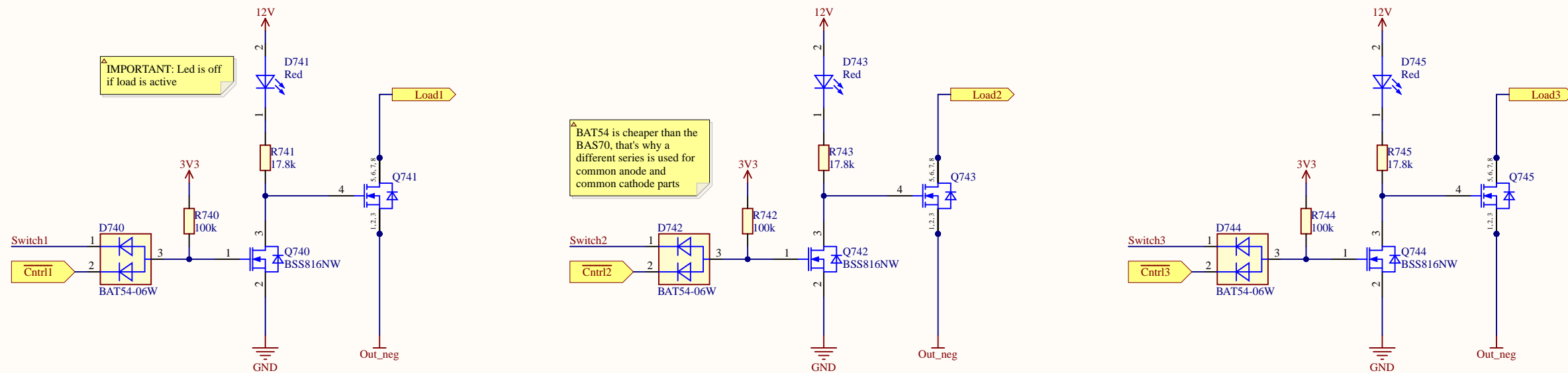
## Switch Current



**Output.SchDoc**



## Output Stages



## Output Signal Switches

