

Introduction to the ADALM-PLUTO SDR, Linux's IIO, and Open-Source Toolchains

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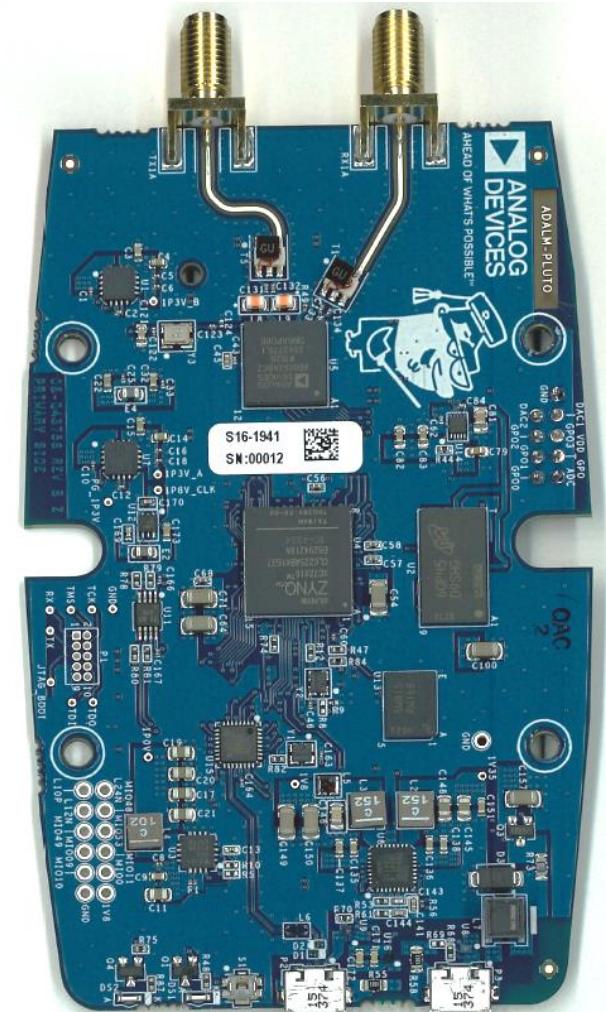
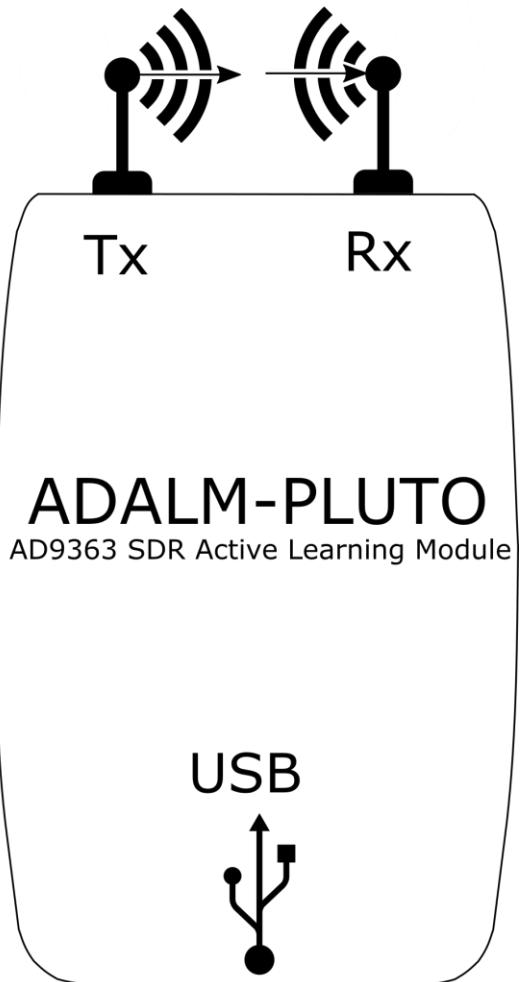
ADRIAN SUCIU



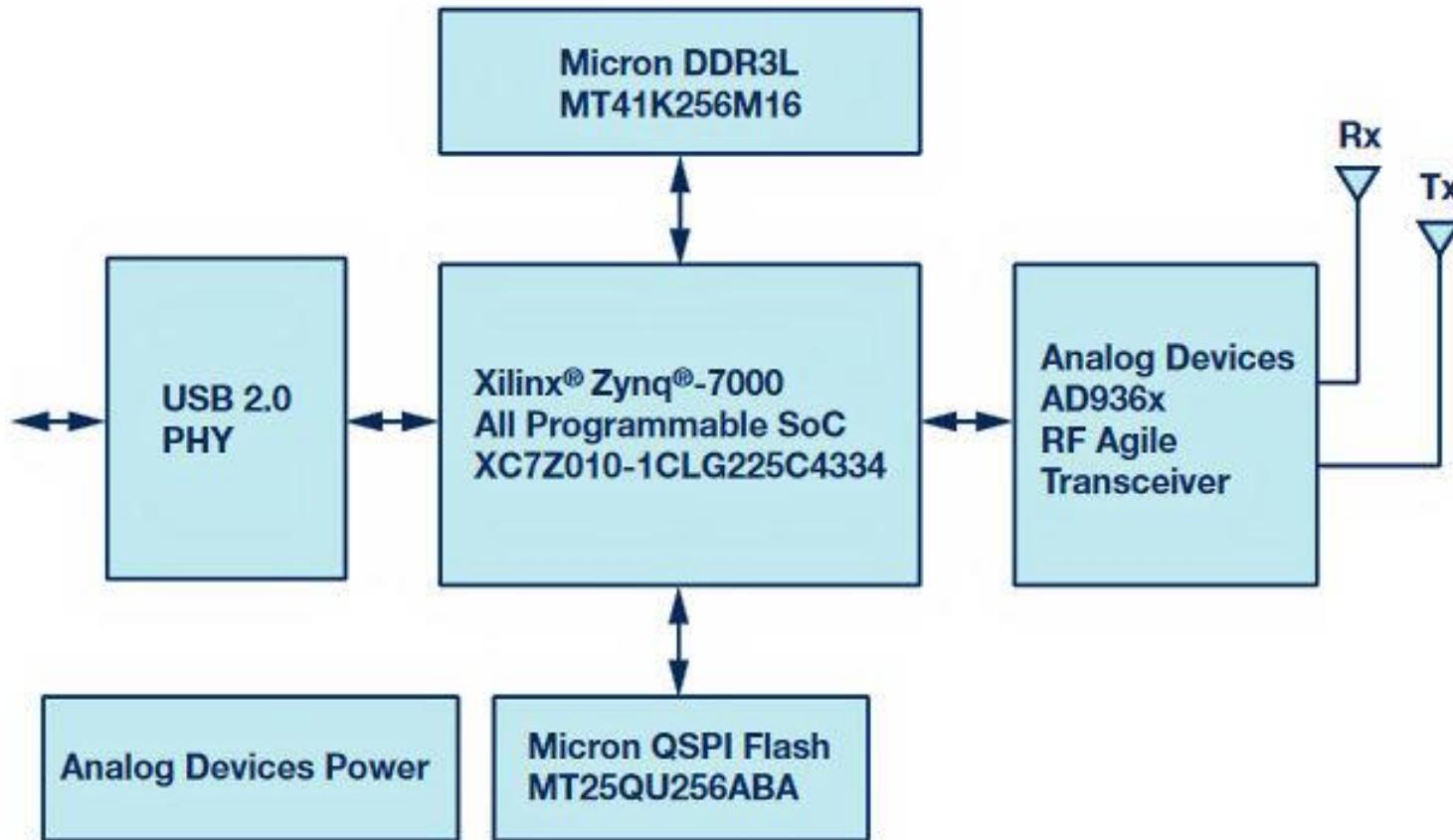
Labs

- Available at
 - <https://github.com/sdrforengineers/LabGuides/>

ADALM-PLUTO (PlutoSDR)



PlutoSDR: Layout

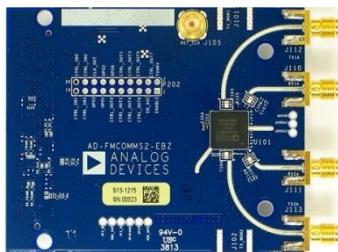


ADI (and other) General Purpose SDR Boards

Designed, manufactured,
tested and sold by ADI

AD-FMCOMMS2

- AD9361 Integrated
- 2 x Rx, 2 x Tx
- **2.2 GHz – 2.6GHz tuning range**
- 200kHz - 56 MHz channel bandwidth
- Shipping Now!

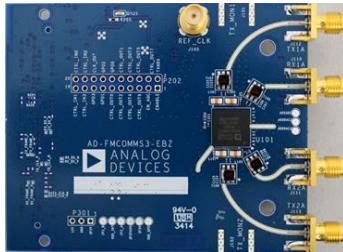


Power, Transceiver
Projects for :

- ZedBoard,
- Xilinx ZC702 & ZC706

AD-FMCOMMS3

- AD9361 Integrated
- 2 x Rx, 2 x Tx
- **70 MHz – 6GHz tuning range**
- 200kHz - 56 MHz channel bandwidth
- Shipping Now!

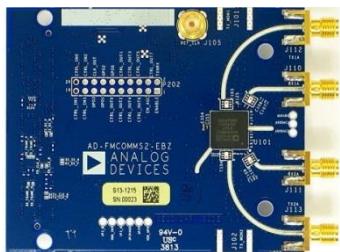


Power, Transceiver
Projects for :

- ZedBoard,
- Xilinx ZC702 & ZC706

AD-FMCOMMS4

- **AD9364 Integrated**
- **1 x Rx, 1 x Tx**
- 70 MHz – 6GHz tuning range
- 200kHz - 56 MHz channel bandwidth
- Shipping Now!

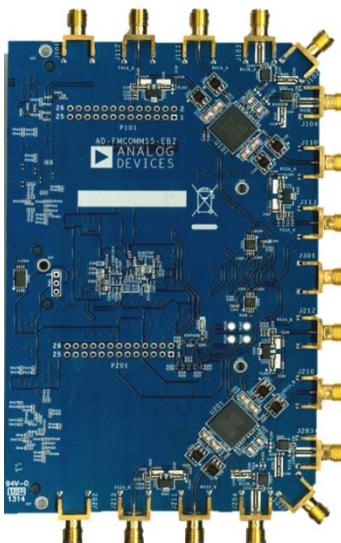


Power, Transceiver
Projects for :

- ZedBoard,
- Xilinx ZC702 & ZC706

AD-FMCOMMS5

- **2 x AD9361 Integrated**
- **4 x Rx, 4 x Tx**
- **Synchronized RF**
- 70 MHz – 6GHz tuning range
- 200kHz - 56 MHz channel bandwidth
- Shipping Now!



Power, Transceiver, PLL,
LNA
Projects for ZC706 only

ADRV9371-[NW]

- **1 x AD9371 Integrated**
- **2 x Rx, 2 x Tx, 2 x Obs, 1 x Sniffer**
- 300 MHz – 6GHz tuning range
- Shipping Now!



Power, Transceiver, PLL,
LNA
Projects for Xilinx and
Intel/Altera

ARRADIO

- AD9361 Integrated
- 2 x Rx, 2 x Tx
- **2.2 GHz – 2.6GHz tuning range**
- 200kHz - 56 MHz channel bandwidth
- Shipping Now!



Power, Transceiver
Projects for :
Altera SOCKIT

Assistance and some
qualification done by ADI

Evaluating the AD9361

► Hardware

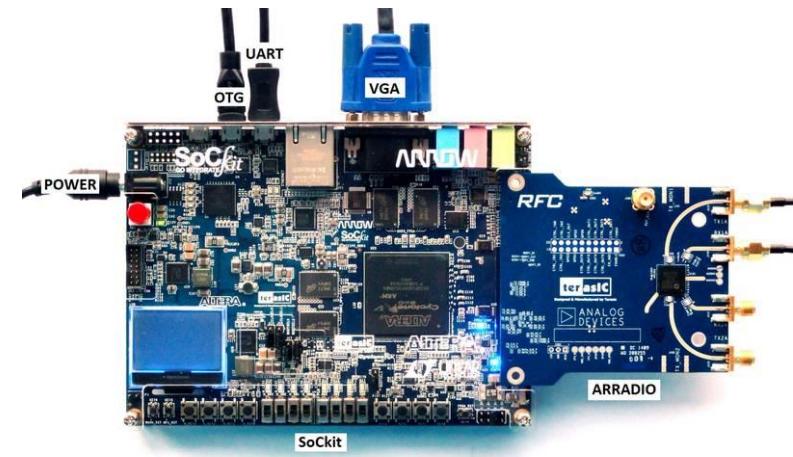
- AD-FMCOMMS2-EBZ (AD9361)
 - Narrow RF Tuning Range
- AD-FMCOMMS3-EBZ (AD9361)
 - Wide RF Tuning Range
- AD-FMCOMMS4-EBZ (AD9364)
- Works with:
 - Xilinx Zynq based solutions, including ZC706 (shown), ZC702, and Zedboard
 - Xilinx Kintex (KC705) and Virtex (VC707)

► Software

- Device drivers
 - Linux and/or No-OS
- FPGA HDL
- IIO scope
 - Data visualization application
 - Graphical configuration application



Xilinx ZC706 + FMCOMMS2



Arrow SoCKit + ARRADIO



RFSOM

RFSOM SD2 (7035 / AD9361) : \$1095

- Processor
 - Dual Core ARM Cortex A9 (800MHz each)
 - L1 cache : 32 KB Instruction, 32 KB Data
 - L2 cache : 512 KB

- FPGA
 - Kintex-7 Fabric
 - 275K Logic Cells
 - ~4.1M Asic gates
 - 17.6 Mb Block RAM
 - 900 DSP Slices

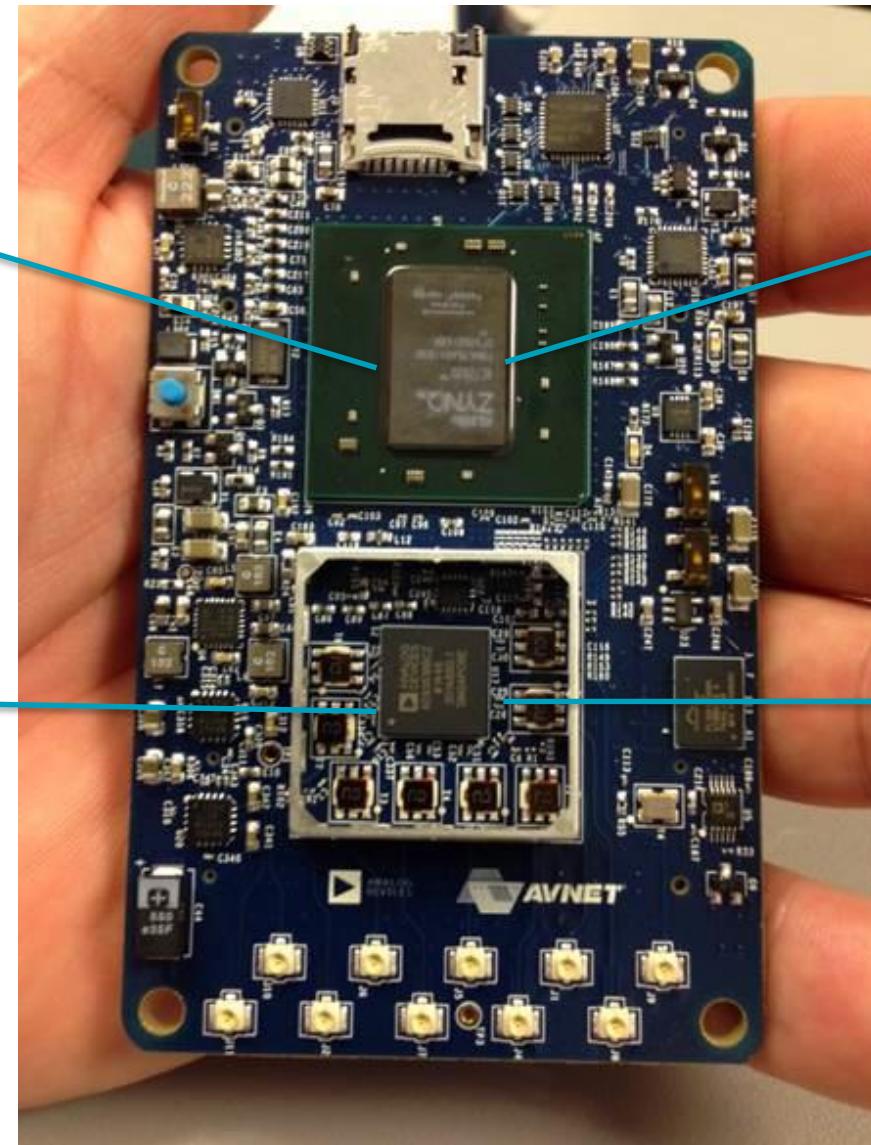
- 1 Gbyte DDR3L
- 32 Mbyte SPI Flash
- MicroSD Card (lockable)

- Radio
 - 200kHz - 56 MHz RF bandwidth
 - 128-tap FIR Filters for equalization
 - 70 – 6000 MHz tuning range
 - 2 Rx, 2 Tx, 2 Tx Monitor

Xilinx 7035

- 10/100/1000 Ethernet (MAC + Phy)
- USB 2.0 (OTG Controller + Phy)
- Low power
 - sub 5W at full data rates
- Full Linux based reference design
- Fully integrated and tested system
- # Pins of I/O

ADI AD9361



RFSOM SDR1 (7020 / AD9364) : \$549

- Processor
 - Dual Core ARM Cortex A9 (800MHz each)
 - L1 cache : 32 KB Instruction, 32 KB Data
 - L2 cache : 512 KB

- FPGA
 - Artix-7 Fabric
 - 85K Logic Cells
 - ~1.3M Asic gates
 - 4.9 Mb Block RAM
 - 220 DSP Slices

Xilinx 7020

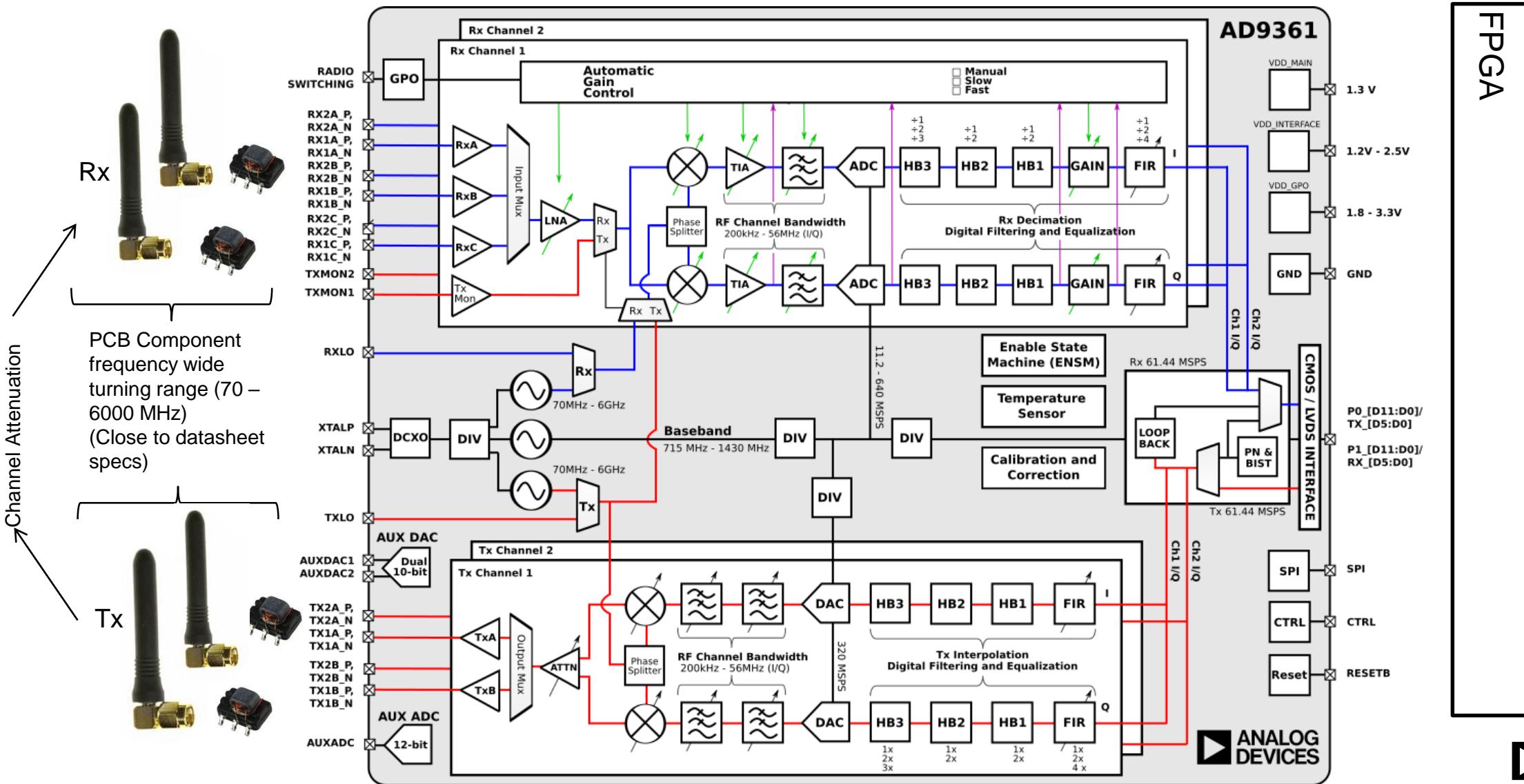
- 1 Gbyte DDR3L
- 32 Mbyte SPI Flash
- MicroSD Card (lockable)

- Radio
 - 200kHz - 56 MHz RF bandwidth
 - 128-tap FIR Filters for equalization
 - 70 – 6000 MHz tuning range
 - 1 Rx, 1 Tx, 1 Tx Monitor

ADI AD9364

- 10/100/1000 Ethernet (MAC + Phy)
- USB 2.0 (OTG Controller + Phy)
- Low power
 - sub 5W at full data rates
- Full Linux based reference design
- Fully integrated and tested system
- # Pins of I/O

AD9361: Transceiver



FPGA

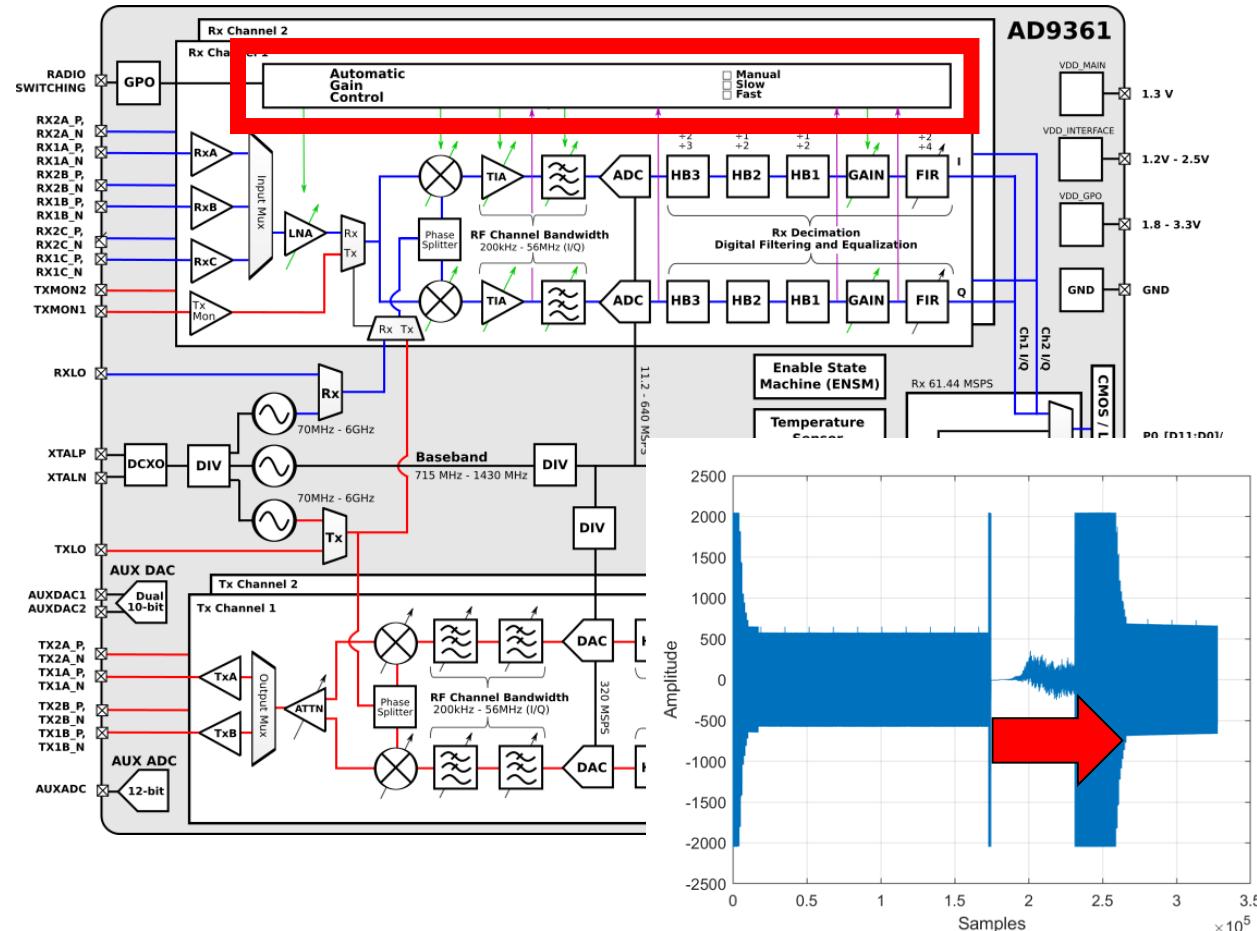


AHEAD OF WHAT'S POSSIBLE™

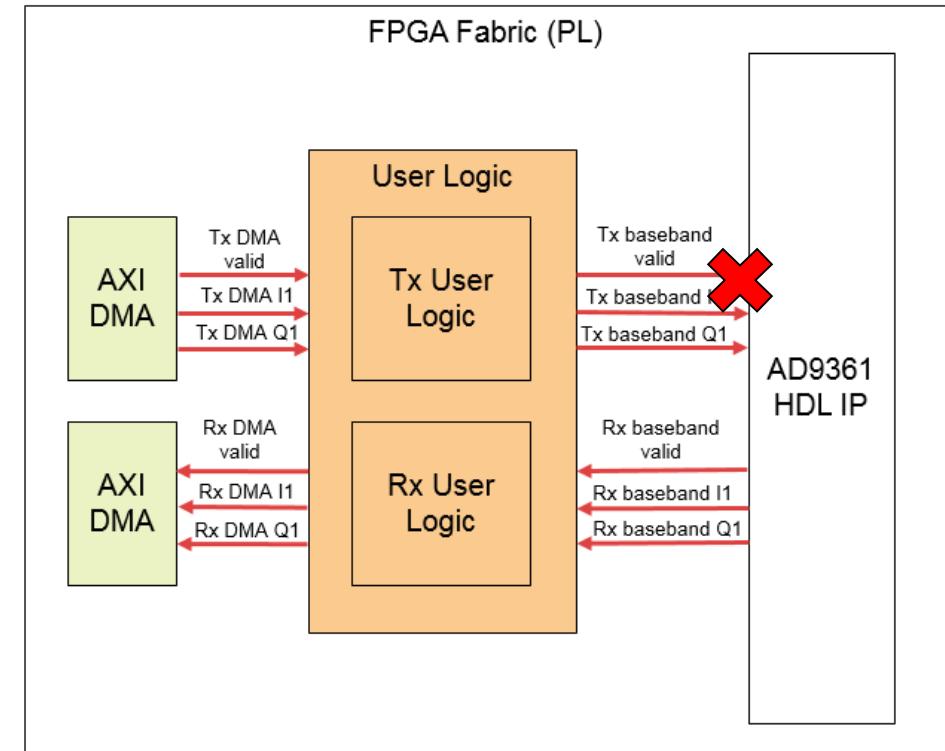
Demo: CAUTION! No Front-End Filters....

Using the AD936X

Changing AGC



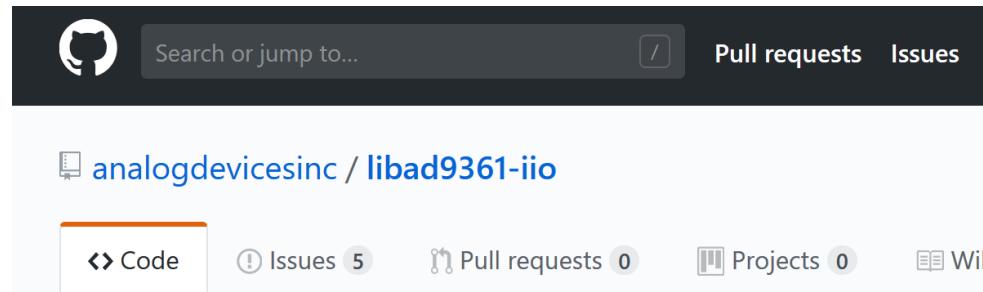
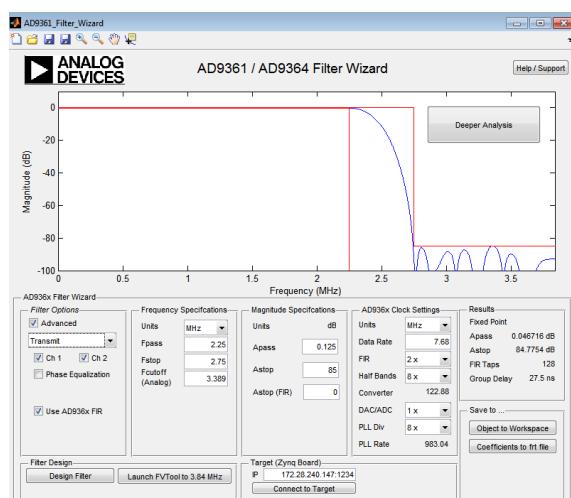
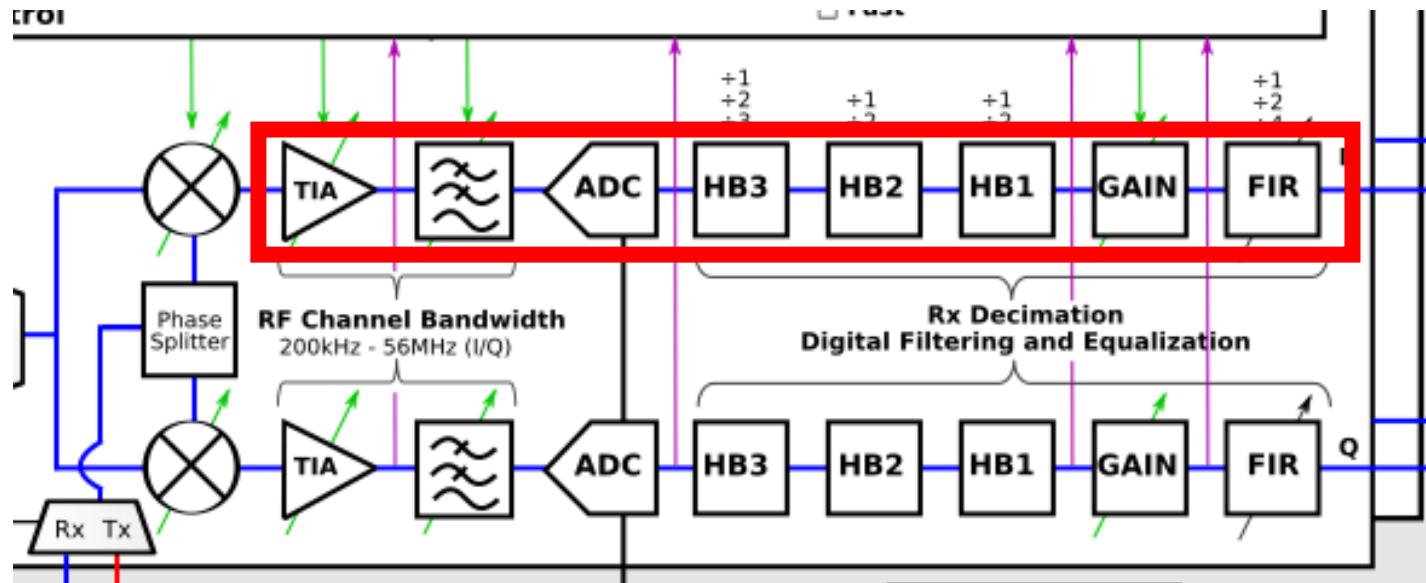
Constant TX



► FIRMWARE VERSION

The Data Path and Filters

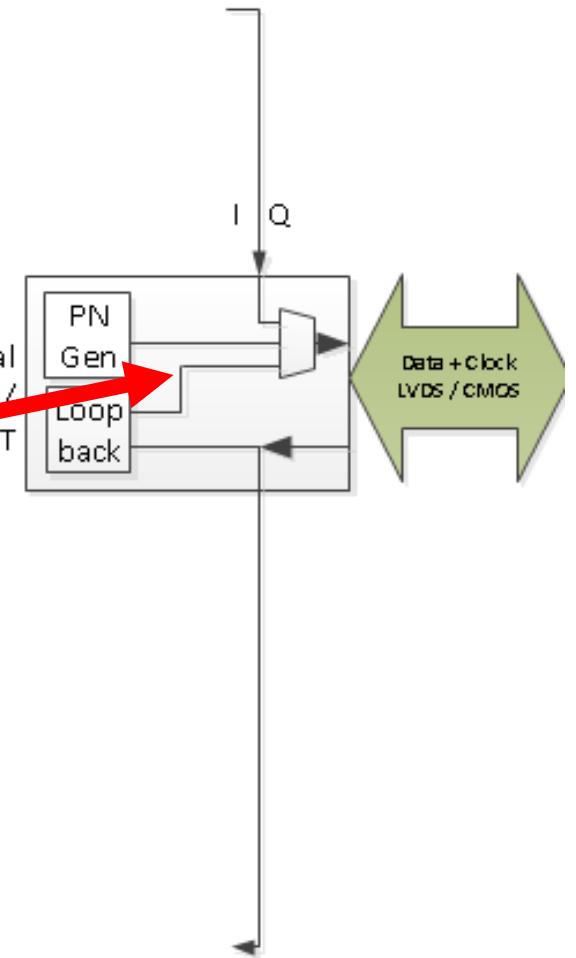
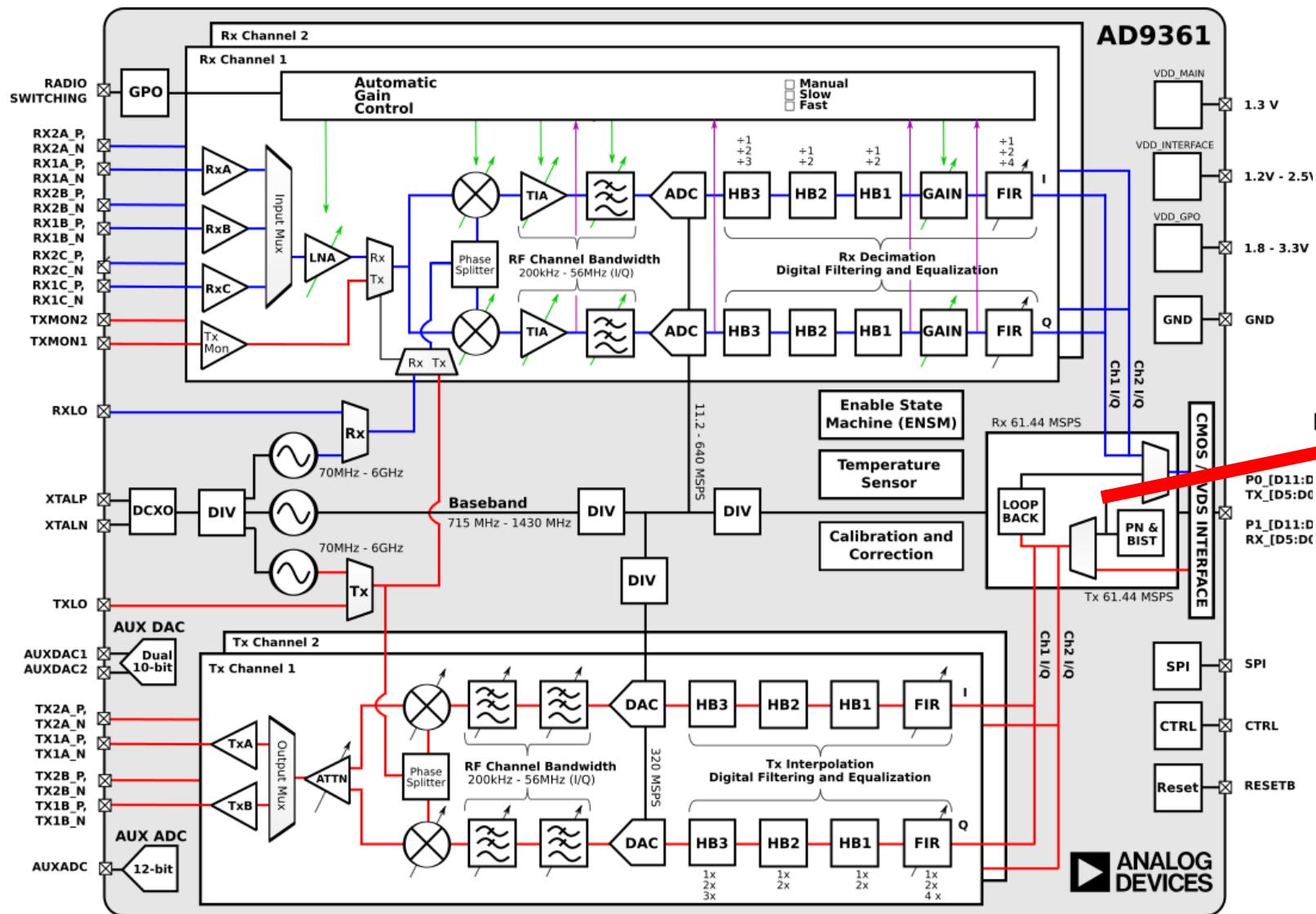
- Half bands, FIR, and clocking determine data rate of Pluto
 - FIR needs to be loaded to go below 2.08 MHz
- Multiple options for loading FIR
 - Auto-filter options in gr-iio, IIO-Scope, and through libad9361
 - Filters can be custom created with AD9361 Filter Wizard
 - Filters can be custom created in C/C++ with libad9361



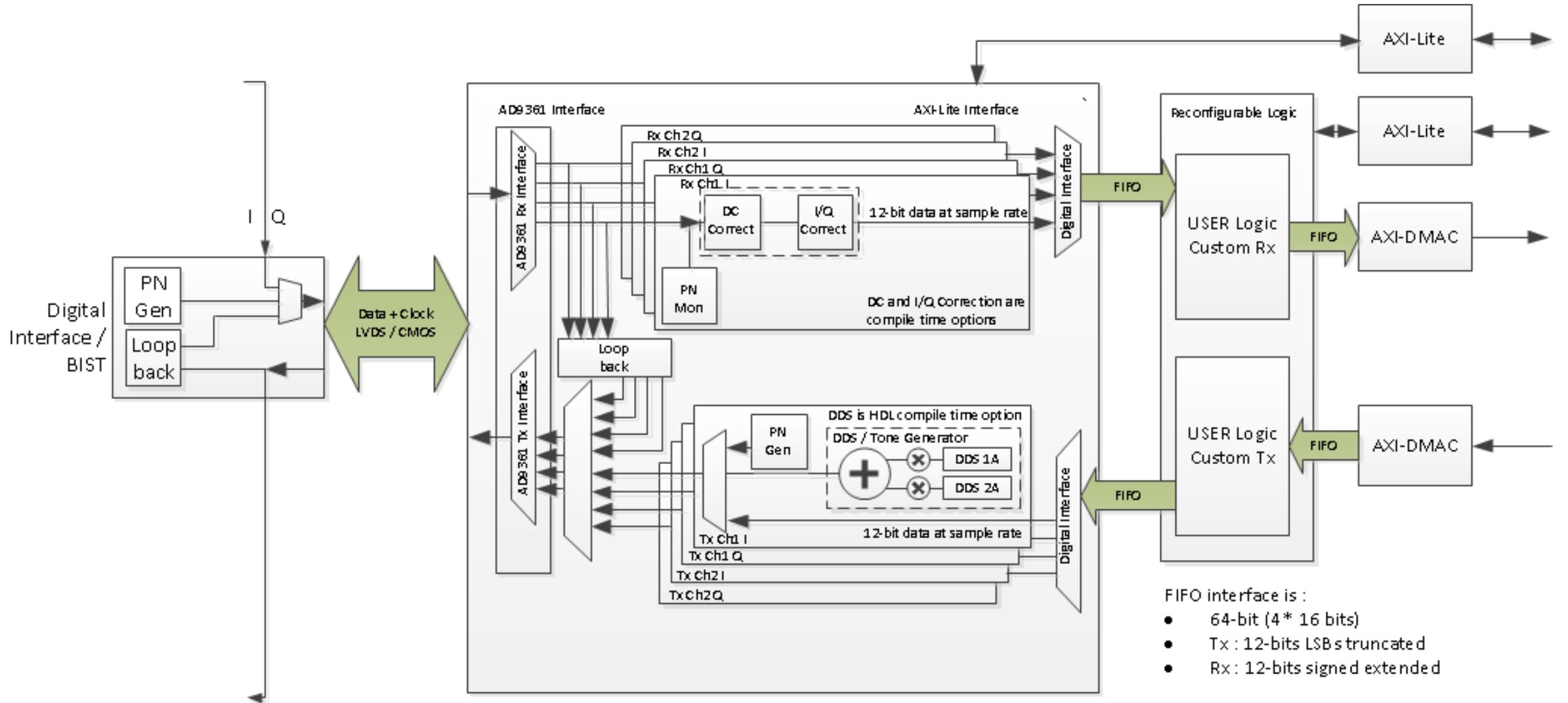
IIO AD9361 library for filter design and handling, multi-chip sync, etc.

iio ad9361 mcs fir plutosdr adalm-pluto adrv9361 Manage topic:

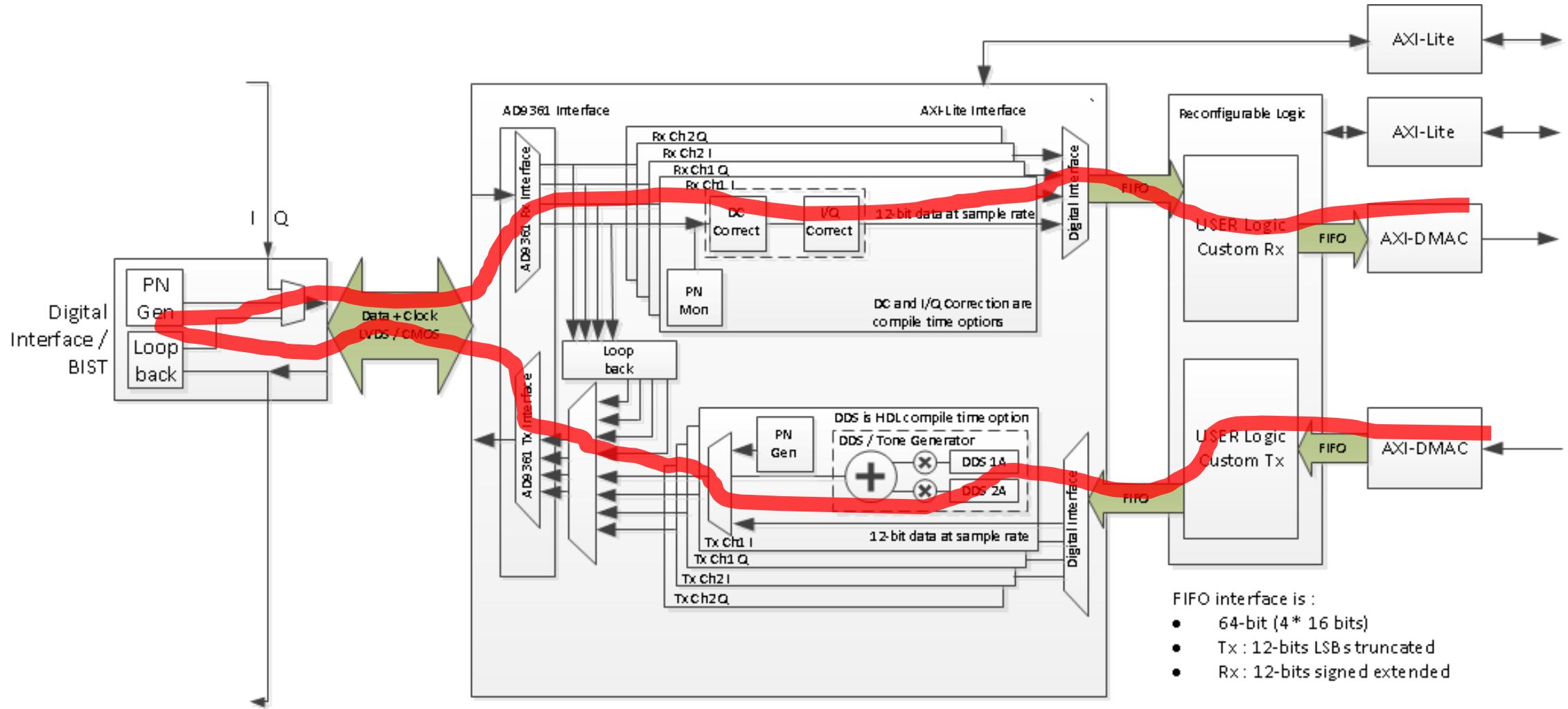
Features on the FPGA



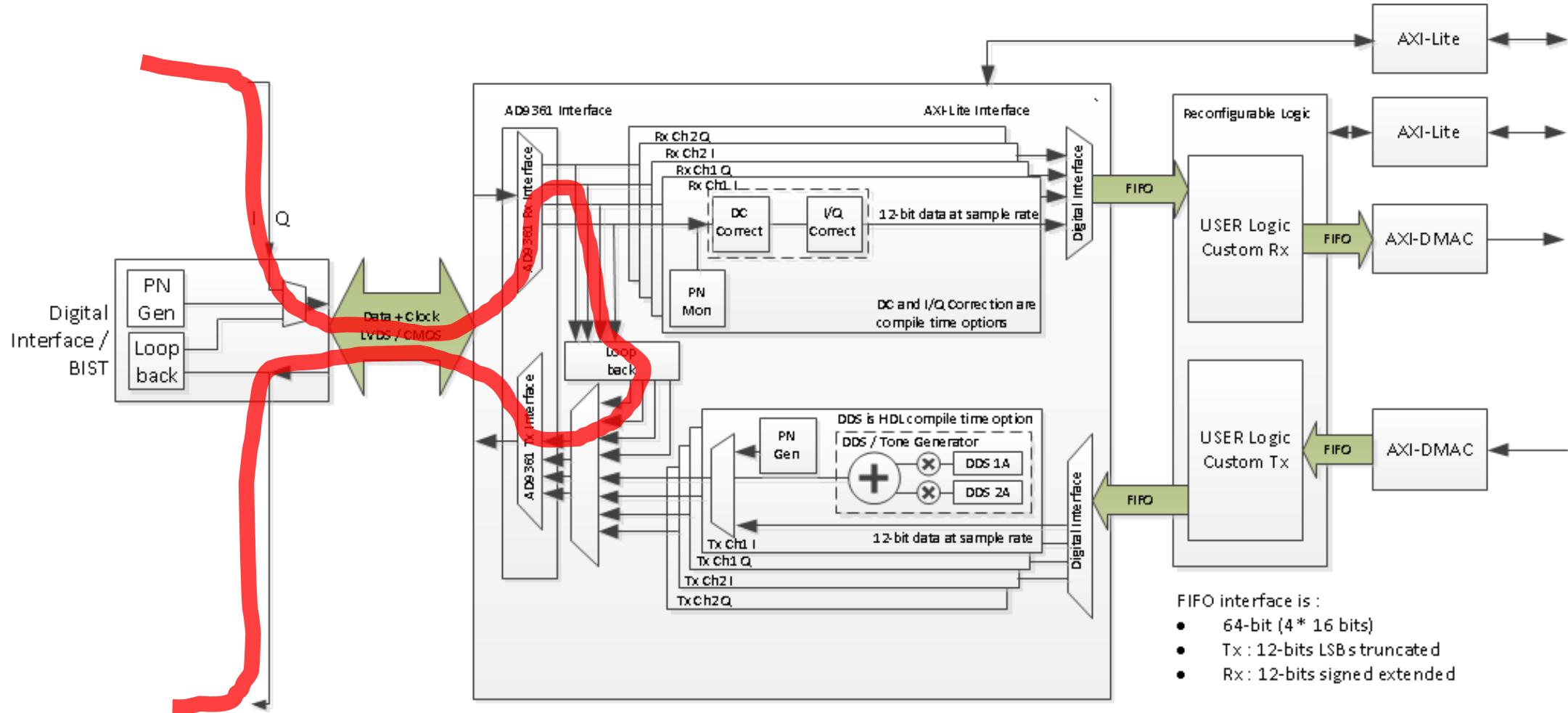
HDL: Digital Loopback



HDL: Digital Loopback



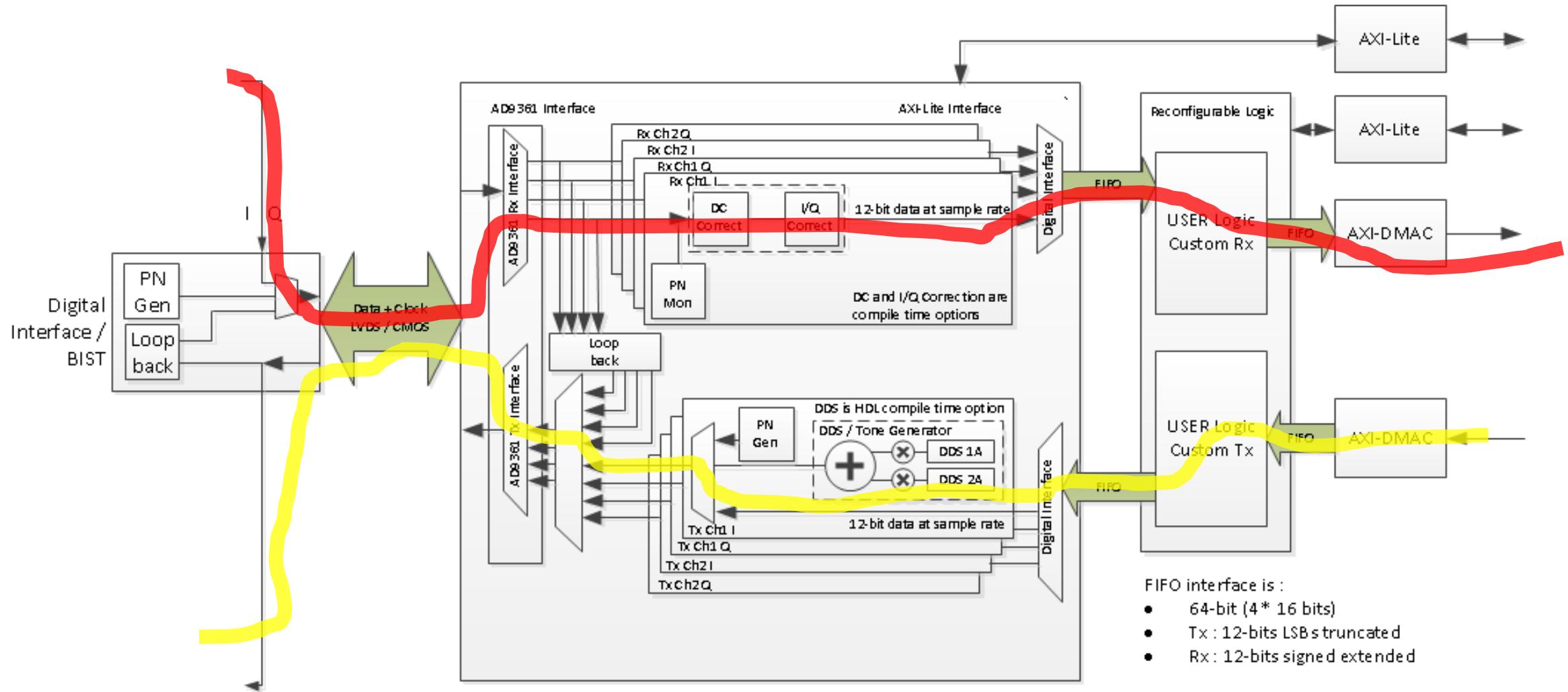
HDL: RF Loopback



FIFO interface is :

- 64-bit (4 * 16 bits)
- Tx : 12-bits LSBs truncated
- Rx : 12-bits signed extended

HDL: Tx/Rx Active



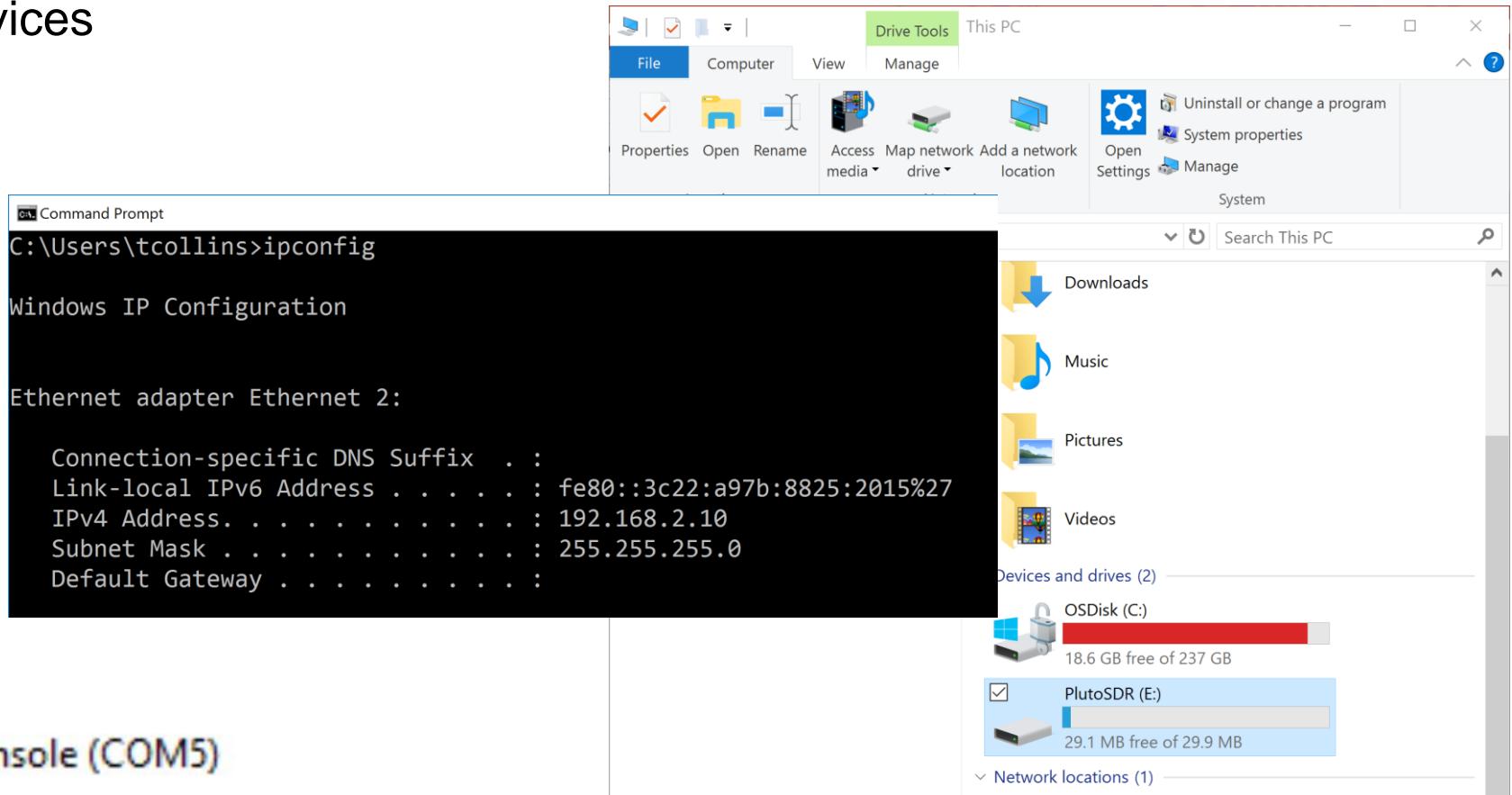


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Drivers and Firmware

Connecting With PlutoSDR

- Pluto enumerates three devices
 - Mass storage
 - Ethernet (RNDIS)
 - Serial



Drivers

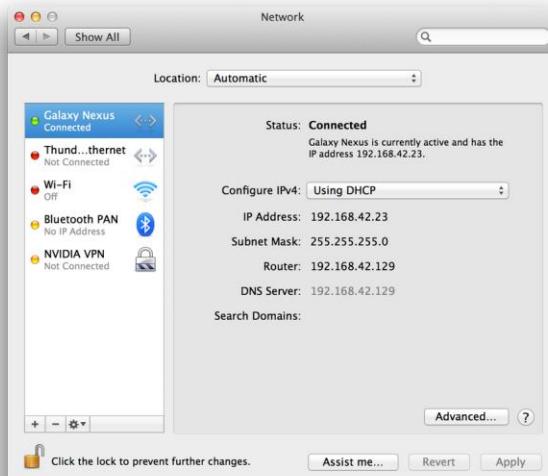
- ▶ Linux and Windows have all three options out of the box
- ▶ macOS requires HoRNDIS install for networking

Downloading and installing HoRNDIS

HoRNDIS is available in source form from its [project page on GitHub](#), and in binary form [on this site](#). For quick start instructions:

- Download [the appropriate binary package](#), and double-click on it in your Downloads folder. Follow the instructions in the installer.
- Assuming that the installation proceeds without errors, after it completes, connect your phone to your Mac by USB.
- Enter the settings menu on your phone.
- In the connections section, below Wi-Fi and Bluetooth, select “More...”.
- Select “Tethering & portable hotspot”.
- Check the “USB tethering” box. It should flash once, and then become solidly checked.

On some versions of OS X, a dialog box may pop up, prompting you to configure the device; follow its instructions. To verify that the device is connected,



analogdevicesinc / libiio

Code Issues 26 Pull requests 5 Projects 0 Wiki Pulse

Releases Tags Draft a new release

v0.15: Version 0.15 Latest release

mhennrich released this on May 7 · 6 commits to master since this release

Assets 16

Asset	Size
libiio-0.15.g6ecff5d-Darwin-osx_10.10.tar.gz	334 KB
libiio-0.15.g6ecff5d-Darwin-osx_10.11.tar.gz	333 KB
libiio-0.15.g6ecff5d-Darwin-osx_10.12.tar.gz	332 KB
libiio-0.15.g6ecff5d-Linux-precise.deb	467 KB
libiio-0.15.g6ecff5d-Linux-precise.rpm	392 KB
libiio-0.15.g6ecff5d-Linux-precise.tar.gz	477 KB
libiio-0.15.g6ecff5d-Linux-trusty.deb	542 KB
libiio-0.15.g6ecff5d-Linux-trusty.rpm	447 KB
libiio-0.15.g6ecff5d-Linux-trusty.tar.gz	565 KB
libiio-0.15.g6ecff5d-osx_10.10.pkg	85 KB
libiio-0.15.g6ecff5d-osx_10.11.pkg	85 KB
libiio-0.15.g6ecff5d-osx_10.12.pkg	85 KB
libiio-0.15.g6ecff5d-Windows-setup.exe	2.31 MB
libiio-0.15.g6ecff5d-Windows.zip	9.13 MB
Source code (zip)	
Source code (tar.gz)	

Firmware

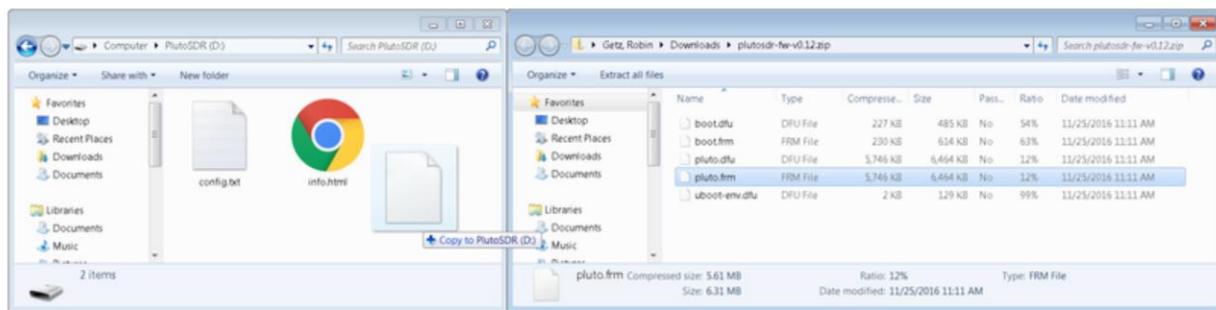
ADI default firmware images:

- [Latest ADALM-PLUTO \(PlutoSDR\) Release](#)
- [Latest ADALM-2000 \(M2k\) Release](#)

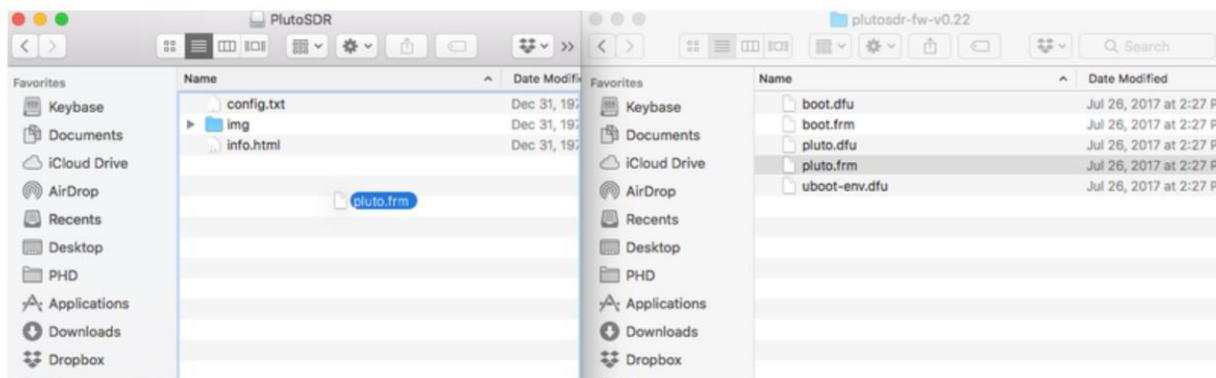
This zip file should include these files:

Filename	Purpose
 boot.dfu	DFU file for First Stage Boot Loader, and U-Boot
 boot.frm	Firmware file for First Stage Boot Loader, U-Boot and it's default environment
 pluto.dfu	DFU file for Pluto Firmware, this would include FPGA Bit File, Linux kernel (all drivers), and ram based file system
 m2k.dfu	DFU file for M2k Firmware, this would include FPGA Bit File, Linux kernel (all drivers), and ram based file system
 pluto.frm	Firmware file for Pluto Firmware, this would include FPGA Bit File, Linux kernel (all drivers), and ram based file system
 m2k.frm	Firmware file for M2k Firmware, this would include FPGA Bit File, Linux kernel (all drivers), and ram based file system
 uboot-env.dfu	DFU file which includes the default U-Boot environment

1. Windows

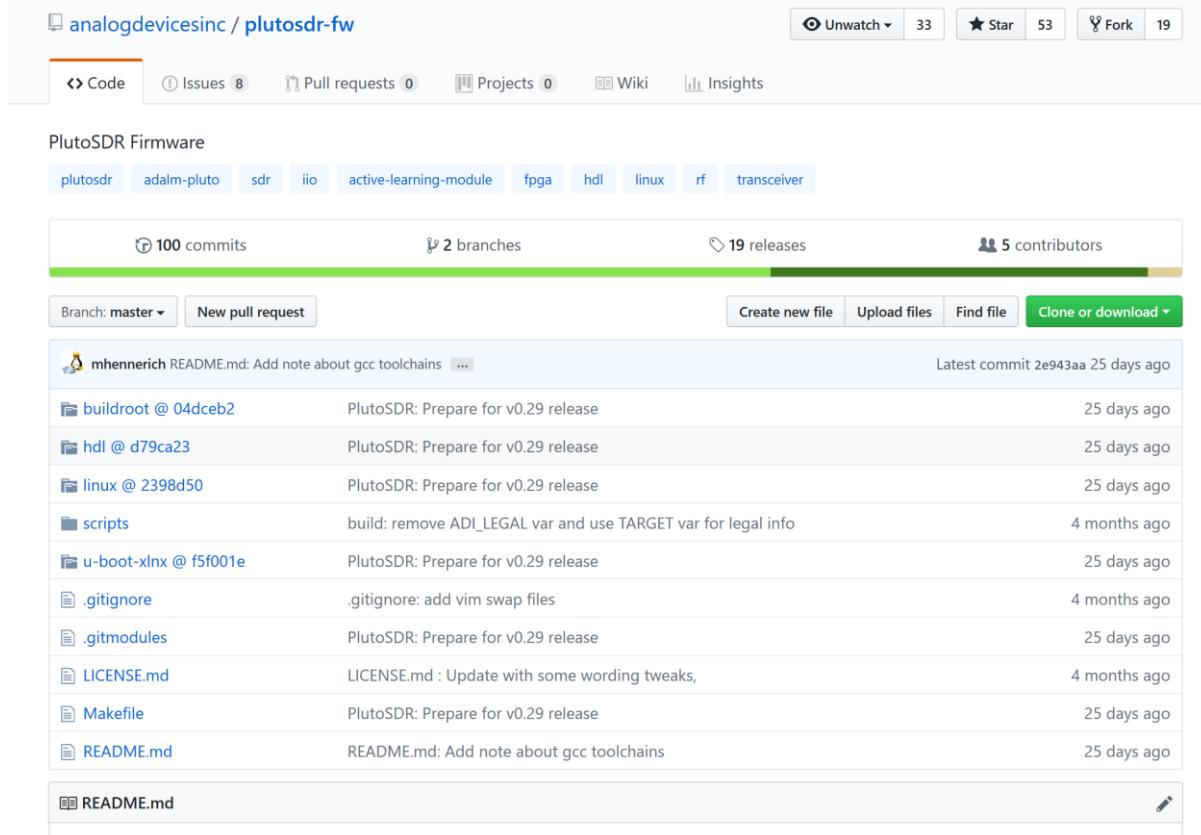


2. OSX



► Notes on using Zip file

Building your own firmware (Linux + HDL)



The screenshot shows the GitHub repository page for `analogdevicesinc / plutosdr-fw`. The repository name is `plutosdr-fw`, which is described as "PlutoSDR Firmware". It has 33 watchers, 53 stars, and 19 forks. The repository has 100 commits, 2 branches, 19 releases, and 5 contributors. The latest commit was made 25 days ago. The repository includes files like `README.md`, `buildroot`, `hdl`, `linux`, `scripts`, `u-boot-xlnx`, `.gitignore`, `.gitmodules`, `LICENSE.md`, `Makefile`, and `README.md`. The code is primarily in C and Verilog.

plutosdr-fw

PlutoSDR Firmware for the [ADALM-PLUTO](#) Active Learning Module

Latest binary Release : [release v0.29](#)

[Instructions from the Wiki: Building the image](#)

- Build Instructions

```
sudo apt-get install git build-essential fakeroot libncurses5-dev libssl-dev ccache
sudo apt-get install dfu-util u-boot-tools device-tree-compiler libssl1.0-dev mtools
git clone --recursive https://github.com/analogdevicesinc/plutosdr-fw.git
cd plutosdr-fw
export CROSS_COMPILE=arm-xilinx-linux-gnueabi-
export PATH=$PATH:/opt/Xilinx/SDK/2017.2/gnu/arm/bin
export VIVADO_SETTINGS=/opt/Xilinx/Vivado/2017.4/settings64.sh
make
```

The project may build also using Vivado 2017.2, 2016.4 or 2016.2. However 2017.4 is the current tested FPGA synthesis toolchain. For compatibility reasons with existing targeting workflows we continue to use the arm-xilinx-linux-gnueabi-gcc toolchain included in the SDK 2017.2.

If you want to use the arm-linux-gnueabihf-gcc hard-float toolchain included in SDK 2017.4. Following variables should be exported:

```
export CROSS_COMPILE=arm-linux-gnueabihf-
export PATH=$PATH:/opt/Xilinx/SDK/2017.4/gnu/aarch32/bin/gcc-arm-linux-gnueabi/bin
```



AHEAD OF WHAT'S POSSIBLE™

Lab: Updating Firmware

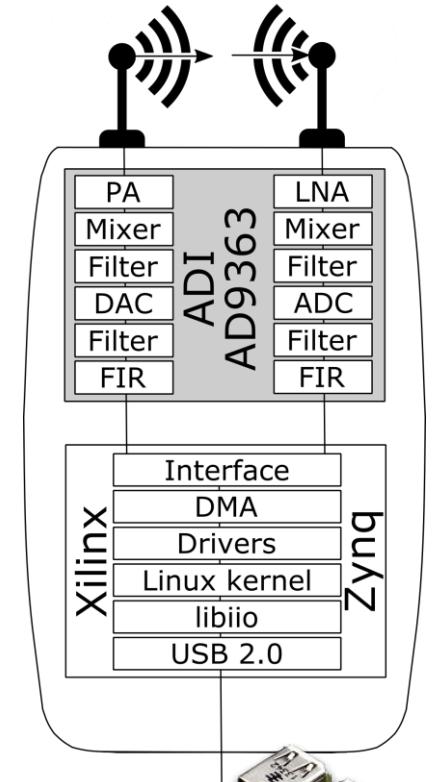


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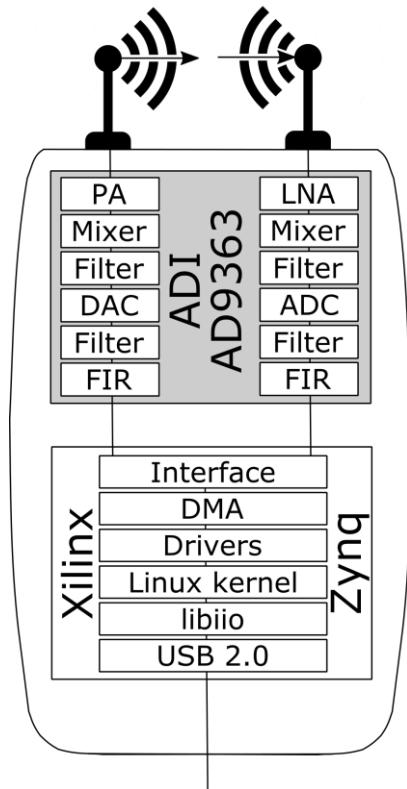
USB Configurations

ADALM-PLUTO possible use cases

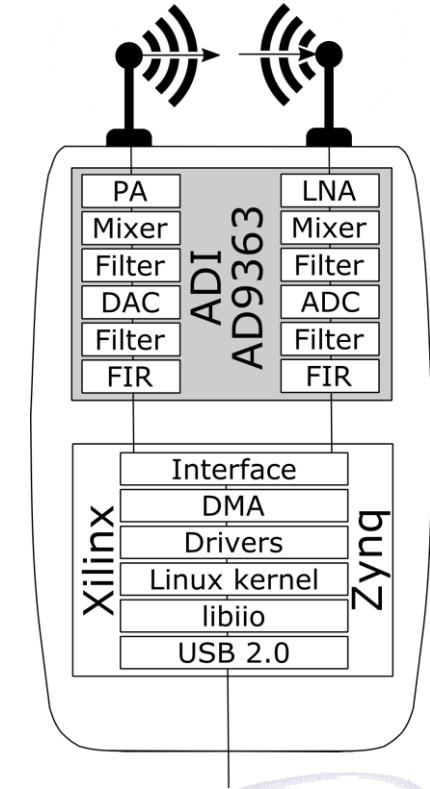
Connect to host



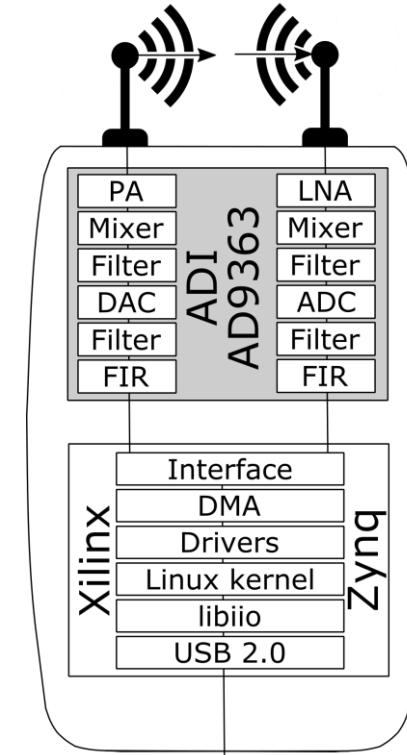
Connect to thumb drive



Connect to USB LAN



Connect to USB WiFi



USB OTG

- The Pluto will automount any USB mass storage device such as thumb drive or Hard Drives
- The automounter will then look for some special file names:
 - runme[0-9].sh which it will run as a shell script
 - runme[0-9] which it will run as a binary file.
- For those interested, it will do that via the automounter script in /lib/mdev/automounter.sh which is maintained here.



1. Provide power to right USB
2. Plug OTG+USB drive into left USB port
3. Left LED will become solid
4. Script runs

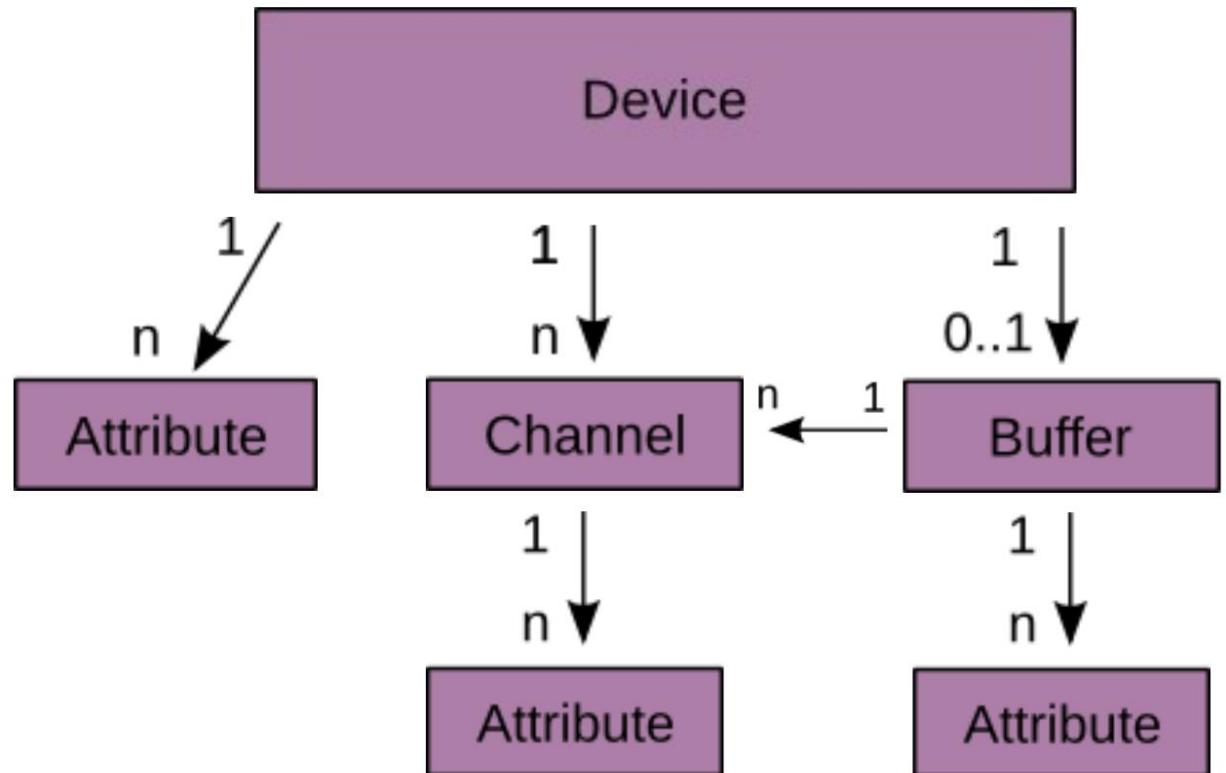


AHEAD OF WHAT'S POSSIBLE™

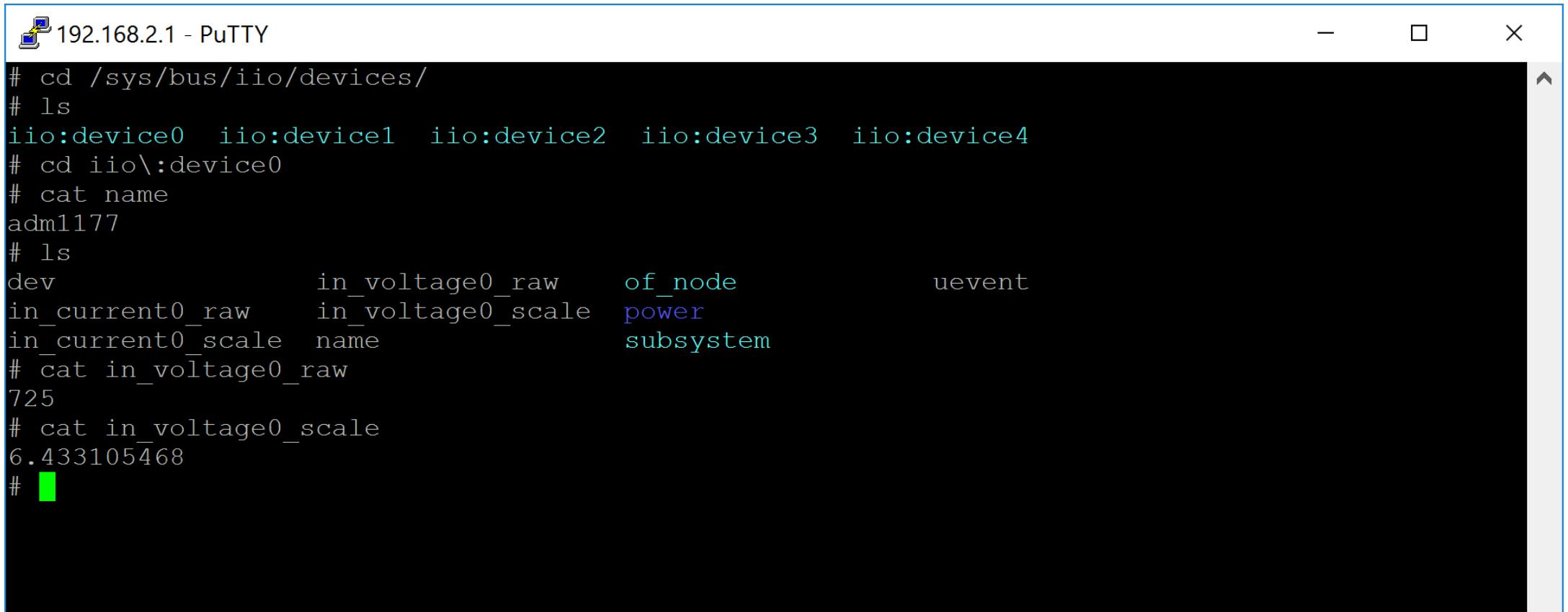
IIO and (The) libIIO

IIO Structure

- ▶ Device represents logical functional unit
 - Typically a piece of physical hardware
- ▶ Attributes
 - Describe hardware capabilities
 - Allow to change hardware configuration
- ▶ Channels represent data channels
 - Channels have a type and direction
 - E.g. ADC has voltage channels
 - Channels can have attributes

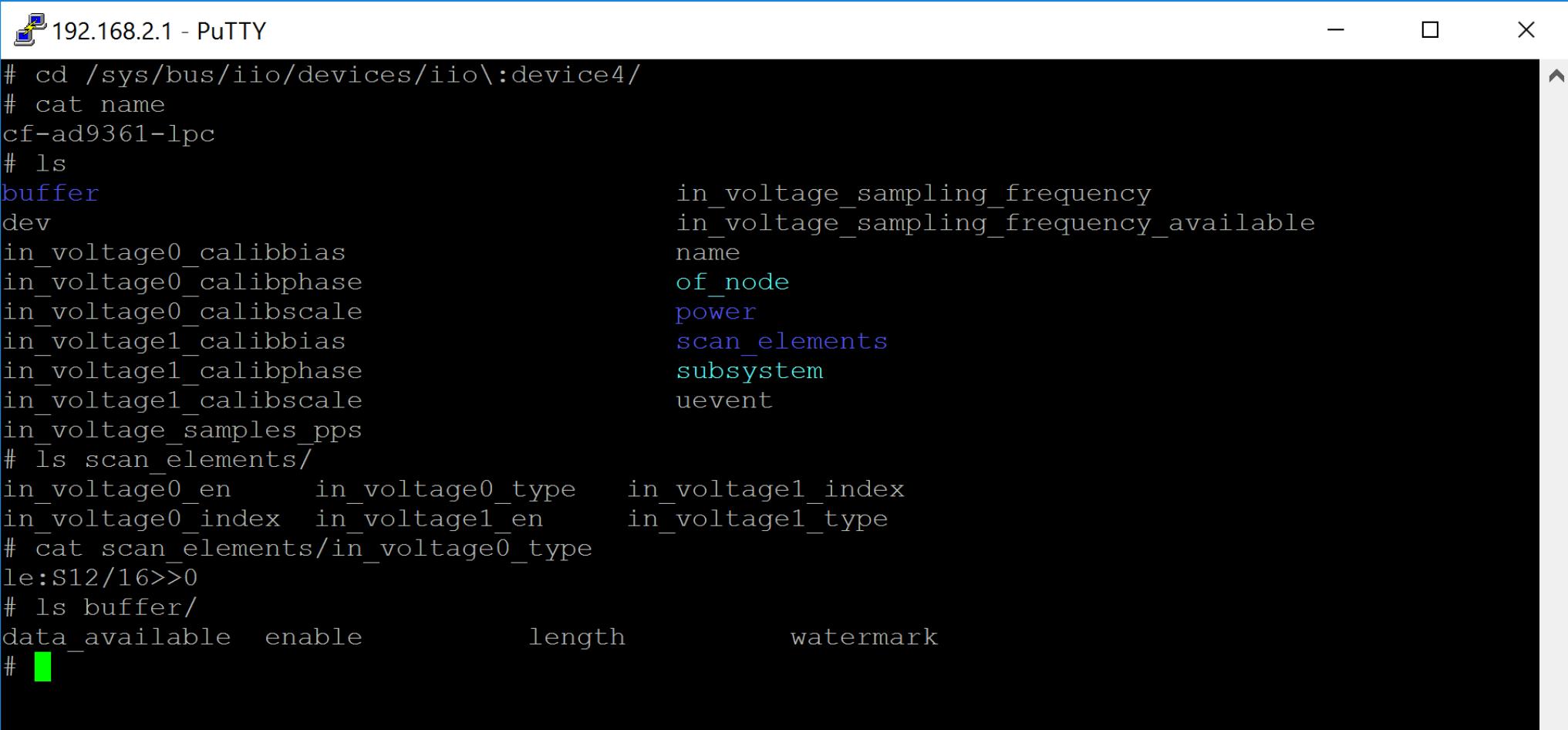


IIO Userspace ABI - Devices

A screenshot of a PuTTY terminal window titled "192.168.2.1 - PuTTY". The window shows a command-line session where the user navigates to the "/sys/bus/iio/devices/" directory, lists devices (iio:device0 through iio:device4), and then inspects the contents of iio:device0. The output shows various files like "name", "uevent", and "subsystem", along with raw and scaled values for voltage and current. The PuTTY window has standard window controls (minimize, maximize, close) and a scroll bar on the right.

```
# cd /sys/bus/iio/devices/
# ls
iio:device0  iio:device1  iio:device2  iio:device3  iio:device4
# cd iio\:device0
# cat name
adm1177
# ls
dev          in_voltage0_raw    of_node      uevent
in_current0_raw   in_voltage0_scale  power
in_current0_scale  name          subsystem
# cat in_voltage0_raw
725
# cat in_voltage0_scale
6.433105468
#
```

IIO Userspace ABI - Buffers



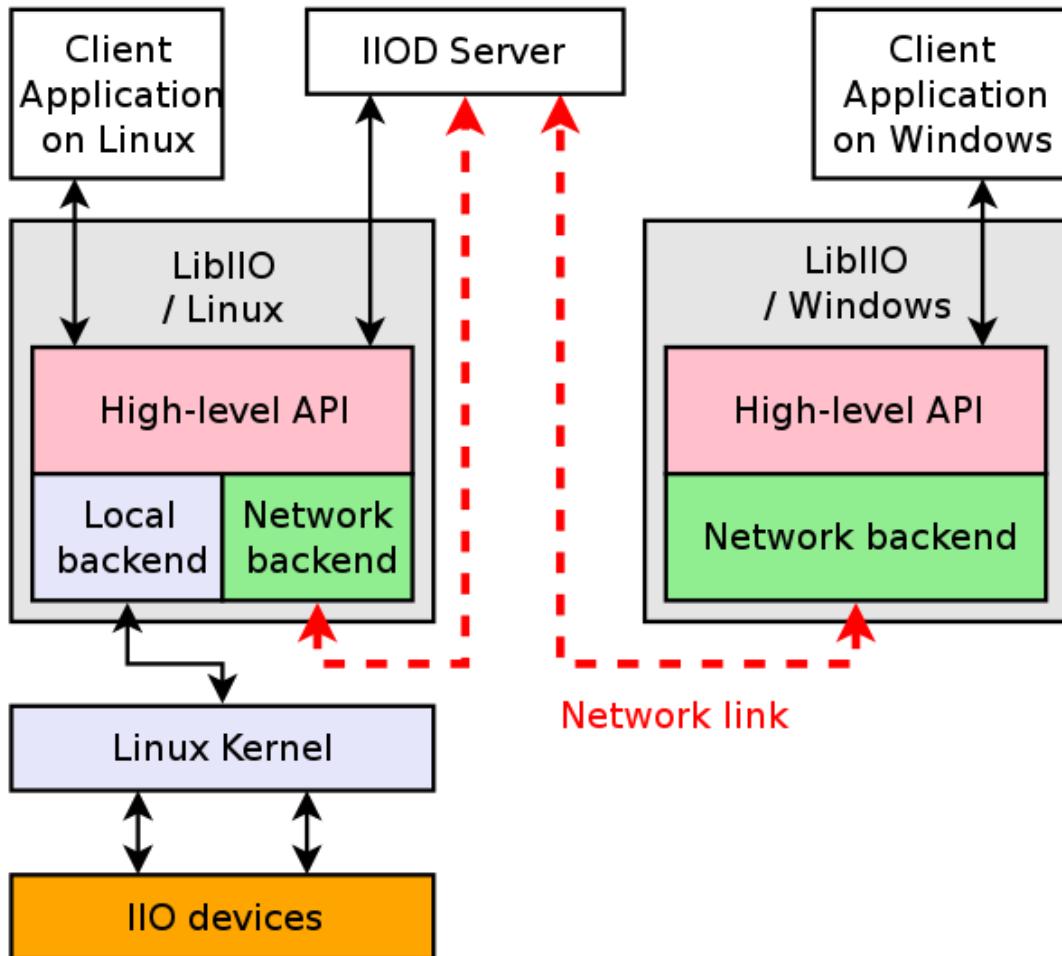
The screenshot shows a PuTTY terminal window titled "192.168.2.1 - PuTTY". The terminal displays a command-line session navigating through the /sys/bus/iio/devices directory for device 4. The session includes commands like "cd", "cat", "ls", and "cat" to inspect specific files such as "name", "cf-ad9361-lpc", "buffer", "dev", and various calibration and scan-related files. The output is color-coded using syntax highlighting.

```
# cd /sys/bus/iio/devices/iio\device4/
# cat name
cf-ad9361-lpc
# ls
buffer
dev
in_voltage0_calibbias
in_voltage0_calibphase
in_voltage0_calibscale
in_voltage1_calibbias
in_voltage1_calibphase
in_voltage1_calibscale
in_voltage_samples_pps
# ls scan_elements/
in_voltage0_en      in_voltage0_type      in_voltage1_index
in_voltage0_index   in_voltage1_en       in_voltage1_type
# cat scan_elements/in_voltage0_type
le:S12/16>>0
# ls buffer/
data_available    enable          length        watermark
#
```

Color-coded words in the output:

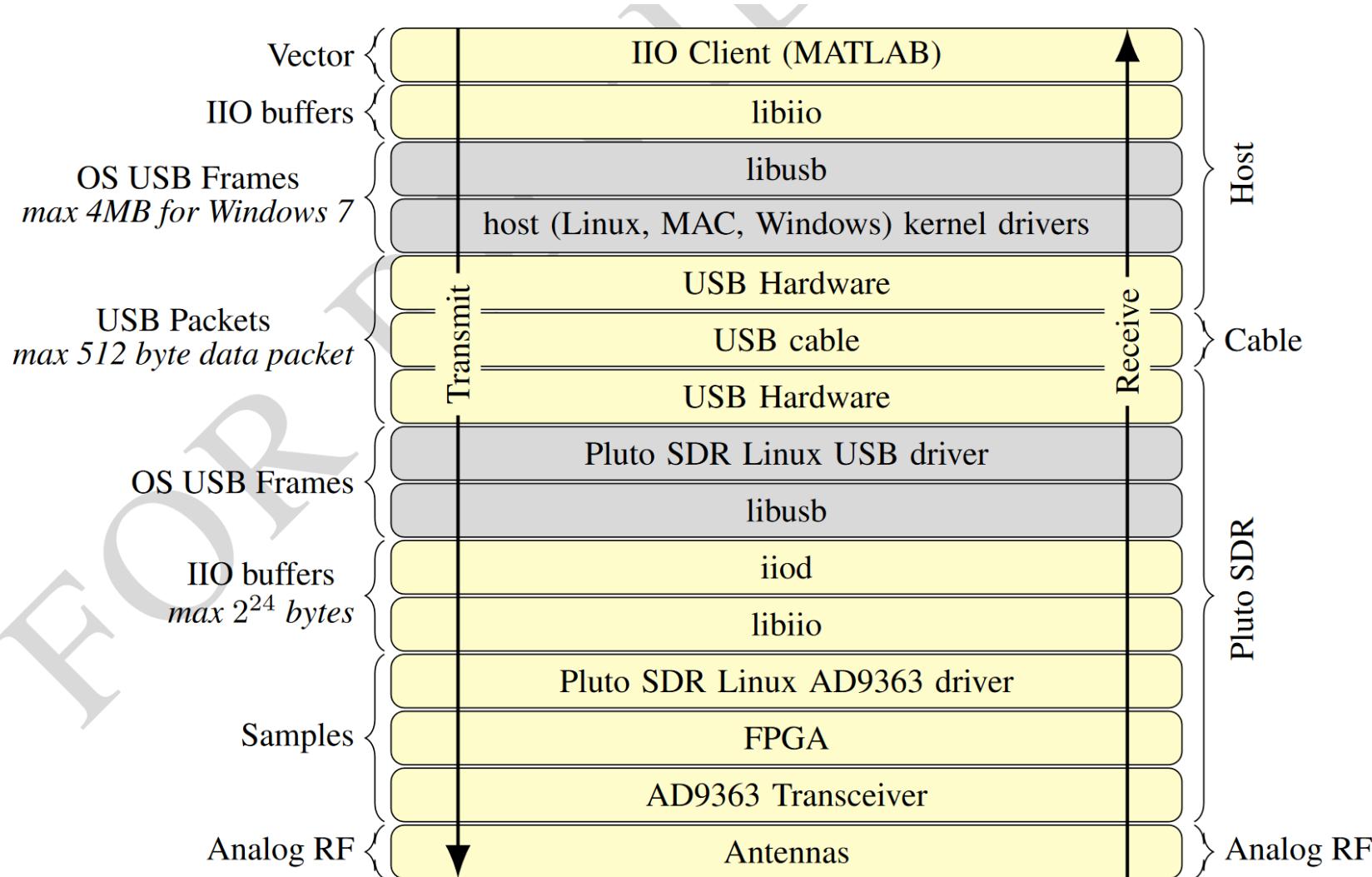
- blue: in_voltage_sampling_frequency, in_voltage_sampling_frequency_available, name, of_node, power, scan_elements, subsystem, uevent
- red: in_voltage0_en, in_voltage0_index, in_voltage1_en, in_voltage1_index, in_voltage0_type, in_voltage1_type
- green: data_available, enable, length, watermark

libIIO



```
struct iio_context *ctx;  
struct iio_device *dev;  
struct iio_channel *ch;  
  
/* Error handling is missing */  
ctx = iio_create_default_context();  
dev = iio_context_get_device(ctx, 0);  
ch = iio_device_get_channel(dev, 0);  
  
iio_device_attr_write_longlong(dev, "sample_rate", 1000);  
iio_channel_attr_write_double(ch, "scale", 0.525);
```

The Stack



libIIO Command Line Tools

- ▶ **iio_adi_xflow_check**
 - Overflow/underflow testing
- ▶ **iio_attr**
 - Attribute reading and writing
- ▶ **iio_genxml**
 - Generate xml from context tree
- ▶ **iio_info**
 - Find devices and list attributes
- ▶ **iio_readdev**
 - Read from stream devices
- ▶ **iio_reg**
 - Read and write to registers
- ▶ **iio_writedev**
 - Write to stream devices
- ▶ All these tools come with a libIIO install
- ▶ Available on target platforms as well
- ▶ Cross-platform



AHEAD OF WHAT'S POSSIBLE™

ILD: libIIO

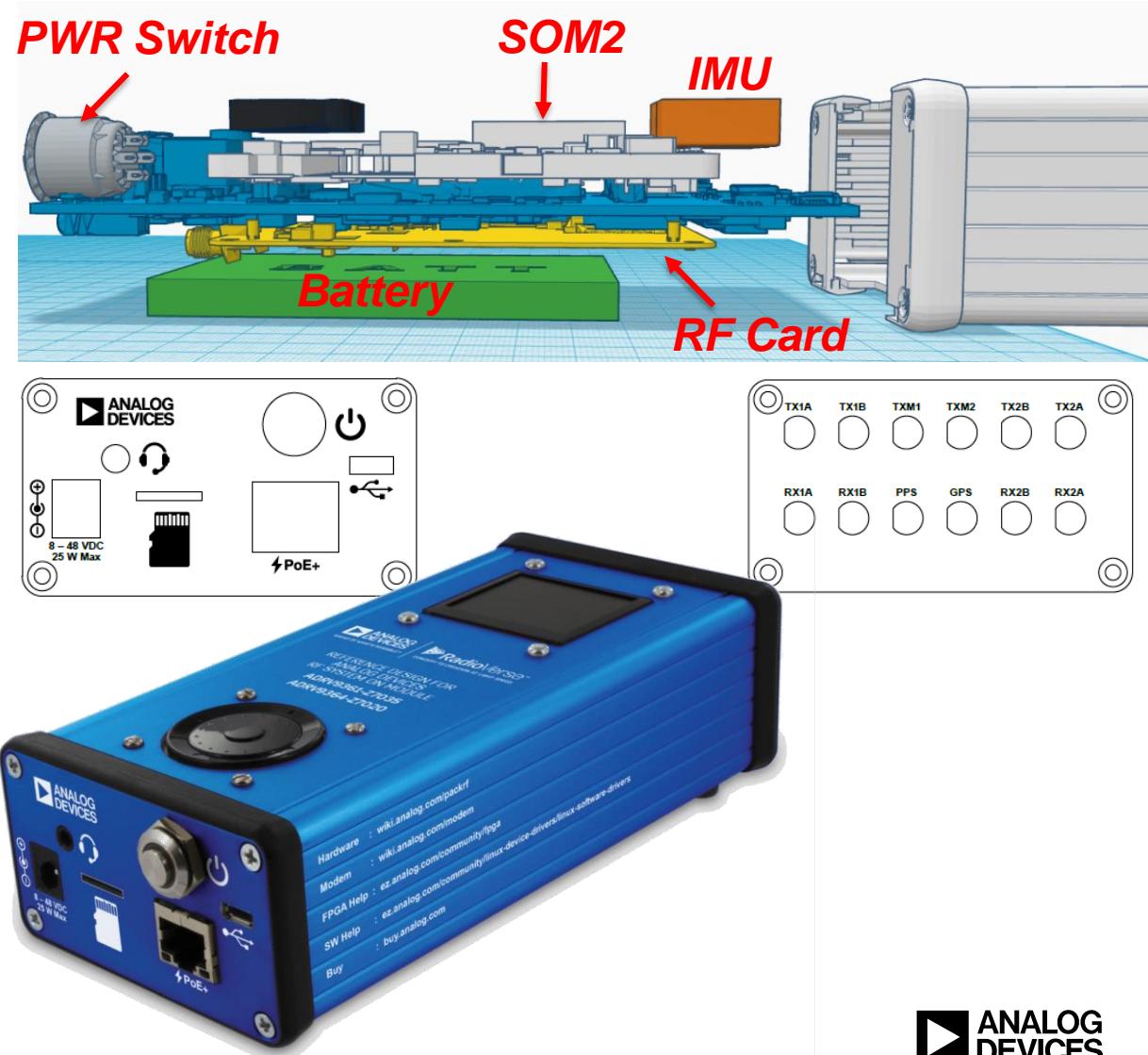


AHEAD OF WHAT'S POSSIBLE™

Lab: IIO Tools + IIO-Scope

Devices Outsides of Transceivers

- ▶ Example design which shows how to design RF SOM into a custom carrier
- ▶ Custom Carrier includes:
 - OLED
 - Nav Switch
 - Power Button
 - Wake on RTC
 - Power over Ethernet (PoE+)
 - Automotive DC-DC converter
 - 8 – 48V DC input
 - Battery Management
 - Hot Power swap
 - Inertial Measurement Unit
 - Six Degrees of Freedom
 - GPS Chipset
 - 1 PPS in and out
 - Audio headset (stereo headphones, mic and button control)





AHEAD OF WHAT'S POSSIBLE™

ILD: PackRF and IIO

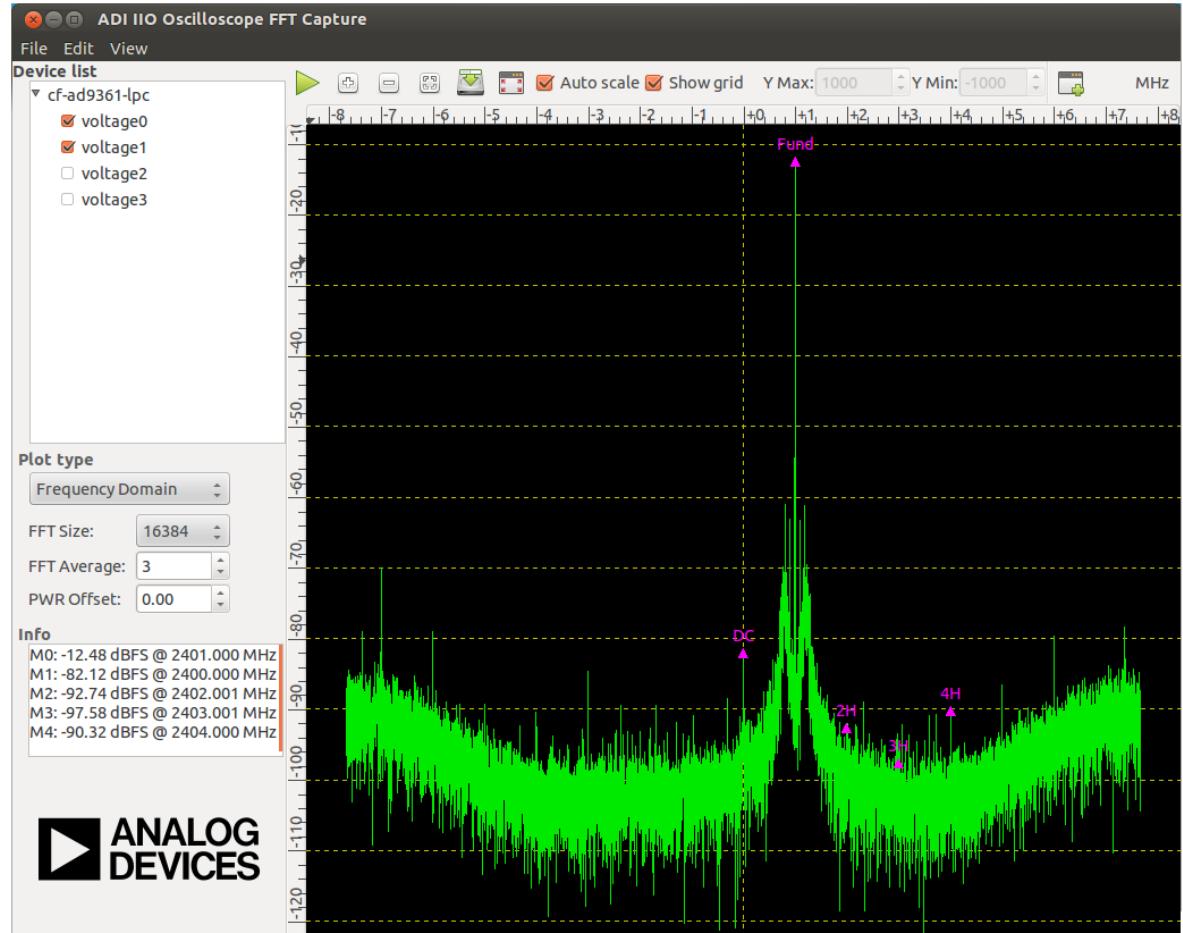


AHEAD OF WHAT'S POSSIBLE™

IIO-Scope

IIO-Scope

- ▶ Capture and display data
 - Time domain
 - Frequency domain
 - Constellation plot
 - Markers
 - Math operations
- ▶ Device configuration
- ▶ Plug-in system allow to create device or complex specialized GU





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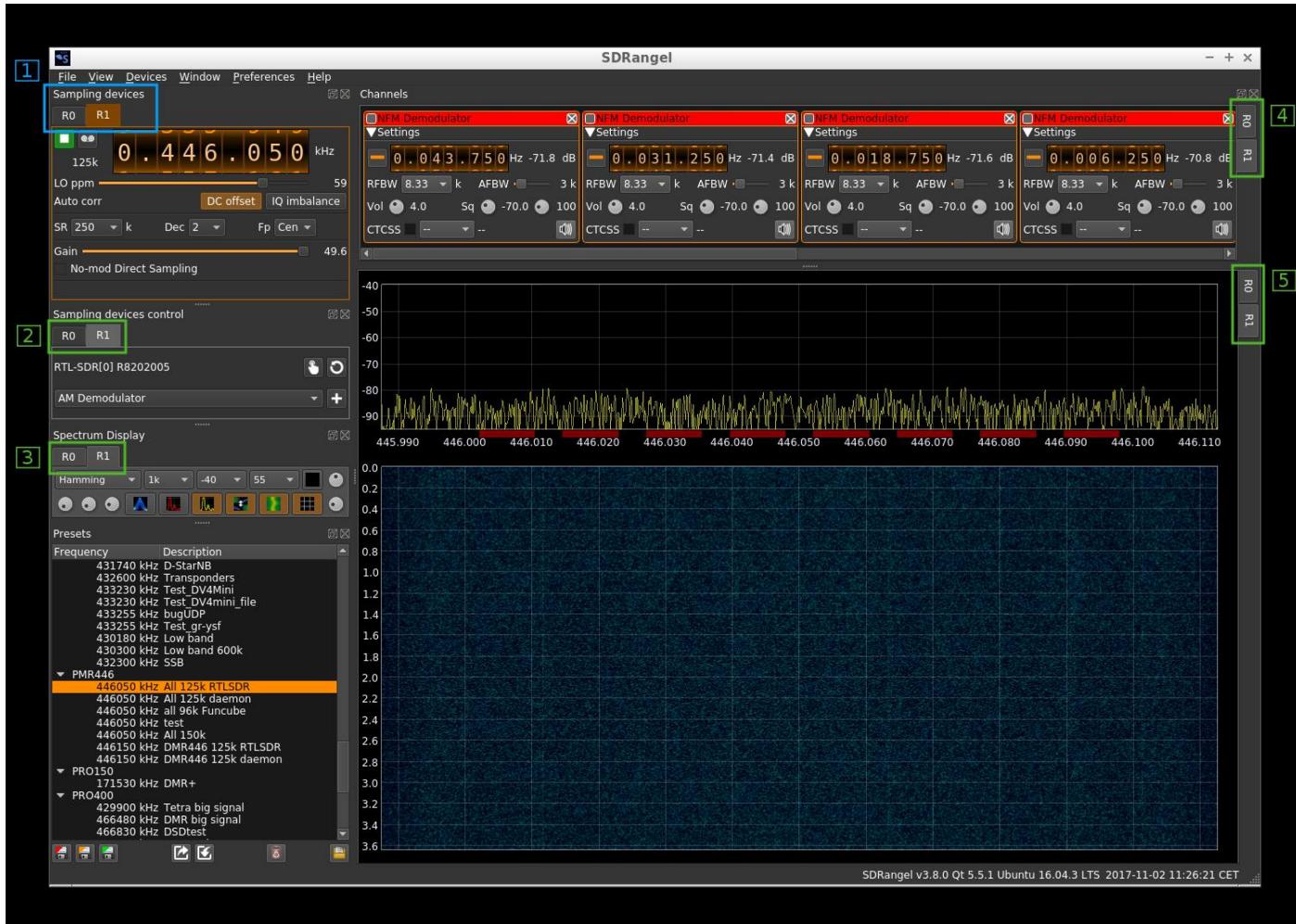
Lab: IIO-Scope



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Fun with SDRAngel

SDRAngel at a glance





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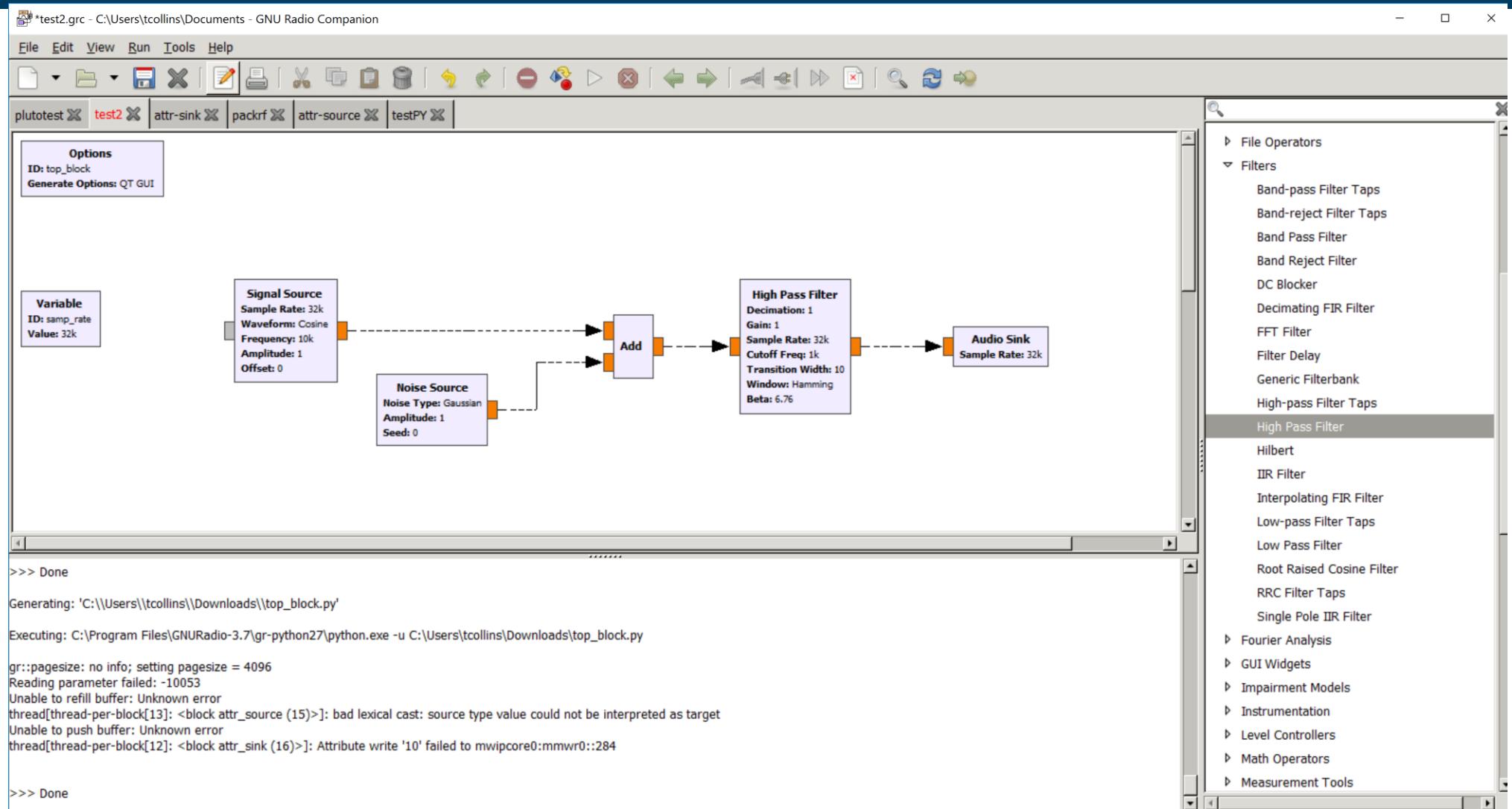
ILD: SDR Angel



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GNU Radio + IIO = gr-iio

Intro To GNU Radio



gr-iio: Details

- Repository
 - <https://github.com/analogdevicesinc/gr-iio>
- Dependencies
 - libiio
 - libad9361-iio
- Doc
 - <https://wiki.analog.com/resources/tools-software/linux-software/gnuradio>

```
git clone https://github.com/analogdevicesinc/gr-iio.git
cd gr-iio
cmake .
make
sudo make install
cd ..
sudo ldconfig
```

► Windows

The screenshot shows the GitHub Releases page for the 'gr-iio' repository. The 'Releases' tab is selected. There are three releases listed:

- v1.5.0** (Latest release, released 23 hours ago): Includes a Windows MSI installer (gnuradio_3.7.11_iiosupport_win64.msi) and source code in zip and tar.gz formats.
- v1.4.0** (released on Apr 16): Includes a Windows MSI installer (6eb21af.zip) and source code in zip and tar.gz formats.
- v1.0.0** (released on Apr 13 2016): Includes a Windows MSI installer (1a6ec98.zip) and source code in zip and tar.gz formats.

The page also features a summary message: "Initial gr-iio support" by tfcollins, noting it's a beta build functional with IIO devices and associated blocks, distributed without warranty, and asking users to respect associated licenses.

IIO GNU Radio Support: gr-iio

- Industrial IO
 - FMComms
 - FMComms2/3/4 Sink
 - FMComms2/3/4 Source
 - FMComms5 Sink
 - FMComms5 Source
 - IIO Attribute Sink
 - IIO Attribute Source
 - IIO Attribute Updater
 - IIO Device Sink
 - IIO Device Source
- Math Operators
 - Function
 - Modulo
 - Modulo Const
 - Power
- PlutoSDR
 - PlutoSDR Sink
 - PlutoSDR Source
- Waveform Generators
 - Function Generator

SDR
Attributes
Streams

Math (Scopy)

SDR
Math (Scopy)

IIO Device Source
IIO context URI: local:
Device Name/ID:
PHY Device Name/ID:
Buffer size: 32.768k
Decimation: 1
Parameters:

IIO Device Sink
IIO context URI: local:
Device Name/ID:
PHY Device Name/ID:
Buffer size: 32.768k
Interpolation: 1
Cyclic: False
Parameters:

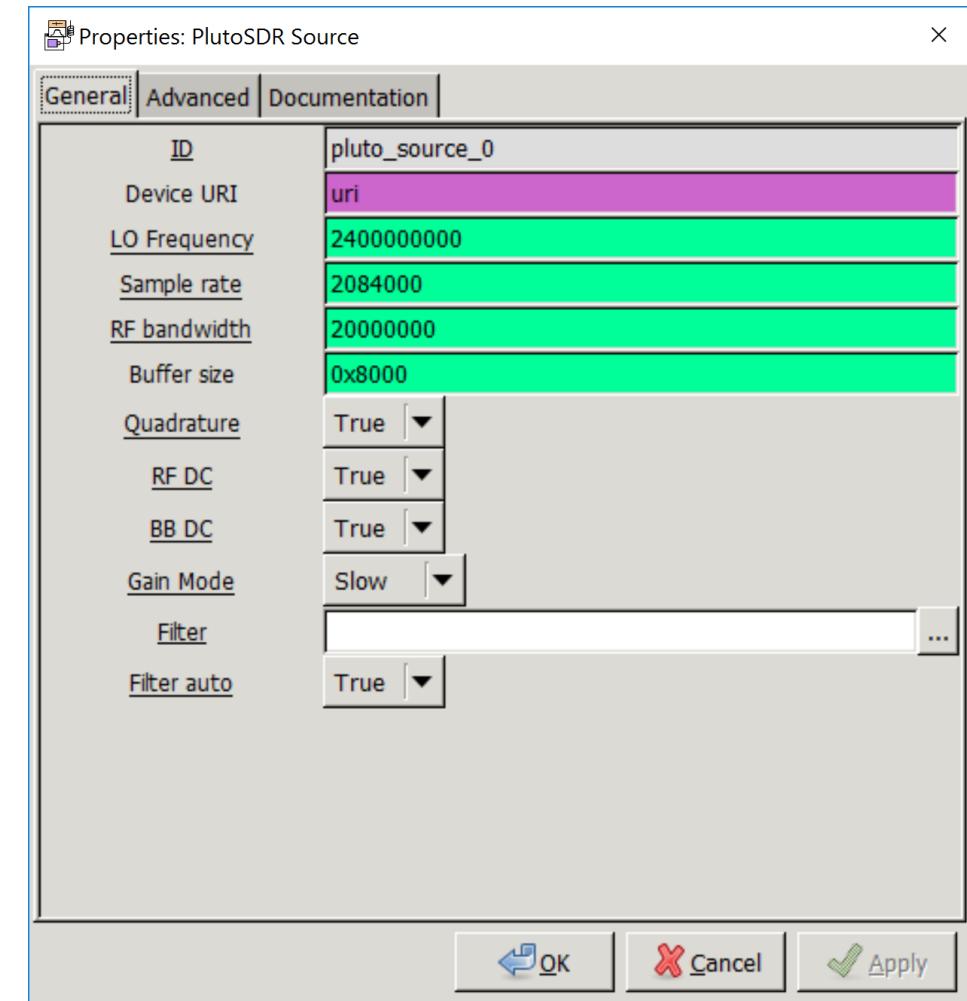
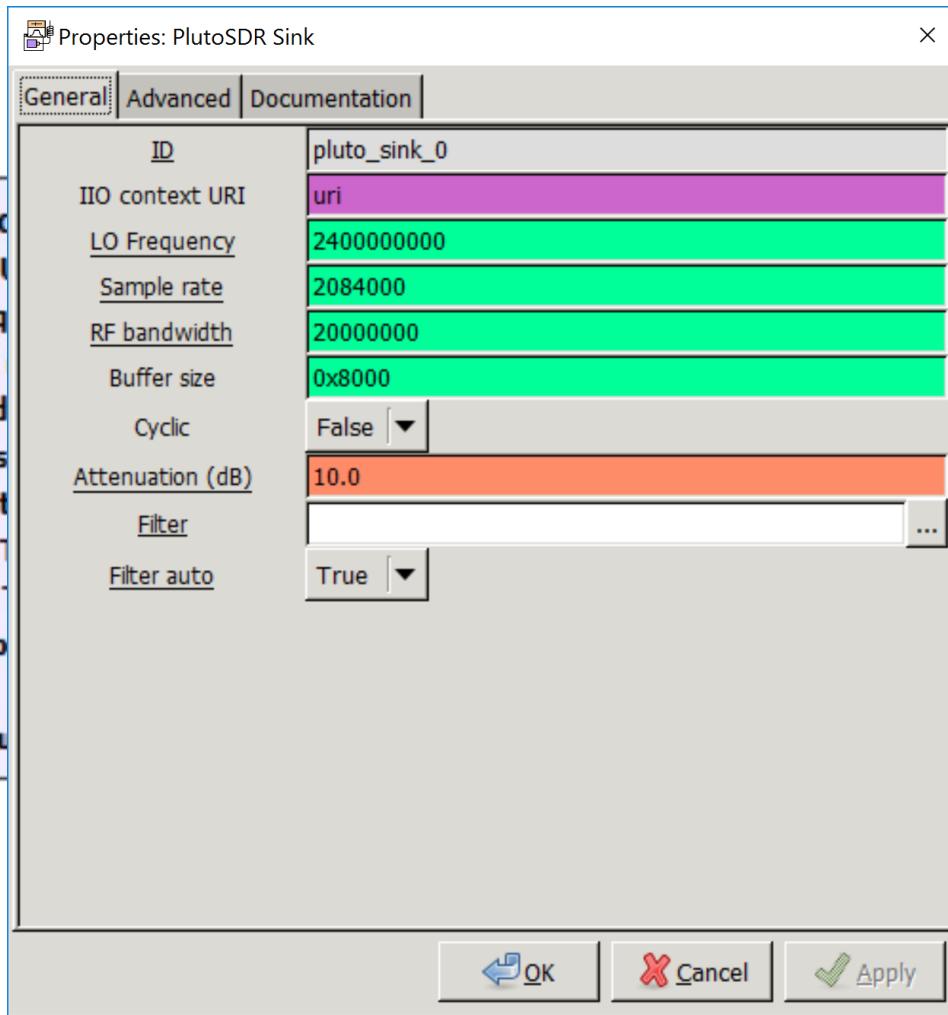
IIO Attribute Source
uri:
Device:
Input/Output: Input
Channel Name:
Attribute Name:
Data Type: Double
Update Interval (ms): 1
Samples Per Update: 1.024k

FMComms2/3/4 Sink
IIO context URI: local:
LO Frequency: 2.4G
Sample rate: 2.084M
RF bandwidth: 20M
Buffer size: 32.768k
Cyclic: False
RF Port Select: A
Attenuation TX1 (dB): 10
Attenuation TX2 (dB): 10
Filter:
Filter auto: True

IIO Attribute Sink
uri:
Device:
Attribute Type: Channel
Input/Output: Input
Channel Name:

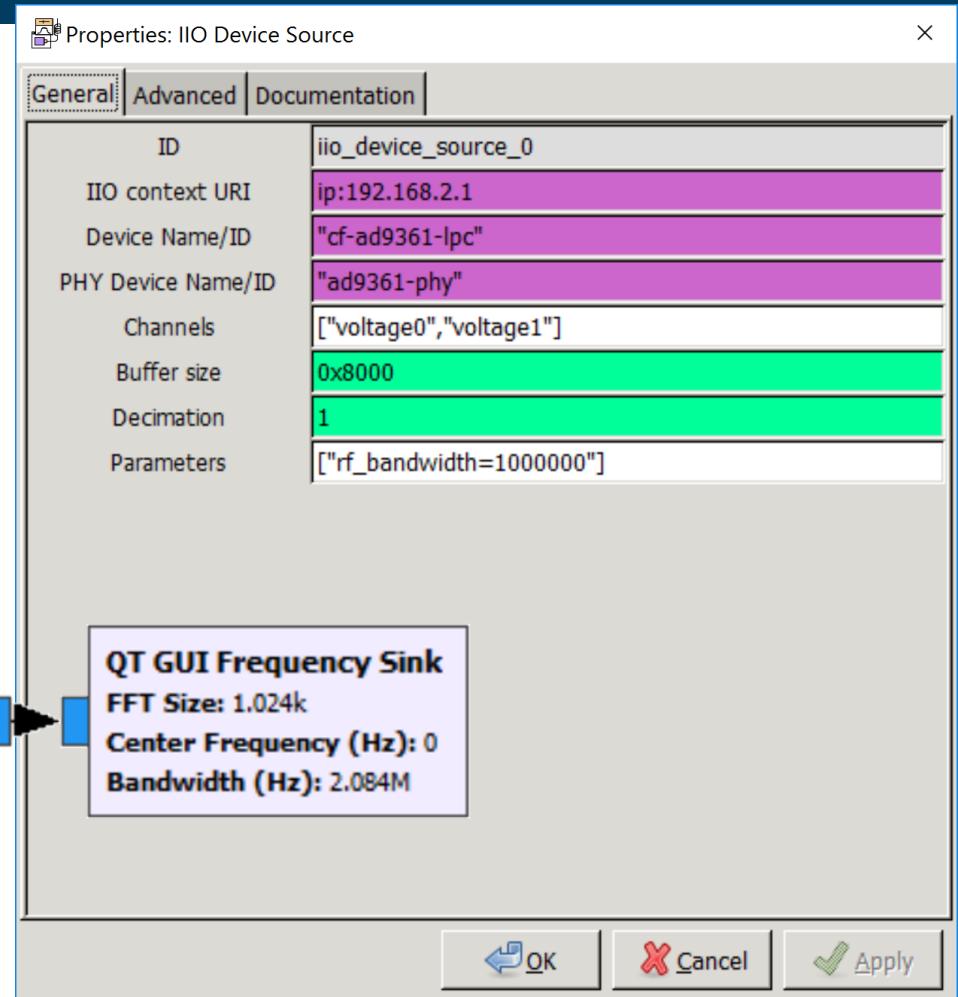
Using Pluto Blocks

Pluto
Device ID
LO Freq
Sample
RF band
Buffer s
Quadrat
RF DC:
BB DC:
Gain Mode
Filter:
Filter au



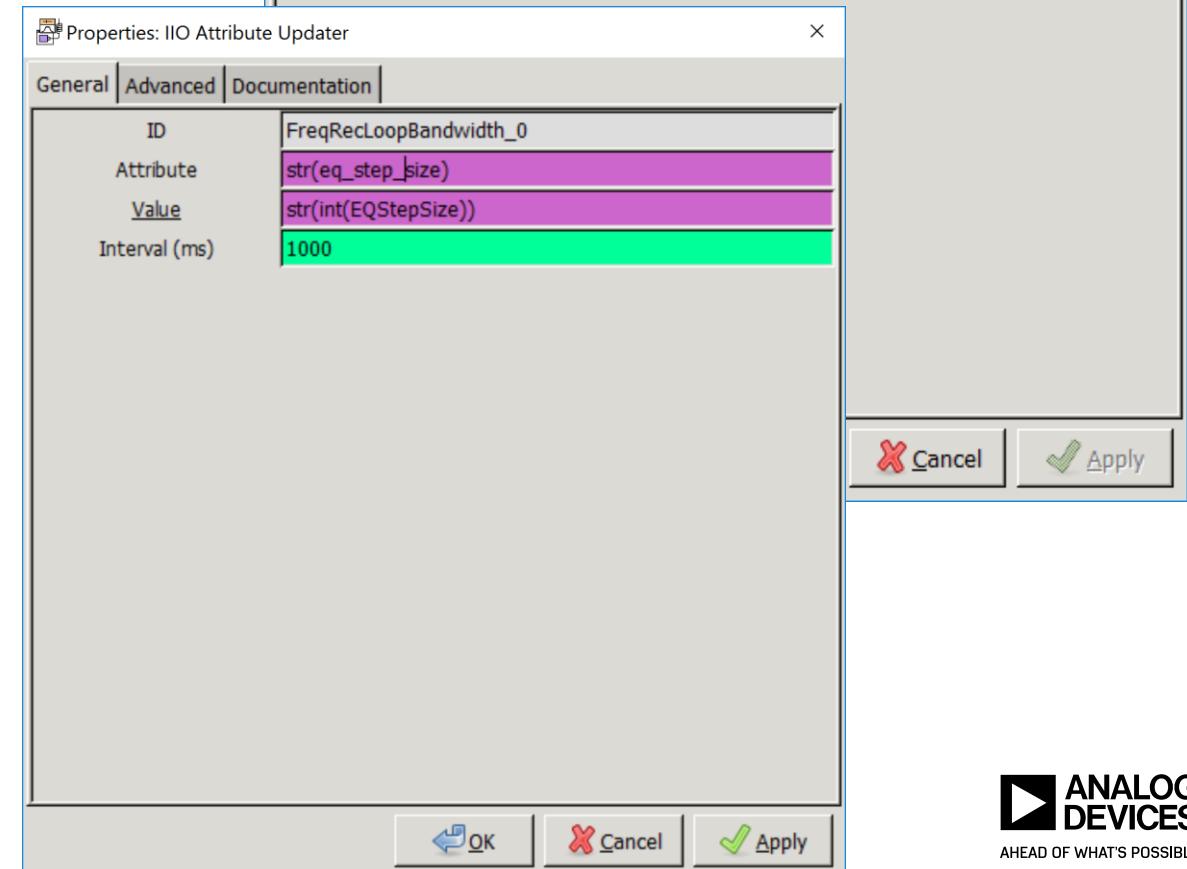
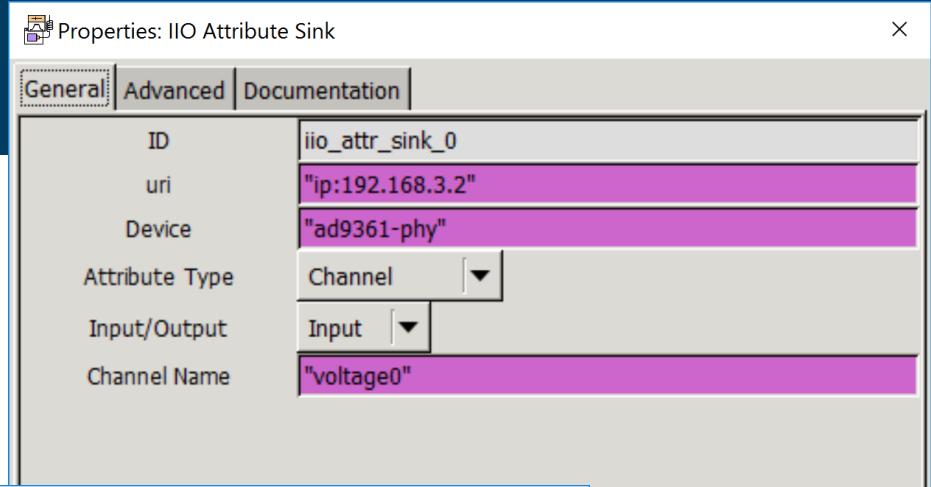
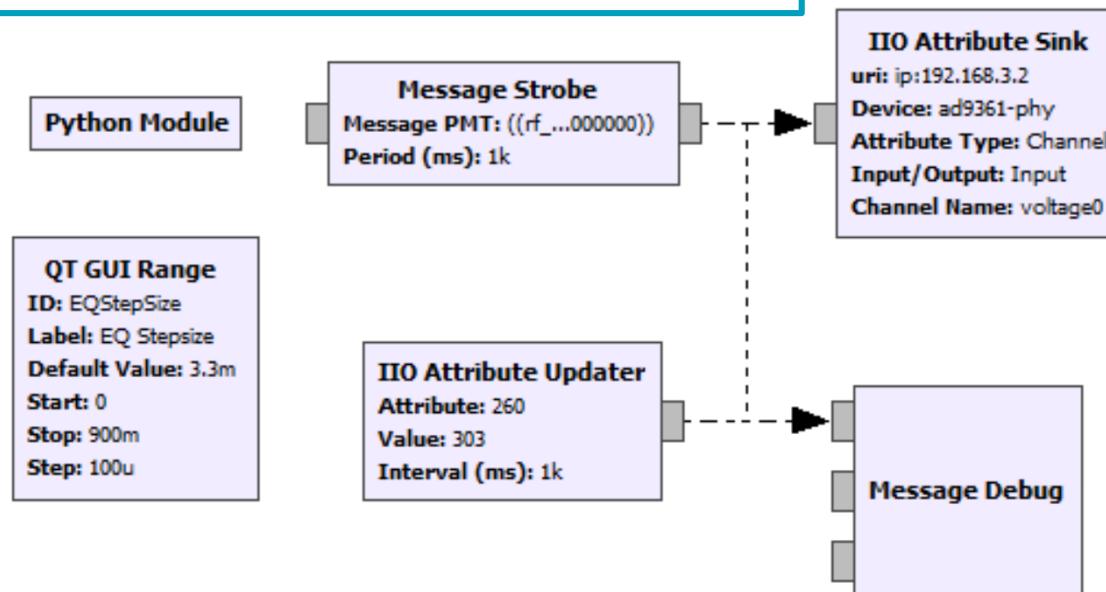
Stream (Device) Blocks

```
C:\Users\tcollins>iio_attr -q -u ip:192.168.2.1 -d
IIO context has 5 devices:
    iio:device3: cf-ad9361-dds-core-lpc, found 0 device attributes
    iio:device1: ad9361-phy, found 18 device attributes
    iio:device4: cf-ad9361-lpc, found 0 device attributes
    iio:device2: xadc, found 1 device attributes
    iio:device0: adm1177, found 0 device attributes
```

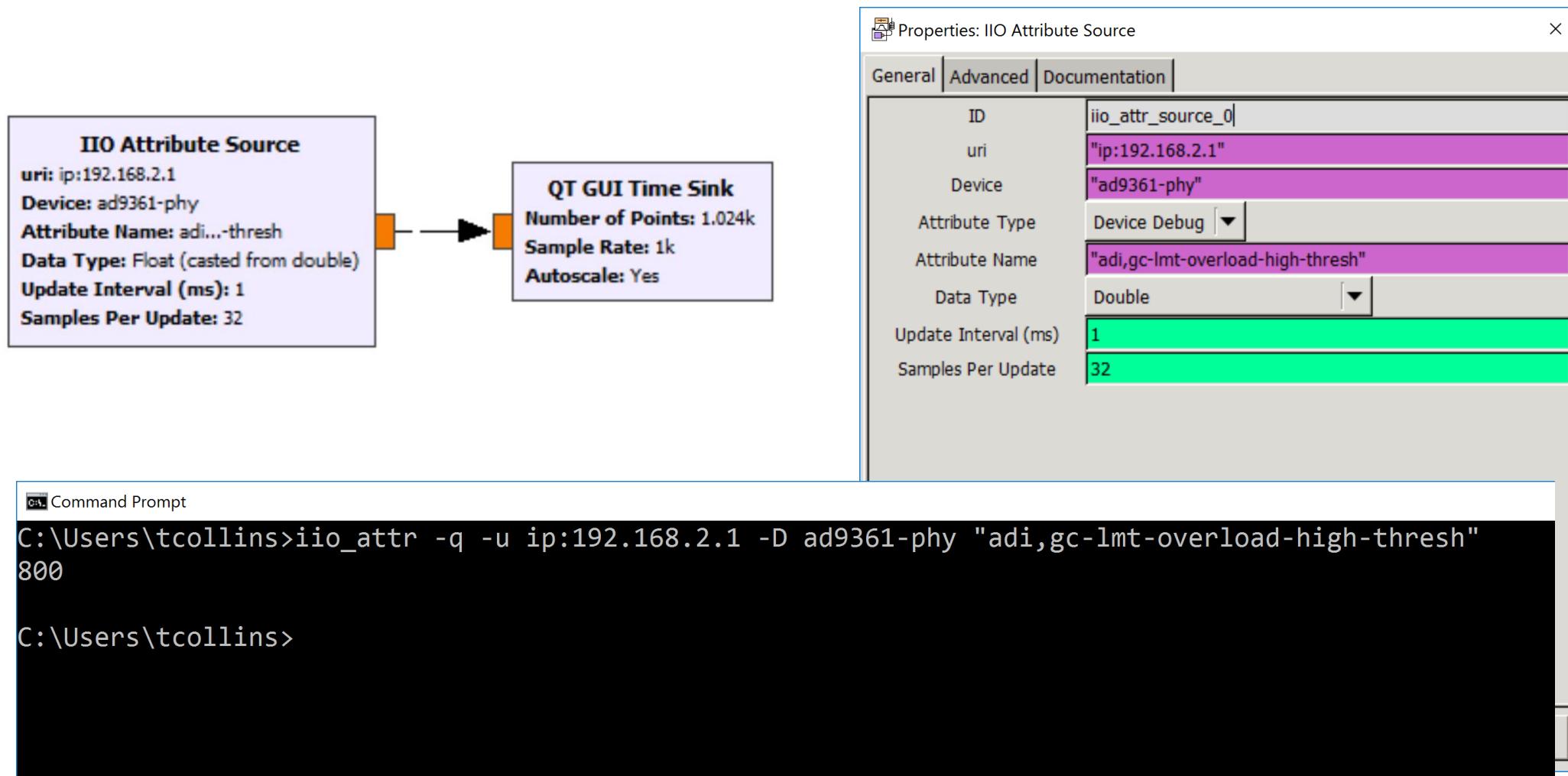


Using Attribute Sink

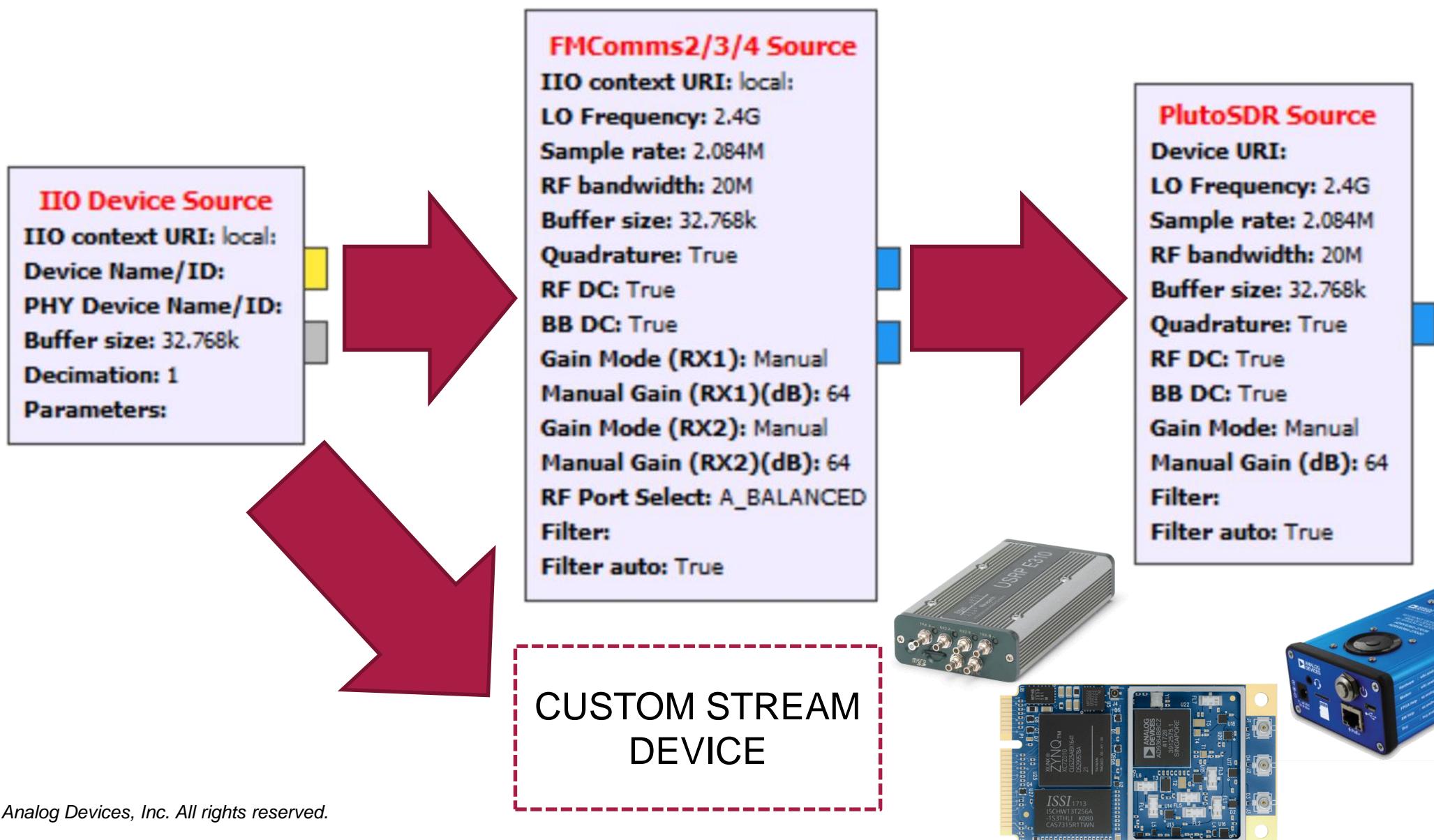
```
import pmtkey0 = pmt.intern("rf_bandwidth")
val0 = pmt.intern("23000000")
msg_dic = pmt.make_dict()
msg_dic = pmt.dict_add(msg_dic, key0, val0)
```



Using Attribute Source



Hardware Support Through IIO





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Lab: GNU Radio + gr-iio

FMC Carrier Demo with GNU Radio



Thank you!

- For future support:
 - <http://ez.analog.com>
 - <http://wiki.analog.com>



Wiki