Correção Quiz 4

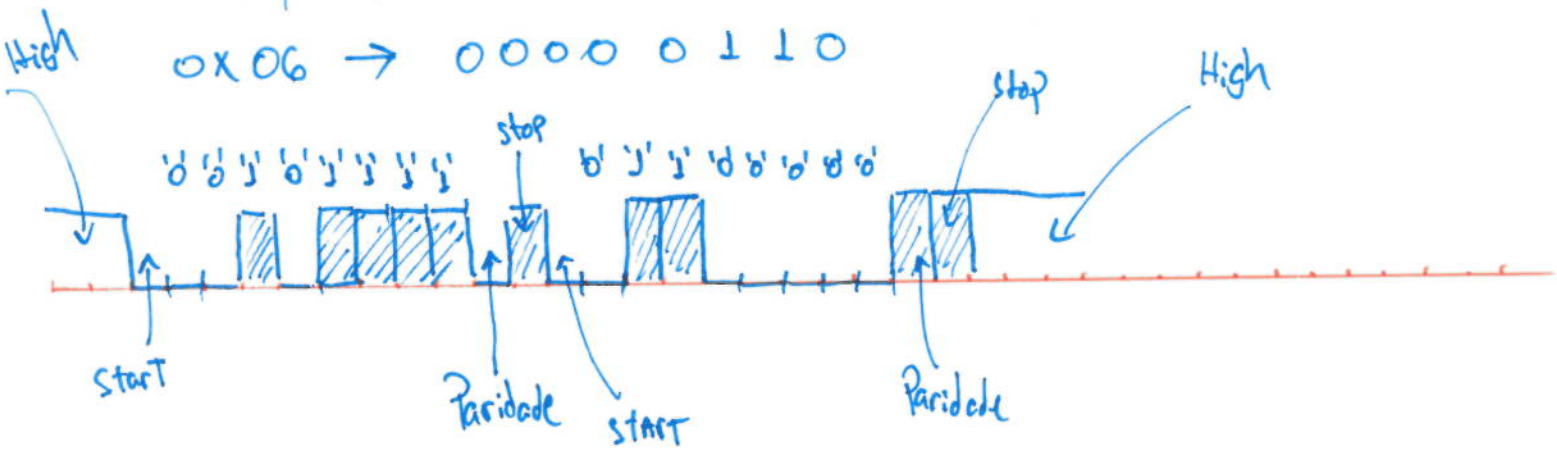
1a.

1b. 8 bits de dado, ímpar, 1 Stop bit

8 0 1

0xF4 → 1 1 1 1 0 1 0 0

0x06 → 0 0 0 0 0 1 1 0

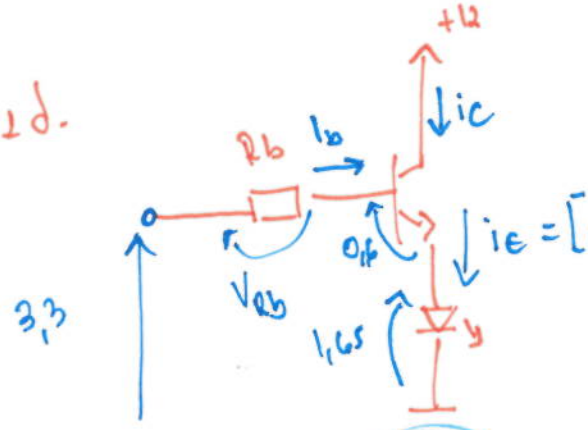


1c. Para cada byte a ser transmitido adiciona-se:

- 1 bit start
- 1 bit paridade
- 1 bit stop

$\therefore$ 2 bytes → $2 \cdot 8 + 2 \cdot (1+1+1)$ $\quad \frac{22\ b}{9600\ \frac{b}{s}} = 2\ ms$ //

1d.



① $V_{Rb} = 3,3 - 1,65 - 0,6 = 1,05V$ //

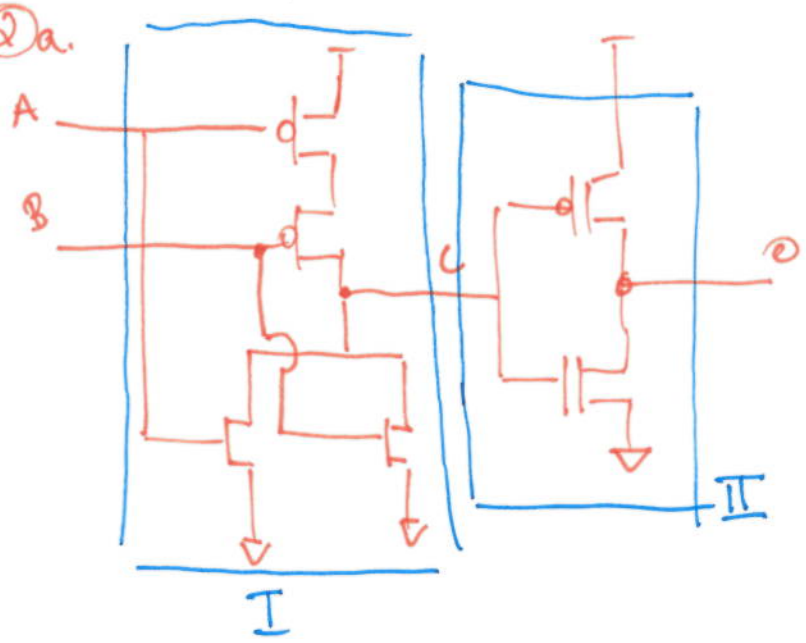
② 20mA / 40mA

$i_b \cdot \beta = i_c \simeq i_E \rightarrow i_b = \frac{20m}{100} = 0,20mA$ //

(β = 100)

③ $R_b = \frac{V_b}{i_b} = \frac{1,05}{0,20mA} = 5,2\ k\Omega$ //

② a.



A	B	C
0	0	1
0	1	0
1	0	0
1	1	0

C	O
1	0
0	1

+

A	B	O
0	0	0
0	1	1
1	0	1
1	1	1

b.

③ $\text{Ganho} = -\frac{R_f}{R_{in}} = \frac{-100\,k}{10\,k} = -10$