

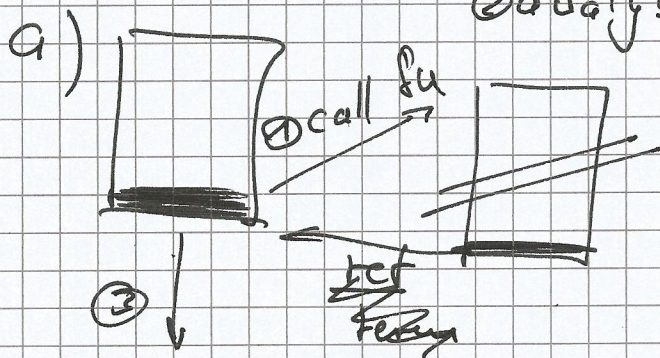
9

analysis()

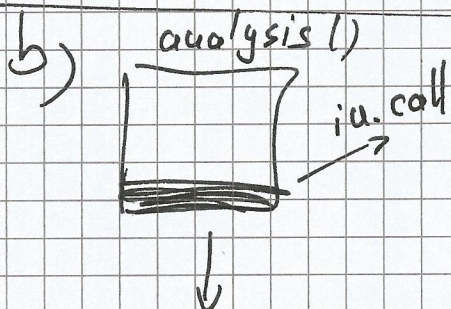
or

liveness principle

analysis(fu)



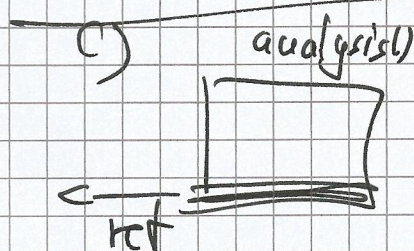
invoke analysis
and merge horizontal
as if it was
with result of analysis
(fu)



=> feru

ours

set all testable
to unknown
(system/ABI!)

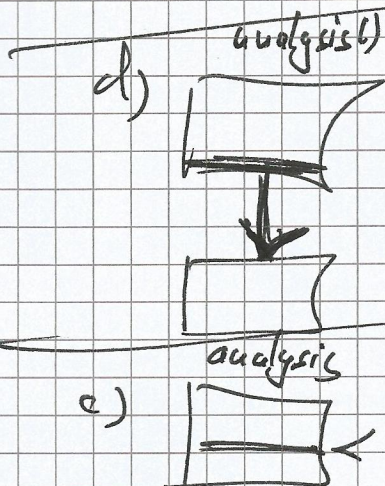


=> feru

set all param. regs
to unknown

ours

set all ~~param~~
testable
to unknown
(system/ABI)



=> simply follow

uop => ignore ours

const-write xor rax, rax

=> write before read

part of vararg passthrough

=> ignore

read > write if looking at one
register in one instr

neg rax => RW rax => Read-64-W