Bit 32 (in each 32-bit data content)
0: synaptic weight (on-core)
1: axon event (off-core)

HBM data section

| 1 | | 1 | | 1 | | 1 | | 1 | | 0 | | 0 | | 0 | |

| 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 |

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Intermediate FIFOs

FIFO arbitrator

Properties
- For now, the upper bit in each 32-bit data content in the HBM data section indicates if the event is local to the core (synaptic weight) or should be sent off core (output axon event).
- Output axon events get funneled out a single FIFO, and there is currently no special behavior defined for the FIFO arbitration.
- Off-core events should be sent to the Command Interpreter (or, eventually, to the Router).

Output Axon Event FIFO