Assembler-level computer sketch (All registers and memory locations are 4 bytes wide)

	- Register I	bank -		Stack		Data memory
R0	0x0006	1_	A1 0x0000	1077 Caller Ir	0x0000007F	
R1			A2	Caller r4		
R2			A3			
R3	0x0006	+	A4			
R4			V1			
R5			V2			
R6			V3			
R7			V4/WR			
R8			V5			
R9			V6/SB			
R10			V7/SL			
R11			V8/FP			
R12			IP			
R13	0x1064		SP			
R14			LR			
R15	0x0008		PC			
]			
CSPR						
			_			
			\neg	•		
0	0	0	0			
0						
N		C				
		O	V			
IR [
"`\[
MAR			0x0000	1000		
,				:		
MDR					0x00000000	0x0006

COMMENTS:

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