Assembler-level computer sketch (All registers and memory locations are 4 bytes wide)

	Register bank			Stack		Data memory
R0		A ²	0x00001077	Caller Ir	0x000007F	
R1		A2	2 0x0000106	Caller r4		
R2		A3	3 0x00001063			
R3	0x0006	A4	0x0000105F			
R4	Caller r4	V′				
R5		V2	2			
R6		V3	3			
R7		V4	l/WR			
R8		V	5			
R9		V	S/SB			
R10		V7	7/SL			
R11		V8	B/FP			
R12		IP				
R13	0x1068 SP					
R14	Caller Ir LR					
R15	0x0010 PC					
•						
CSPR						
\angle						
		•				
0	0	0	0			
			V			
N	Z	С	V			
ın [
IR [
MAR			0x00001000			
MDR					0x00000000	0x0006

COMMENTS:

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