Assembly programmer's model of a computer (All registers and memory locations are 4 bytes wide)

	Register bank			Stack		Data memory
R0	0x0006	A1	0x1074	CALLER-Ir	0x007C	
R1		A2	0x1070	CALLER-r4		
R2	0X0006	A3	0x106C	0X0006		
R3	0XFFFA	A4	0x1068		↓	
R4		V1	0x1064			
R5		V2	0x1060			
R6		V3	0x105C			
R7		V4/WR				
R8		V5				
R9		V6/SB				
R10		V7/SL				
R11		V8/FP				
R12		IP				
R13	0x00001065	SP				
R14	0X0015	LR				
R15	0X0008	PC				
0000		ı .				
CSPR						
0		0				
N = 1	Z = 0 $C = 0$	V = 0				
IR]				
ш		J			0x000C	•
MAR]	0x1000		0x0008	
		_			0x0004	
MDR					0x0000	0X0006
Processor core Memory hierarchy						

COMMENTS: