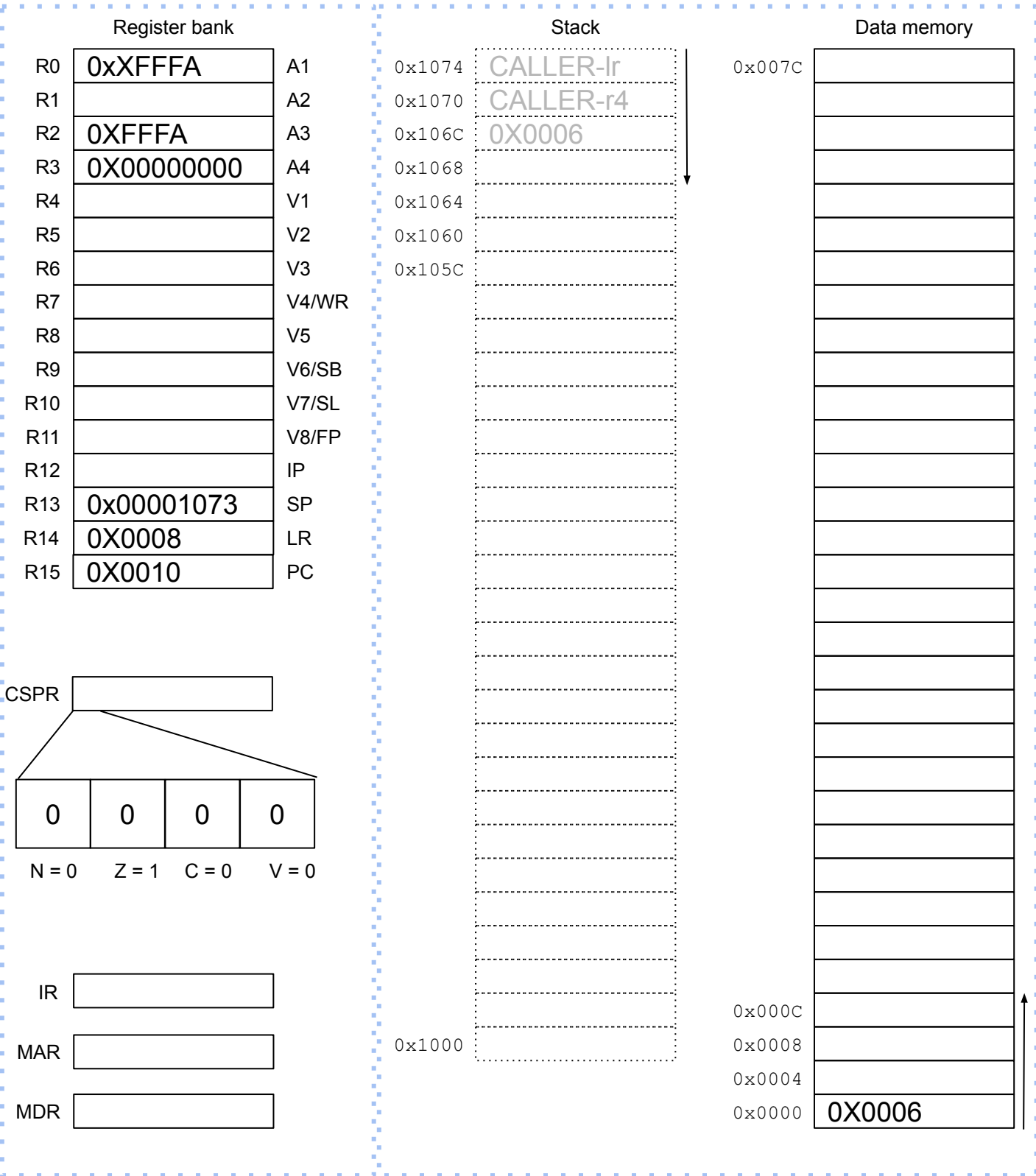


Assembly programmer's model of a computer

(All registers and memory locations are 4 bytes wide)



Processor core

Memory hierarchy

COMMENTS: