Assembler-level computer sketch (All registers and memory locations are 4 bytes wide)

	- Register ba	ank -			Stack		Data memory
R0	R0		A1 0x00001	1077	R4	0x0000007F	
R1			A2		R14/LR		
R2			A3		R0 = R3		
R3	R3 = #0 -	.L6	A4			\downarrow	
R4			V1			•	
R5			V2				
R6			V3				
R7			V4/WR				
R8			V5				
R9			V6/SB				
R10			V7/SL				
R11			V8/FP				
R12			IP				
R13			SP				
R14			LR				
R15			PC				
0000							
CSPR							
					<u> </u>		
			$\neg \neg$				
0	0	0	0				
0							
N	Z	C	V		<u></u>		
.,	_	Ū	•				
IR							
MAR			0x00001	1000			
MDR						0x00000000	.L6 = #6

COMMENTS:

· · · · · Processor · · · ·