

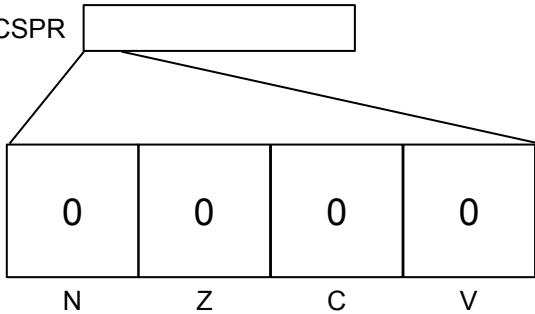
Assembler-level computer sketch

(All registers and memory locations are 4 bytes wide)

Register bank

R0	R0 = R3	A1	0x00001077
R1		A2	
R2	#0 - .L6	A3	
R3	#0	A4	
R4		V1	
R5		V2	
R6		V3	
R7		V4/WR	
R8		V5	
R9		V6/SB	
R10		V7/SL	
R11		V8/FP	
R12		IP	
R13		SP	
R14		LR	
R15		PC	

CSPR



IR

MAR

MDR

0x00001000

Stack

R4
R14/LR

0x0000007F

Data memory

.L6 = #6

Processor

COMMENTS: