Assembler-level computer sketch (All registers and memory locations are 4 bytes wide)

	- Register ba	ank · · ·			Stack		Data memory
R0		T A	\1 0x00001	077 R4		0x000007F	
R1		- A	\ 2	R14	/LR		
R2		- A	\ 3				
R3	.L6	- A	\ 4			↓	
R4		\	/1			•	
R5		\	/2				
R6		\	/3				
R7		\	/4/WR	•			
R8		\	/5				
R9		\	/6/SB				
R10		\	/7/SL				
R11		\	/8/FP				
R12		1	Р				
R13			SP				
R14		L	.R				
R15		F	PC .				
cepp [Compare D2 t	0					
CSPR Compare R3 to 0							
			_				
		Ι		•			
0	0	0	0	•			
0							
N	Z	C		•			
11	_	O	V				
IR [
[
MAR			0x00001	1000			
MDR						0x00000000	.L6 = #6

COMMENTS:

· · · · · · Processor · · · · · · ·