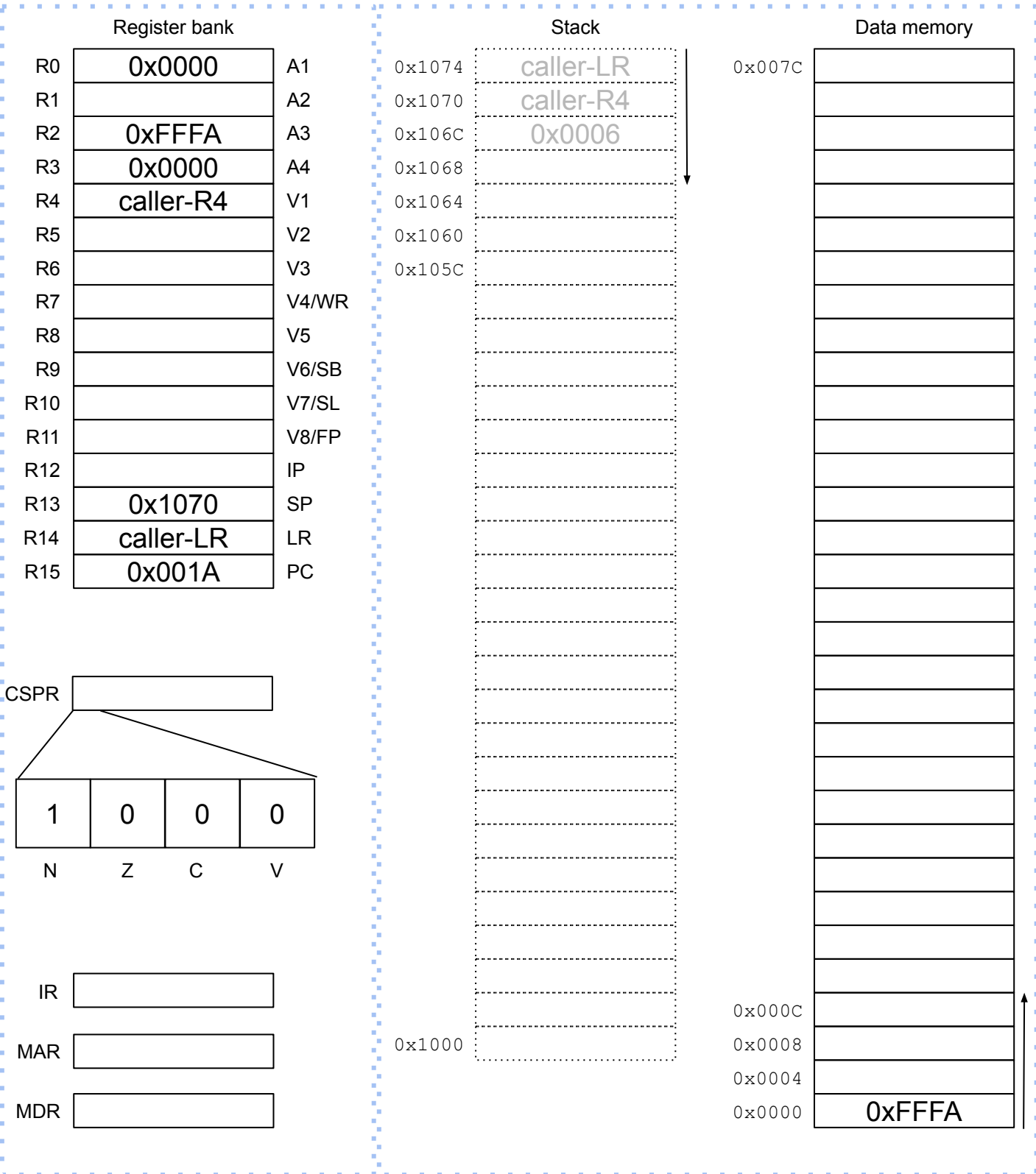


Assembly programmer's model of a computer

(All registers and memory locations are 4 bytes wide)



Processor core

Memory hierarchy

COMMENTS:

Has just finished moving the value of zero into both R0 and R3 and is about to pop PC and R4 out of the stack.