Assembler-level computer sketch (All registers and memory locations are 4 bytes wide)

	Register ba	ank		Stack		Data memory
R0	0x0000		A1 0x00001074	Caller-LR	0x000007F	
R1			A 2	Caller-R4		
R2		/	A 3	0x0006		
R3	0xFFFA		\4		↓	
R4	0x0000	\	/1			
R5	Caller-R4	\	/2	:		
R6		\	/3	:		
R7		\	/4/WR			
R8		\	/5			
R9		\	/6/SB			
R10		\	/7/SL			
R11		\	/8/FP			
R12			Р			
R13	0x1070		SP			
R14	Caller-LR	l l	_R			
R15	0x001A	F	PC			
CSPR 7						
		<u> </u>				
0	1	0	0			
	 	C	V			
N	Z	C	V			
IR [
iix [
MAR			0x00001000			
ļ						
MDR					0x0000000	0xFFFA
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COMMENTS:

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