Assembler-level computer sketch
(All registers and memory locations are 4 bytes wide)

Register bank

R0	0x0006	A1	0x00001074
R1		A2	
R2		A3	
R3	0xFFFA	A4	
R4	Caller-R4	V1	
R5		V2	
R6		V3	
R7		V4/WR	
R8		V5	
R9		V6/SB	
R10		V7/SL	
R11		V8/FP	
R12		IP	
R13	0x1068	SP	
R14	Caller-LR	LR	
R15	0x0008	PC	

CSPR

1	0	0	0
N	Z	C	V

IR

MAR

MDR

Processor

Stack

0x00001074 Caller-LR
Caller-R4
0x0006

0x00001000

Data memory

0x0000007F

0x00000000 0x0006

COMMENTS: