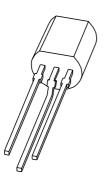
DISCRETE SEMICONDUCTORS

DATA SHEET



BSN304 N-channel enhancement mode vertical D-MOS transistor

Product specification Supersedes data of 1997 Jun 17 2001 Dec 11





N-channel enhancement mode vertical D-MOS transistor

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FEATURES

- Direct interface to C-MOS, TTL, etc.
- · High-speed switching
- No secondary breakdown.

APPLICATIONS

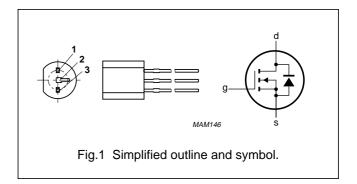
- Line current interruptor in telephone sets
- Relay, high-speed and line transformer drivers.

DESCRIPTION

N-channel enhancement mode vertical D-MOS transistor in a TO-92 variant package.

PINNING - TO-92 variant

PIN	DESCRIPTION
1	gate
2	drain
3	source



QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{DS}	drain-source voltage (DC)		_	300	V
I _D	drain current (DC)		_	300	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	_	1	W
V_{GSO}	gate-source voltage	open drain	_	±20	V
R _{DSon}	drain-source on-state resistance	$I_D = 250 \text{ mA}; V_{GS} = 10 \text{ V}$	_	6	Ω
V_{GSoff}	gate-source cut-off voltage	$I_D = 1 \text{ mA}; V_{GS} = V_{DS}$	0.8	2	V

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	PARAMETER CONDITIONS		MAX.	UNIT
V _{DS}	drain-source voltage (DC)		_	300	V
V _{GSO}	gate-source voltage (DC)	open drain	_	±20	V
I _D	drain current (DC)		_	300	mA
I _{DM}	peak drain current		_	1.2	Α
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	1	W
T _{stg}	storage temperature		-55	+150	°C
Tj	operating junction temperature		_	150	°C

Note

1. Device mounted on an epoxy printed-circuit board, maximum lead length 4 mm; mounting pad for the drain lead minimum 10 mm x 10 mm.

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient; note 1	125	K/W

Note

1. Device mounted on an epoxy printed-circuit board, maximum lead length 4 mm; mounting pad for the drain lead minimum 10 mm x 10 mm.

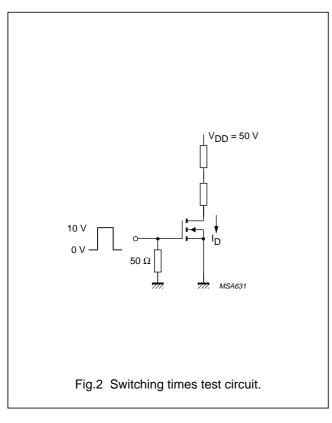
STATIC CHARACTERISTICS

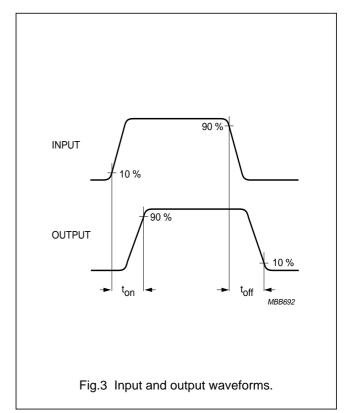
 $T_i = 25$ °C unless otherwise specified.

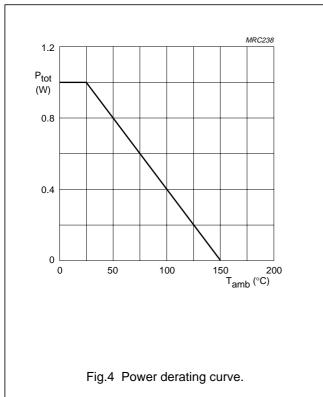
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _{(BR)DSS}	drain-source breakdown voltage	$I_D = 10 \mu A; V_{GS} = 0$	300	_	_	٧
I _{GSS}	gate-source leakage current	$V_{GS} = \pm 20 \text{ V}; V_{DS} = 0$	_	_	±100	nA
V _{GSth}	gate-source threshold voltage	$I_D = 1 \text{ mA}; V_{DS} = V_{GS}$	0.8	_	2	V
R _{DSon}	drain-source on-state resistance	$I_D = 250 \text{ mA}; V_{GS} = 10 \text{ V}$	-	3.7	6	Ω
		$I_D = 20 \text{ mA}; V_{GS} = 2.4 \text{ V}$	_	4.8	10	Ω
I _{DSS}	drain-source leakage current	V _{DS} = 240 V; V _{GS} = 0	Ī-	_	100	nA
Y _{fs}	transfer admittance	I _D = 250 mA; V _{DS} = 25 V	200	690	_	mS
C _{iss}	input capacitance	V _{DS} = 25 V; V _{GS} = 0; f = 1 MHz	Ī-	100	120	pF
C _{oss}	output capacitance	V _{DS} = 25 V; V _{GS} = 0; f = 1 MHz	Ī-	21	30	pF
C _{rss}	feedback capacitance	V _{DS} = 25 V; V _{GS} = 0; f = 1 MHz	-	10	15	pF
Switching tir	nes (see Figs 2 and 3)		•			
t _{on}	turn-on time	$I_D = 250 \text{ mA}; V_{DD} = 50 \text{ V};$ $V_{GS} = 0 \text{ to } 10 \text{ V}$	_	6	10	ns
t _{off}	turn-off time	$I_D = 250 \text{ mA}; V_{DD} = 50 \text{ V};$ $V_{GS} = 10 \text{ to } 0 \text{ V}$	_	46	60	ns

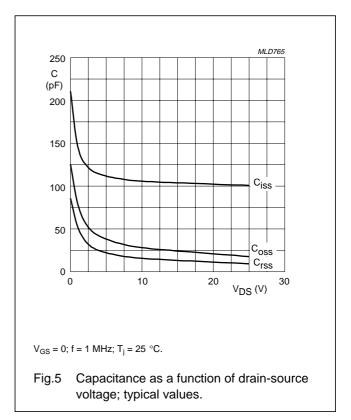
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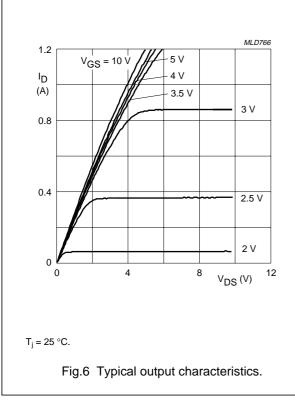


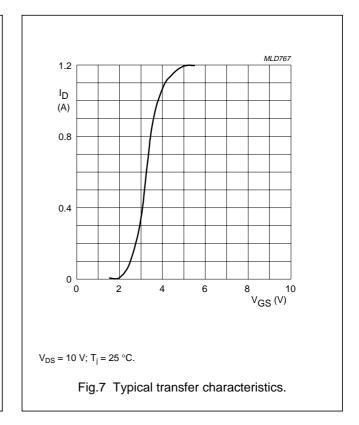


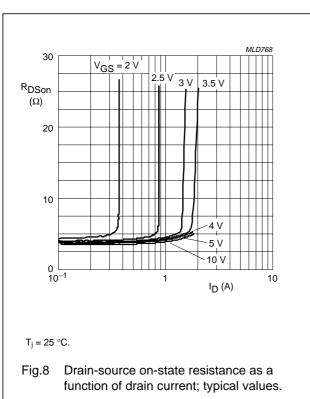


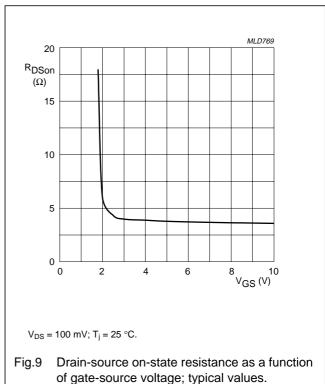
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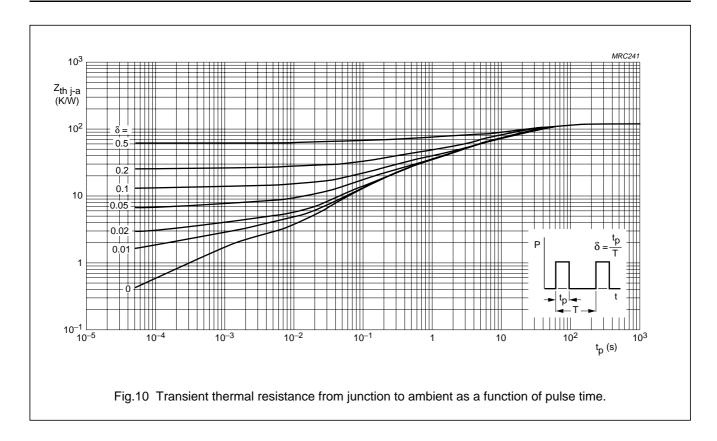


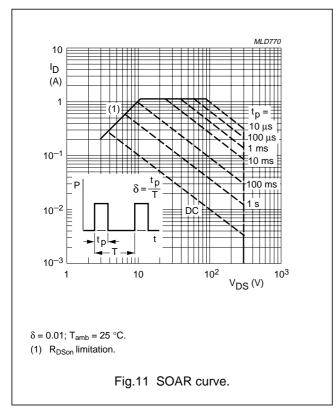




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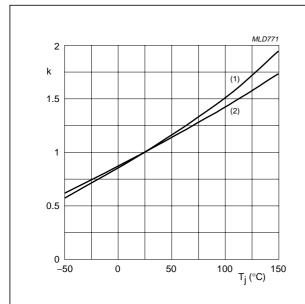
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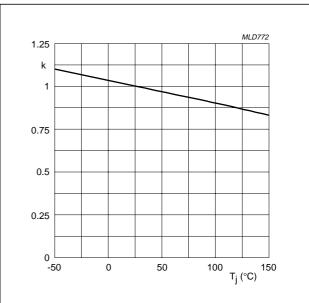


$$k \, = \, \frac{R_{DS(on)} \, \, at \, T_j}{R_{DS(on)} \, \, at \, 25 \, {}^{\circ}C}$$

Typical R_{DSon};

- (1) $I_D = 250 \text{ mA}$; $V_{GS} = 10 \text{ V}$.
- (2) $I_D = 20 \text{ mA}$; $V_{GS} = 2.4 \text{ V}$.

Fig.12 Temperature coefficient of drain-source on-state resistance; typical values.



$$k \, = \, \frac{V_{GS(th)} \, \, at \, T_j}{V_{GS(th)} \, \, at \, 25 \, \, ^{\circ}C}. \label{eq:kappa}$$

Fig.13 Temperature coefficient of gate-source threshold voltage; typical values.

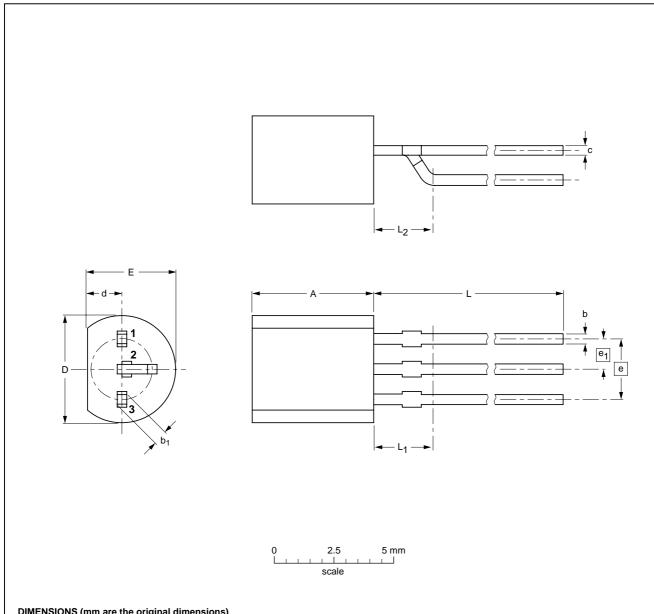
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PACKAGE OUTLINES

Plastic single-ended leaded (through hole) package; 3 leads (on-circle)

SOT54 variant



DIMENSIONS (mm are the original dimensions)

UNIT	Α	b	b ₁	С	D	d	E	е	e ₁	L	L ₁ ⁽¹⁾ max	L ₂ max
mm	5.2 5.0	0.48 0.40	0.66 0.56	0.45 0.40	4.8 4.4	1.7 1.4	4.2 3.6	2.54	1.27	14.5 12.7	2.5	2.5

Notes

1. Terminal dimensions within this zone are uncontrolled to allow for flow of plastic and terminal irregularities.

OUTLINE		REFER	ENCES	EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	EIAJ	PROJECTION	ISSUE DATE
SOT54 variant		TO-92 variant	SC-43		98-03-26

2001 Dec 11 8

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NOTES

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Contact information

For additional information please visit http://www.semiconductors.philips.com. Fax: +31 40 27 24825 For sales offices addresses send e-mail to: sales.addresses@www.semiconductors.philips.com.

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