

## CD4028BM/CD4028BC BCD-to-Decimal Decoder

### General Description

The CD4028BM/CD4028BC is a BCD-to-decimal or binary-to-octal decoder consisting of 4 inputs, decoding logic gates, and 10 output buffers. A BCD code applied to the 4 inputs, A, B, C, and D, results in a high level at the selected 1-of-10 decimal decoded outputs. Similarly, a 3-bit binary code applied to inputs A, B, and C is decoded in octal at outputs 0–7. A high level signal at the D input inhibits octal decoding and causes outputs 0–7 to go low.

All inputs are protected against static discharge damage by diode clamps to  $V_{DD}$  and  $V_{SS}$ .

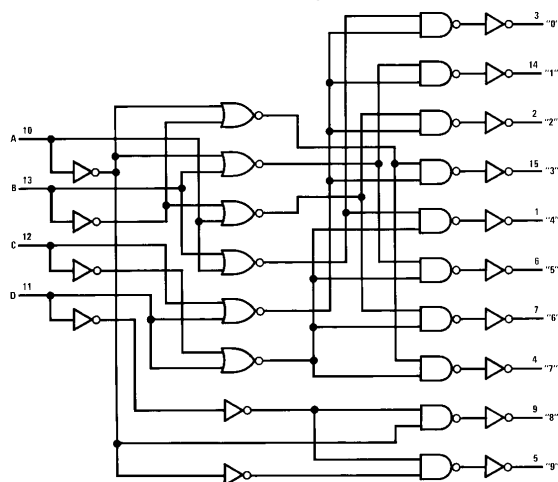
### Features

- Wide supply voltage range 3.0V to 15V
- High noise immunity 0.45  $V_{DD}$  (typ.)
- Low power TTL compatibility fan out of 2 driving 74L or 1 driving 74LS
- Low power
- Glitch free outputs
- "Positive logic" on inputs and outputs

### Applications

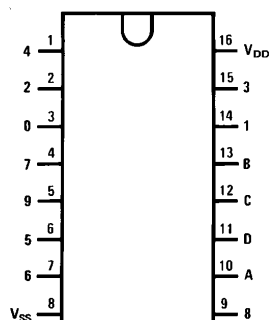
- Code conversion
- Address decoding
- Indicator-tube decoder

### Logic and Connection Diagrams



TL/F/5959-1

### Dual-In-Line Package



TL/F/5959-2

### Top View

Order Number CD4028B

### Truth Table

1 = High Level  
0 = Low Level

D	C	B	A	0	1	2	3	4	5	6	7	8	9
0	0	0	0	1	0	0	0	0	0	0	0	0	0
0	0	0	1	0	1	0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0	0	0	0	0	0	0
0	0	1	1	0	0	0	1	0	0	0	0	0	0
0	1	0	0	0	0	0	0	1	0	0	0	0	0
0	1	0	1	0	0	0	0	0	1	0	0	0	0
0	1	1	0	0	0	0	0	0	0	1	0	0	0
0	1	1	1	0	0	0	0	0	0	0	1	0	0
1	0	0	0	0	0	0	0	0	0	0	0	1	0
1	0	0	1	0	0	0	0	0	0	0	0	0	1
1	0	1	0	0	0	0	0	0	0	0	0	1	0
1	0	1	1	0	0	0	0	0	0	0	0	0	1
1	1	0	0	0	0	0	0	0	0	0	0	0	1
1	1	0	1	0	0	0	0	0	0	0	0	0	1
1	1	1	0	0	0	0	0	0	0	0	0	1	0
1	1	1	1	0	0	0	0	0	0	0	0	0	1

BCD States

Extraordinary States

**Absolute Maximum Ratings** (Notes 1 & 2)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage ( $V_{DD}$ )	− 0.5 to + 18V
Input Voltage ( $V_{IN}$ )	− 0.5 to $V_{DD}$ + 0.5V
Storage Temperature Range ( $T_S$ )	− 65°C to + 150°C
Power Dissipation ( $P_D$ )	
Dual-In-Line	700 mW
Small Outline	500 mW
Lead Temperature ( $T_L$ )	
(Soldering, 10 seconds)	260°C

**Recommended Operating Conditions** (Note 2)

Supply Voltage ( $V_{DD}$ )	3 to 15V
Input Voltage ( $V_{IN}$ )	0 to $V_{DD}$ V
Operating Temperature Range ( $T_A$ )	
CD4028BM	− 55°C to + 125°C
CD4028BC	− 40°C to + 185°C

**DC Electrical Characteristics** CD4028BC (Note 2)

Symbol	Parameter	Conditions	− 55°C		+ 25°C			+ 125°C		Units
			Min	Max	Min	Typ	Max	Min	Max	
$I_{DD}$	Quiescent Device Current	$V_{DD} = 5V, V_{IN} = V_{DD}$ or $V_{SS}$		5		0.01	5		150	$\mu A$
		$V_{DD} = 10V, V_{IN} = V_{DD}$ or $V_{SS}$		10		0.01	10		300	$\mu A$
		$V_{DD} = 15V, V_{IN} = V_{DD}$ or $V_{SS}$		20		0.02	20		600	$\mu A$
$V_{OL}$	Low Level Output Voltage	$ I_O  < 1 \mu A, V_{IL} = 0V, V_{IH} = V_{DD}$								
		$V_{DD} = 5V$		0.05		0	0.05		0.05	V
		$V_{DD} = 10V$		0.05		0	0.05		0.05	V
		$V_{DD} = 15V$		0.05		0	0.05		0.05	V
$V_{OH}$	High Level Output Voltage	$ I_O  < 1 \mu A, V_{IL} = 0V, V_{IH} = V_{DD}$								
		$V_{DD} = 5V$	4.95		4.95	5		4.95		V
		$V_{DD} = 10V$	9.95		9.95	10		9.95		V
		$V_{DD} = 15V$	14.95		14.95	15		14.95		V
$V_{IL}$	Low Level Input Voltage	$ I_O  < 1 \mu A$								
		$V_{DD} = 5V, V_O = 0.5V$ or $4.5V$		1.5		2.25	1.5		1.5	V
		$V_{DD} = 10V, V_O = 1V$ or $9V$		3.0		4.5	3.0		3.0	V
		$V_{DD} = 15V, V_O = 1.5V$ or $13.5V$		4.0		6.75	4.0		4.0	V
$V_{IH}$	High Level Input Voltage	$ I_O  < 1 \mu A$								
		$V_{DD} = 5V, V_O = 0.5V$ or $4.5V$	3.5		3.5	2.75		3.5		V
		$V_{DD} = 10V, V_O = 1V$ or $9V$	7.0		7.0	5.5		7.0		V
		$V_{DD} = 15V, V_O = 1.5V$ or $13.5V$	11.0		11.0	8.25		11.0		V
$I_{OL}$	Low Level Output Current (Note 3)	$V_{IL} = 0V, V_{IH} = V_{DD}$								
		$V_{DD} = 5V, V_O = 0.4V$	0.64		0.51	1.0		0.36		mA
		$V_{DD} = 10V, V_O = 0.5V$	1.6		1.3	2.6		0.9		mA
		$V_{DD} = 15V, V_O = 1.5V$	4.2		3.4	6.8		2.4		mA
$I_{OH}$	High Level Output Current (Note 3)	$V_{IL} = 0V, V_{IH} = V_{DD}$								
		$V_{DD} = 5V, V_O = 4.6V$	− 0.25		− 0.2	− 0.4		− 0.14		mA
		$V_{DD} = 10V, V_O = 9.5V$	− 0.62		− 0.5	− 1.0		− 0.35		mA
		$V_{DD} = 15V, V_O = 13.5V$	− 1.8		− 1.5	− 3.0		− 1.1		mA
$I_{IN}$	Input Current	$V_{DD} = 15V, V_{IN} = 0V$		− 0.1		$-10^{-5}$	− 0.1		− 1.0	$\mu A$
		$V_{DD} = 15V, V_{IN} = 15V$		0.1		$10^{-5}$	0.1		1.0	$\mu A$

**DC Electrical Characteristics** CD4028BC (Note 2)

Symbol	Parameter	Conditions	− 40°C		+ 25°C			+ 85°C		Units
			Min	Max	Min	Typ	Max	Min	Max	
$I_{DD}$	Quiescent Device Current	$V_{DD} = 5V, V_{IN} = V_{DD}$ or $V_{SS}$		20		0.01	20		150	$\mu A$
		$V_{DD} = 10V, V_{IN} = V_{DD}$ or $V_{SS}$		40		0.01	40		300	$\mu A$
		$V_{DD} = 15V, V_{IN} = V_{DD}$ or $V_{SS}$		80		0.02	80		600	$\mu A$
$V_{OL}$	Low Level Output Voltage	$ I_O  < 1 \mu A, V_{IL} = 0V, V_{IH} = V_{DD}$								
		$V_{DD} = 5V$		0.05		0	0.05		0.05	V
		$V_{DD} = 10V$		0.05		0	0.05		0.05	V
		$V_{DD} = 15V$		0.05		0	0.05		0.05	V

## DC Electrical Characteristics CD4028BC (Note 2) (Continued)

Symbol	Parameter	Conditions	−40°C		+25°C			+85°C		Units
			Min	Max	Min	Typ	Max	Min	Max	
V <sub>OH</sub>	High Level Output Voltage	I <sub>O</sub>   < 1 μA, V <sub>IL</sub> = 0V, V <sub>IH</sub> = V <sub>DD</sub> V <sub>DD</sub> = 5V V <sub>DD</sub> = 10V V <sub>DD</sub> = 15V	4.95 9.95 14.95		4.95 9.95 14.95	5 10 15		4.95 9.95 14.95		V V V
V <sub>IL</sub>	Low Level Input Voltage	I <sub>O</sub>   < 1 μA V <sub>DD</sub> = 5V, V <sub>O</sub> = 0.5V or 4.5V V <sub>DD</sub> = 10V, V <sub>O</sub> = 1V or 9V V <sub>DD</sub> = 15V, V <sub>O</sub> = 1.5V or 13.5V		1.5 3.0 4.0		2.25 4.5 6.75	1.5 3.0 4.0		1.5 3.0 4.0	V V V
V <sub>IH</sub>	High Level Input Voltage	I <sub>O</sub>   < 1 μA V <sub>DD</sub> = 5V, V <sub>O</sub> = 0.5V or 4.5V V <sub>DD</sub> = 10V, V <sub>O</sub> = 1V or 9V V <sub>DD</sub> = 15V, V <sub>O</sub> = 1.5V or 13.5V	3.5 7.0 11.0		3.5 7.0 11.0			3.5 7.0 11.0		V V V
I <sub>OL</sub>	Low Level Output Current (Note 3)	V <sub>IH</sub> = V <sub>DD</sub> , V <sub>IL</sub> = 0V V <sub>DD</sub> = 5V, V <sub>O</sub> = 0.4V V <sub>DD</sub> = 10V, V <sub>O</sub> = 0.5V V <sub>DD</sub> = 15V, V <sub>O</sub> = 1.5V	0.52 1.3 3.6		0.44 1.1 3.0	0.88 2.2 6.0		0.36 0.9 2.4		mA mA mA
I <sub>OH</sub>	High Level Output Current (Note 3)	V <sub>IH</sub> = V <sub>DD</sub> , V <sub>IL</sub> = 0V V <sub>DD</sub> = 5V, V <sub>O</sub> = 4.6V V <sub>DD</sub> = 10V, V <sub>O</sub> = 9.5V V <sub>DD</sub> = 15V, V <sub>O</sub> = 13.5V	−0.2 −0.5 −1.4		−0.16 −0.4 −1.2	−0.32 −0.8 −3.5		−0.12 −0.3 −1.0		mA mA mA
I <sub>IN</sub>	Input Current	V <sub>DD</sub> = 15V, V <sub>IN</sub> = 0V V <sub>DD</sub> = 15V, V <sub>IN</sub> = 15V		−0.3 0.3			−0.3 0.3		−1.0 1.0	μA μA

## AC Electrical Characteristics\*

T<sub>A</sub> = 25°C, C<sub>L</sub> = 50 pF, R<sub>L</sub> = 200k, Input t<sub>r</sub> = t<sub>f</sub> = 20 ns, unless otherwise specified

Symbol	Parameter	Conditions	Min	Typ	Max	Units
t <sub>PHL</sub> or t <sub>PLH</sub>	Propagation Delay Time	V <sub>CC</sub> = 5V V <sub>CC</sub> = 10V V <sub>CC</sub> = 15V		240 100 70	480 200 140	ns ns ns
t <sub>THL</sub> or t <sub>TLH</sub>	Transition Time	V <sub>CC</sub> = 5V V <sub>CC</sub> = 10V V <sub>CC</sub> = 15V		175 75 60	350 150 110	ns ns ns
C <sub>IN</sub>	Input Capacitance	Any Input		5	7.5	pF

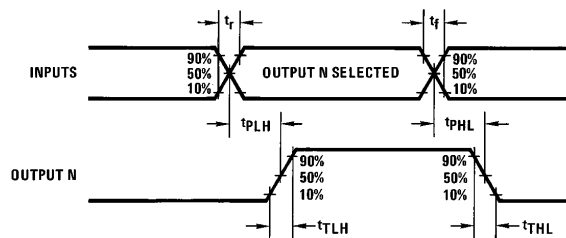
\* AC Parameters are guaranteed by DC correlated testing.

**Note 1:** "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed, they are not meant to imply that the devices should be operated at these limits. The table of "Recommended Operating Conditions" and "Electrical Characteristics" provides conditions for actual device operation.

**Note 2:** V<sub>SS</sub> = 0V unless otherwise specified.

**Note 3:** I<sub>OL</sub> and I<sub>OH</sub> are tested one output at a time.

## Switching Time Waveforms



TL/F/5959-3



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CD4028BCJ - <http://www.ti.com/product/cd4028bcj?HQS=TI-null-null-dscatalog-df-pf-null-ww>

CD4028 - <http://www.ti.com/product/cd4028?HQS=TI-null-null-dscatalog-df-pf-null-ww>

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