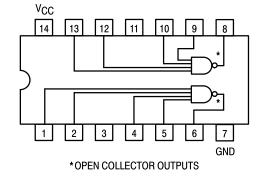


# **DUAL 4-INPUT NAND GATE**

# SN54/74LS22

# DUAL 4-INPUT NAND GATE LOW POWER SCHOTTKY





J SUFFIX CERAMIC CASE 632-08



N SUFFIX PLASTIC CASE 646-06



D SUFFIX SOIC CASE 751A-02

### **ORDERING INFORMATION**

SN54LSXXJ SN74LSXXN SN74LSXXD Ceramic Plastic SOIC

#### **GUARANTEED OPERATING RANGES**

Symbol	Parameter		Min	Тур	Max	Unit
VCC	Supply Voltage	54 74	4.5 4.75	5.0 5.0	5.5 5.25	V
TA	Operating Ambient Temperature Range	54 74	-55 0	25 25	125 70	°C
Vон	Output Voltage — High	54, 74			55	V
loL	Output Current — Low	54 74			4.0 8.0	mA

# SN54/74LS22

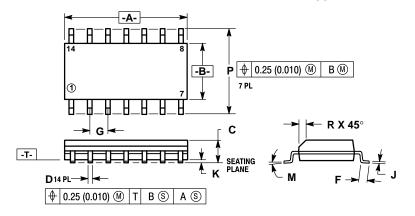
## DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

			Limits					
Symbol	Parameter		Min	Тур	Max	Unit	Test Co	onditions
VIH	Input HIGH Voltage		2.0			V	Guaranteed Input HIGH Voltage for All Inputs	
.,	Input LOW Voltage	54			0.7	V	Guaranteed Input LOW Voltage for All Inputs	
VIL		74			0.8	V		
VIK	Input Clamp Diode Voltage			-0.65	-1.5	V	V <sub>CC</sub> = MIN, I <sub>IN</sub> = -18 mA	
IOH	Output HIGH Current	54, 74			100	μΑ	V <sub>CC</sub> = MIN, V <sub>OH</sub> = MAX	
Voi	Output LOW Voltage	54, 74		0.25	0.4	V	$I_{OL} = 4.0 \text{ mA}$ $V_{CC} = V_{CC} \text{ MI}$	$V_{CC} = V_{CC} MIN,$ $V_{IN} = V_{IL} \text{ or } V_{IH}$
VOL		74		0.35	0.5	V	I <sub>OL</sub> = 8.0 mA	per Truth Table
l	Land I II O I I O I I I I I I I I I I I I I				20	μΑ	$V_{CC} = MAX, V_{IN} = 2.7 V$	
ΊΗ	Input HIGH Current				0.1	mA	V <sub>CC</sub> = MAX, V <sub>IN</sub> = 7.0 V	
I <sub>IL</sub>	Input LOW Current				-0.4	mA	V <sub>CC</sub> = MAX, V <sub>IN</sub> = 0.4 V	
Icc	Power Supply Current Total, Output HIGH				0.8	mA	V <sub>CC</sub> = MAX	
	Total, Output LOW				2.2			

## AC CHARACTERISTICS ( $T_A = 25^{\circ}C$ )

		Limits		Limits			
Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions	
<sup>t</sup> PLH	Turn-Off Delay, Input to Output		17	32	ns	V <sub>CC</sub> = 5.0 V	
tPHL	Turn-On Delay, Input to Output		15	28	ns	$C_L$ = 15 pF, $R_L$ = 2.0 kΩ	

#### Case 751A-02 D Suffix 14-Pin Plastic **SO-14**



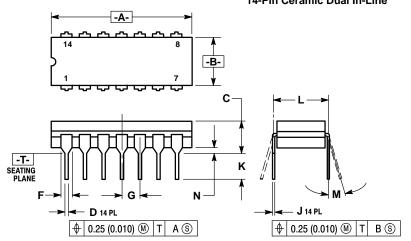
#### NOTES:

- DIMENSIONS "A" AND "B" ARE DATUMS AND
  "T" IS A DATUM SURFACE.

  "T" IS A DATUM SURFACE.
- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: MILLIMETER.
  DIMENSION A AND B DO NOT INCLUDE MOLD PROTRUSION.
  MAXIMUM MOLD PROTRUSION 0.15 (0.006)
- 6. 751A-01 IS OBSOLETE, NEW STANDARD 751A-02.

	MILLIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	8.55	8.75	0.337	0.344	
В	3.80 4.00		0.150	0.157	
С	1.35 1.75		0.054	0.068	
D	0.35 0.49		0.014	0.019	
F	0.40	1.25	0.016	0.049	
G	1.27 BSC		0.050 BSC		
J	0.19	0.25	0.008	0.009	
K	0.10	0.10 0.25		0.009	
M	0°	7°	0°	7°	
P	5.80	6.20	0.229	0.244	
R	0.25	0.50	0.010	0.019	

#### Case 632-08 J Suffix 14-Pin Ceramic Dual In-Line

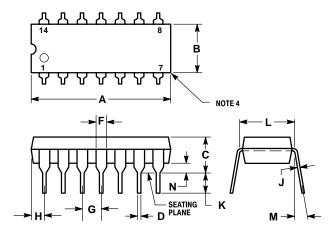


- IOLES:
  1. DIMENSIONING AND TOLERANCING PER ANSI
  Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
  3. DIMENSION L TO CENTER OF LEAD WHEN
  FORMED PARALLEL.
  4. DIM F MAY NARROW TO 0.76 (0.030) WHERE

- THE LEAD ENTERS THE CERAMIC BODY.
  5. 632-01 THRU -07 OBSOLETE, NEW STANDARD

	MILLIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	19.05	19.94	0.750	0.785	
В	6.23	7.11	0.245	0.280	
С	3.94	5.08	0.155	0.200	
D	0.39 0.50		0.015	0.020	
F	1.40	1.65	0.055	0.065	
G	2.54	BSC	0.100 BSC		
J	0.21	0.38	0.008	0.015	
K	3.18	4.31	0.125	0.170	
L	7.62 BSC		0.300 BSC		
М	0°	15°	0°	15°	
N	0.51	1.01	0.020	0.040	

#### Case 646-06 N Suffix 14-Pin Plastic



- NOTES:

  1. LEADS WITHIN 0.13 mm (0.005) RADIUS OF TRUE POSITION AT SEATING PLANE AT MAXIMUM MATERIAL CONDITION.

  2. DIMENSION "L" TO CENTER OF LEADS WHEN FORMED PARALLEL.

  3. DIMENSION "B" DOES NOT INCLUDE MOLD ELACH.
- FLASH
- ROUNDED CORNERS OPTIONAL. 646-05 OBSOLETE, NEW STANDARD 646-06.

	MILLIM	ETERS	INCHES			
DIM	MIN	MAX	MIN	MAX		
Α	18.16	19.56	0.715	0.770		
В	6.10	6.60	0.240	0.260		
С	3.69	4.69	0.145	0.185		
D	0.38	0.53	0.015	0.021		
F	1.02	1.78	0.040	0.070		
G	2.54 BSC		0.100 BSC			
H	1.32	2.41	0.052	0.095		
J	0.20	0.38	0.008	0.015		
K	2.92	3.43	0.115	0.135		
٦	7.62 BSC		0.300 BSC			
M	0°	10°	0°	10°		
N	0.39	1.01	0.015	0.039		

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