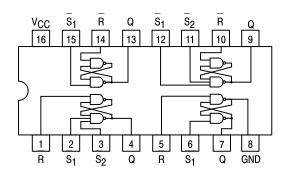


QUAD SET-RESET LATCH



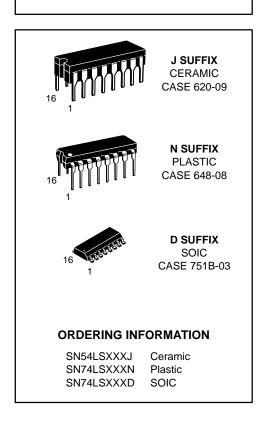
TRUTH TABLE

	INPU	OUTPUT	
S ₁	S ₂	R	(Q)
L	L	L	h
L	Х	Н	Н
X	L	Н	Н
Н	Н	L	L
Н	Н	Н	No Change

- L = LOW Voltage Level
- H = HIGH Voltage Level
- X = Don't Care
- h = The output is HIGH as long as S₁ or S₂ is LOW. If all inputs go HIGH simultaneously, the output state is indeterminate; otherwise, it follows the Truth Table

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QUAD SET-RESET LATCH LOW POWER SCHOTTKY



GUARANTEED OPERATING RANGES

Symbol	Parameter		Min	Тур	Max	Unit
Vcc	Supply Voltage	54 74	4.5 4.75	5.0 5.0	5.5 5.25	V
T _A	Operating Ambient Temperature Range	54 74	-55 0	25 25	125 70	°C
loн	Output Current — High	54, 74			-0.4	mA
l _{OL}	Output Current — Low	54 74			4.0 8.0	mA

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DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

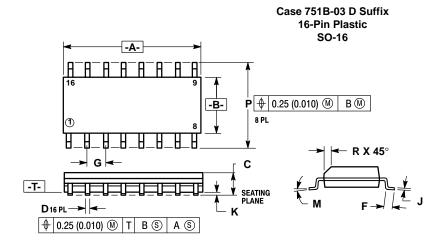
			Limits					
Symbol	Parameter		Min	Тур	Max	Unit	Tes	t Conditions
VIH	Input HIGH Voltage		2.0			V	Guaranteed Input HIGH Voltage for All Inputs	
V Lazari LOWI Valla an		54			0.7	V	Guaranteed Input LOW Voltage for	
VIL	Input LOW Voltage	74			0.8	l v	All Inputs	
VIK	Input Clamp Diode Voltage			-0.65	-1.5	V	V _{CC} = MIN, I _{IN} = -18 mA	
V Output HIGH Value re		54	2.5	3.5		V	V _{CC} = MIN, I _{OH} = MAX, V _{IN} = V _{IH}	
VOH	Output HIGH Voltage	74	2.7	3.5		V	or V _{IL} per Truth Table	
Vo	Output LOW Voltage	54, 74		0.25	0.4	V	I _{OL} = 4.0 mA	$V_{CC} = V_{CC} MIN,$ $V_{IN} = V_{IL} \text{ or } V_{IH}$
VOL	Output LOW Voltage			0.35	0.5	V	I _{OL} = 8.0 mA	per Truth Table
1					20	μА	V _{CC} = MAX, V _{IN} = 2.7 V	
lΉ	Input HIGH Current				0.1	mA	V _{CC} = MAX, V _{IN} = 7.0 V	
IIL	Input LOW Current				-0.4	mA	V _{CC} = MAX, V _{IN} = 0.4 V	
los	Short Circuit Current (Note 1)		-20		-100	mA	V _{CC} = MAX	
ICC	Power Supply Current				7.0	mA	V _{CC} = MAX	

Note 1: Not more than one output should be shorted at a time, nor for more than 1 second.

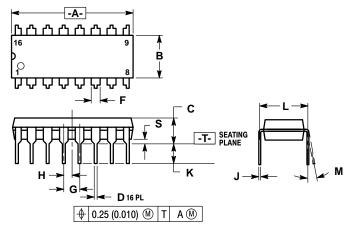
AC CHARACTERISTICS $(T_A = 25^{\circ}C)$

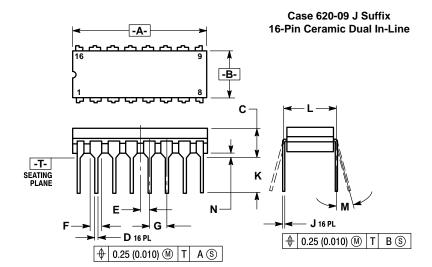
		Limits		Limits			
Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions	
^t PLH ^t PHL	Propagation Delay, S to Output			22 21*	ns	V _{CC} = 5.0 V C _I = 15 pF	
tPHL	Propagation Delay, R to Output			27	ns	OL = 13 βi	

^{*} Add 0.6 ns to spec limit for each 1.0 ns input rise time less than 15 ns.



Case 648-08 N Suffix 16-Pin Plastic





NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: MILLIMETER.
 DIMENSION A AND B DO NOT INCLUDE MOLD PROTRUSION.
 MAXIMUM MOLD PROTRUSION 0.15 (0.006)
- PER SIDE.
 751B-01 IS OBSOLETE, NEW STANDARD
 751B-03.

	MILLIM	ETERS	INC	HES		
DIM	MIN	MAX	MIN	MAX		
Α	9.80	10.00	0.386	0.393		
В	3.80	4.00	0.150	0.157		
С	1.35	1.75	0.054	0.068		
D	0.35	0.49	0.014	0.019		
F	0.40	1.25	0.016	0.049		
G	1.27	BSC	0.050	BSC		
J	0.19	0.25	0.008	0.009		
K	0.10	0.25	0.004	0.009		
M	0°	7°	0°	7°		
P	5.80	6.20	0.229	0.244		
R	0.25	0.50	0.010	0.019		

NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- TO THE STATE OF LEADS WHEN FORMED PARALLEL.
- DIMENSION "B" DOES NOT INCLUDE MOLD
- ROUNDED CORNERS OPTIONAL. 648-01 THRU -07 OBSOLETE, NEW STANDARD 648-08.

	MILLIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	18.80	19.55	0.740	0.770	
В	6.35	6.85	0.250	0.270	
С	3.69	4.44	0.145	0.175	
D	0.39	0.53	0.015	0.021	
F	1.02	1.77	0.040	0.070	
G	2.54	BSC	0.100 BSC		
Н	1.27	BSC	0.050 BSC		
J	0.21	0.38	0.008	0.015	
K	2.80	3.30	0.110	0.130	
L	7.50	7.74	0.295	0.305	
M	0°	10°	0°	10°	
S	0.51	1.01	0.020	0.040	

- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. DIMENSION L'TO CENTER OF LEAD WHEN FORMED PARALLEL.
 4. DIM F MAY NARROW TO 0.76 (0.030) WHERE THE LEAD ENTERS THE CERAMIC BODY.
 5. 620-01 THRU-08 OBSOLETE, NEW STANDARD 620-09.

	MILLIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	19.05	19.55	0.750	0.770	
В	6.10	7.36	0.240	0.290	
С	_	4.19	_	0.165	
D	0.39	0.53	0.015 0.02		
E	1.27	BSC	0.050 BSC		
F	1.40	1.77	0.055	0.070	
G	2.54	BSC	0.100 BSC		
J	0.23	0.27	0.009	0.011	
K	_	5.08	_	0.200	
L	7.62	BSC	0.300	BSC	
M	0°	15°	0°	15°	
N	0.39	0.88	0.015	0.035	

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