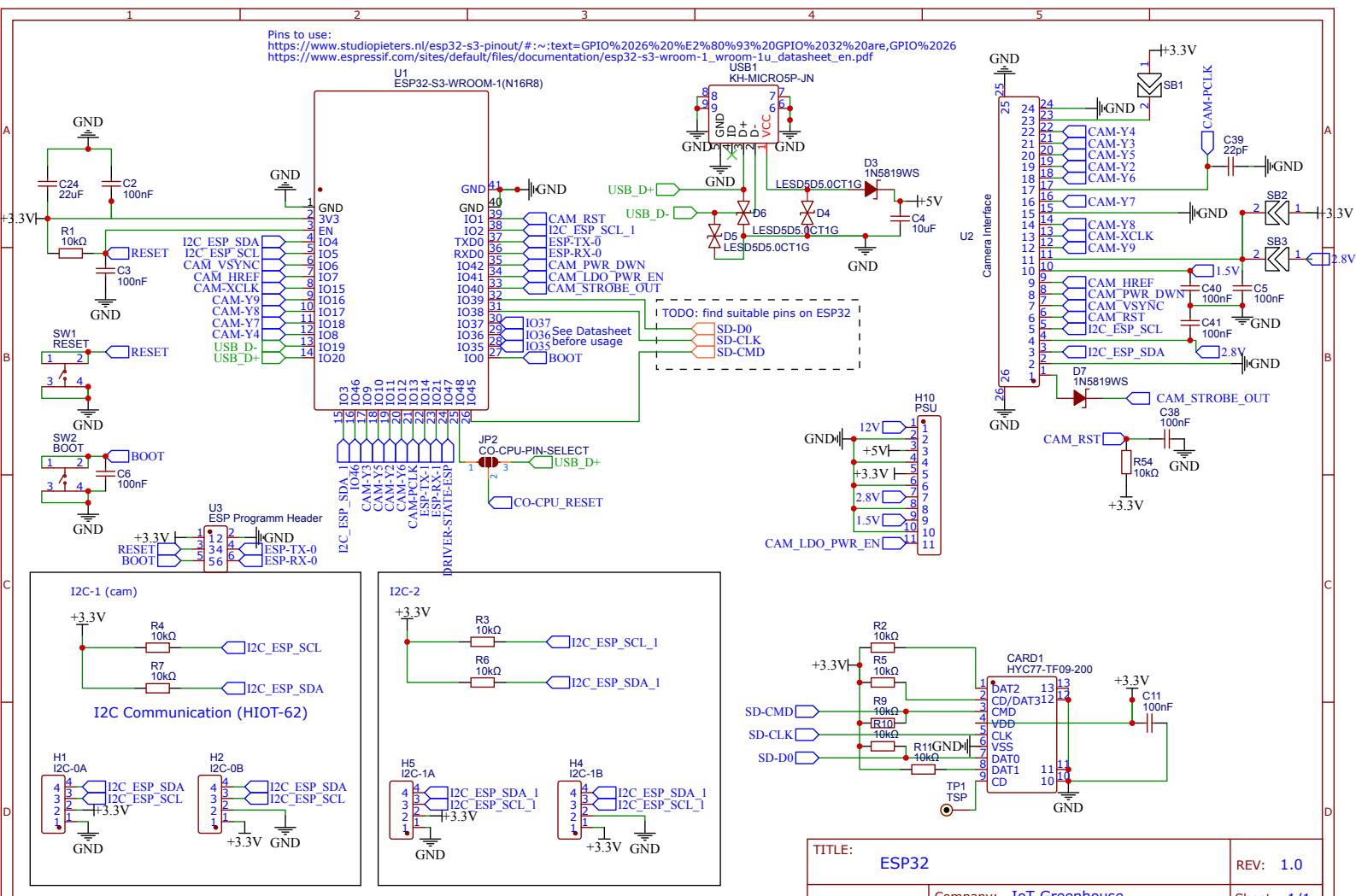
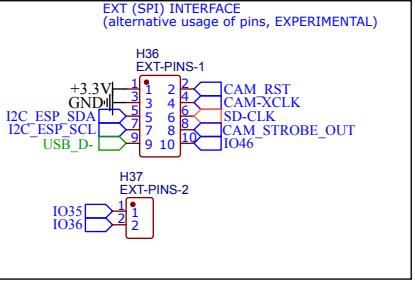


## Appendix A – ESP32-S3 Consolidated Pin Overview

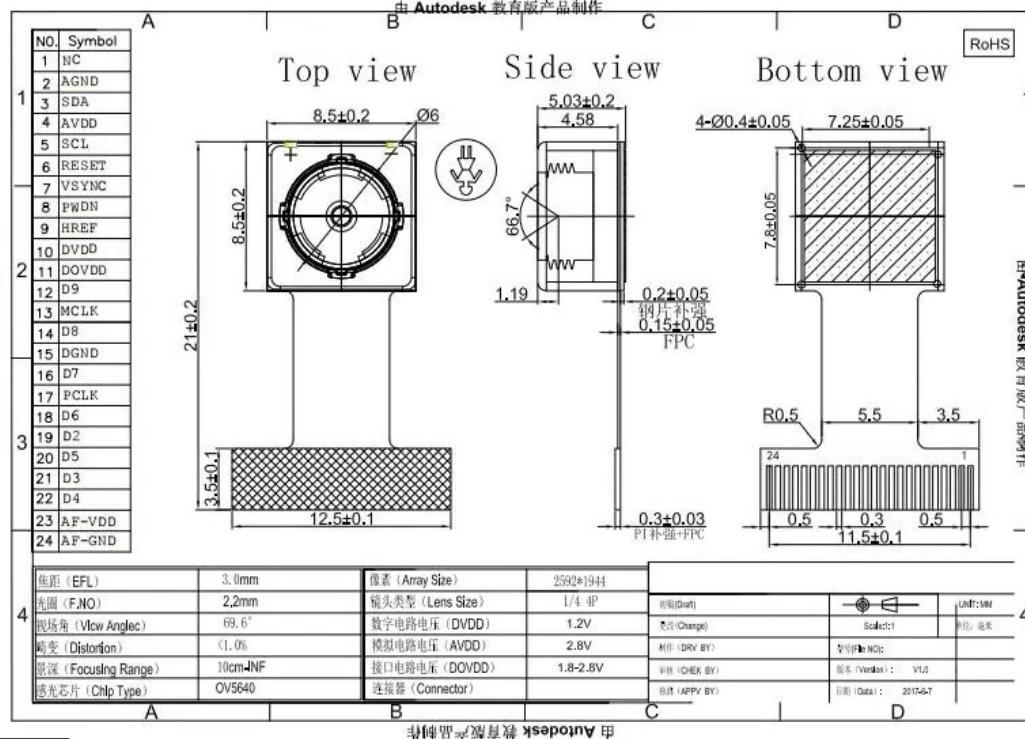
\* For details, see Section 2 Pins. Regarding highlighted cells, see Section 2.3.3 Restrictions for GPIOs and RTC\_GPIOs.

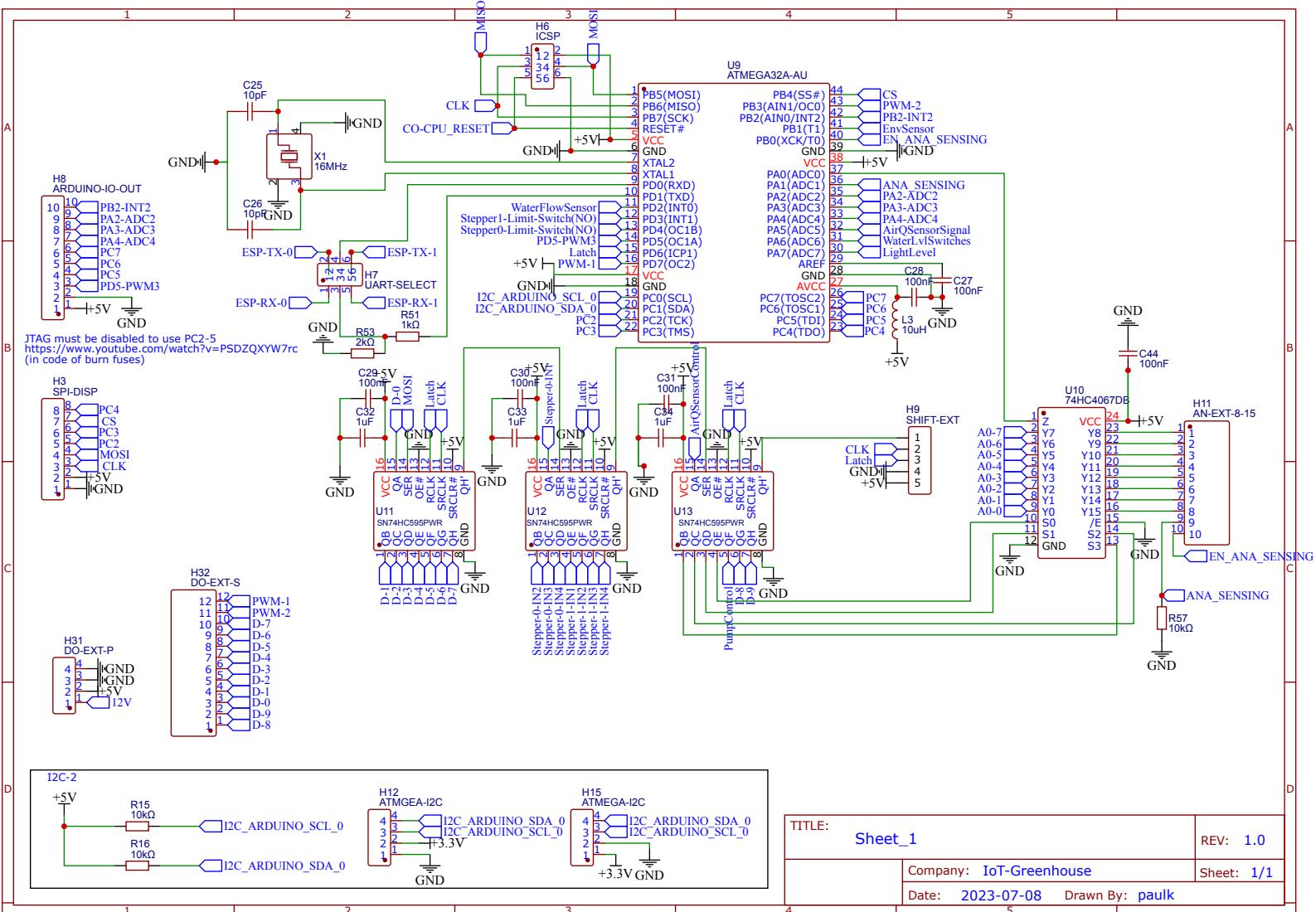


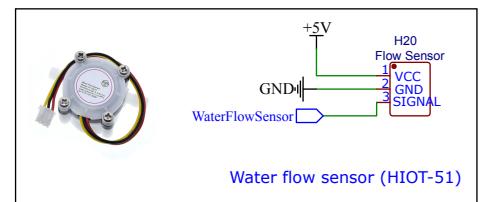
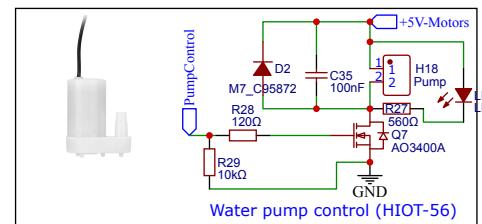
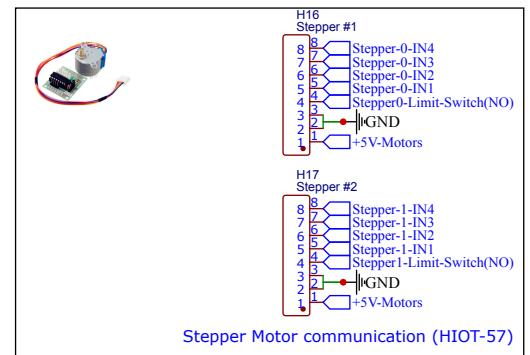
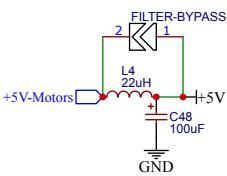
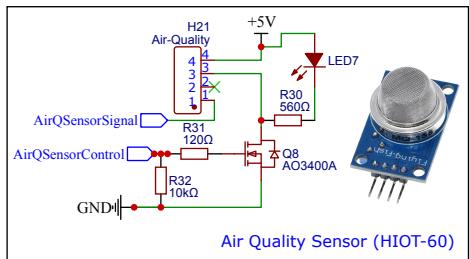
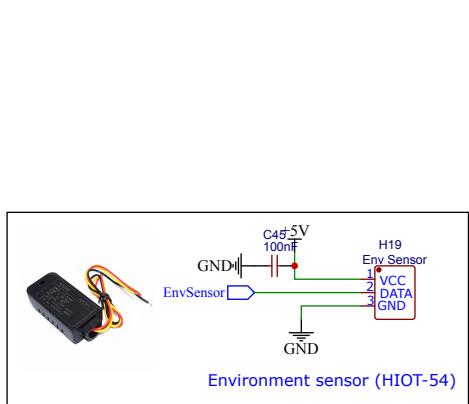


**TODO: Test pin IO47 / RS485 function on real ESP before production!**

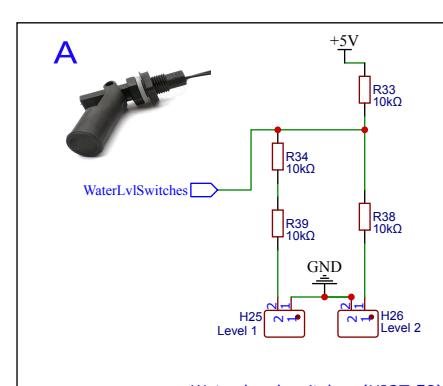
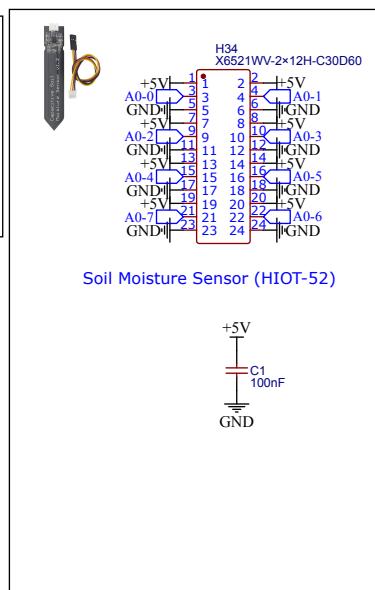
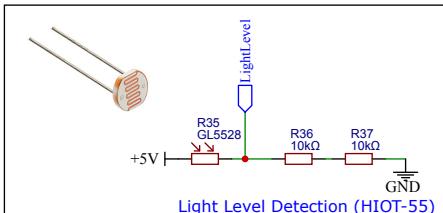
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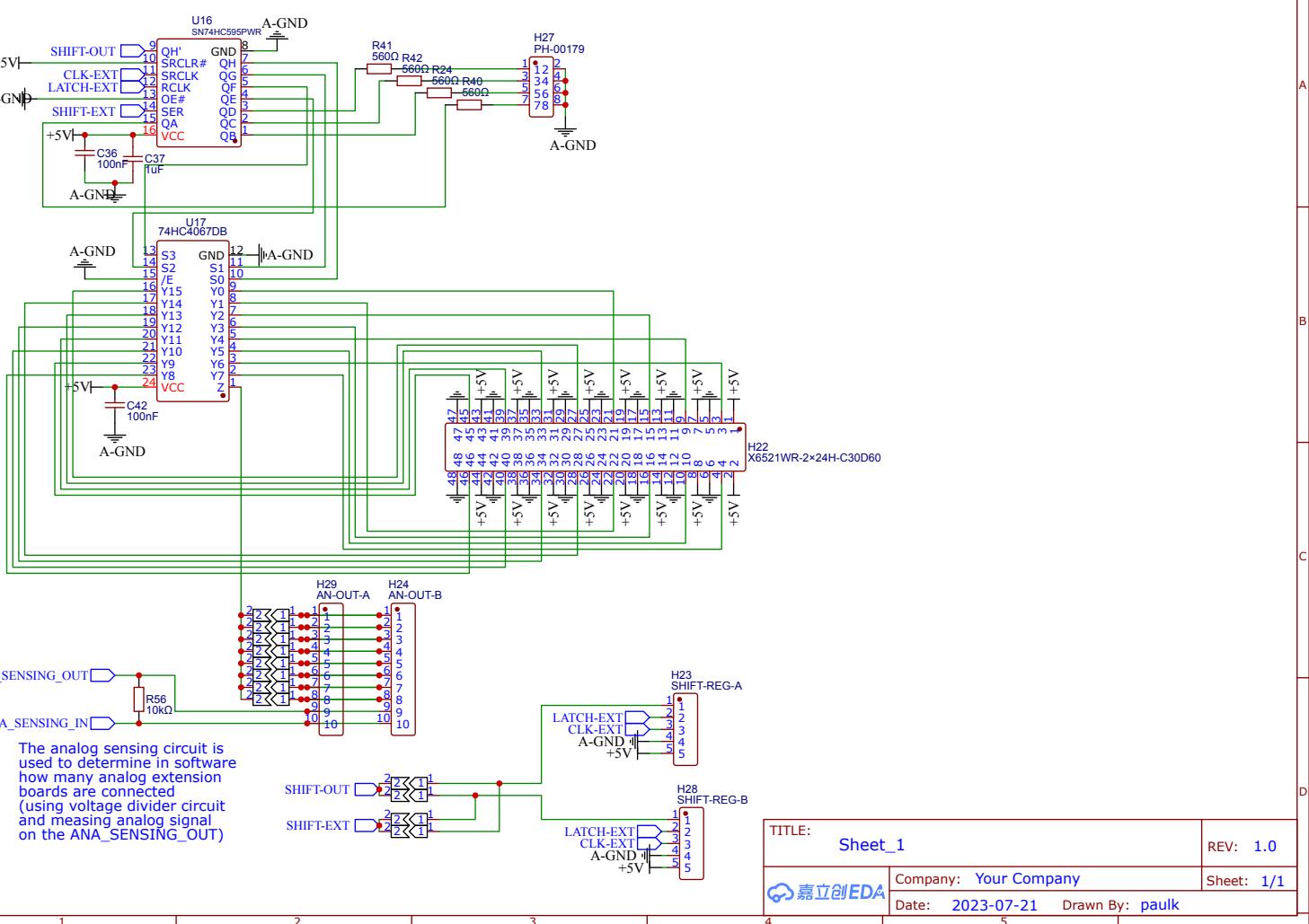




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Date:	2023-07-16	Drawn By: paulk

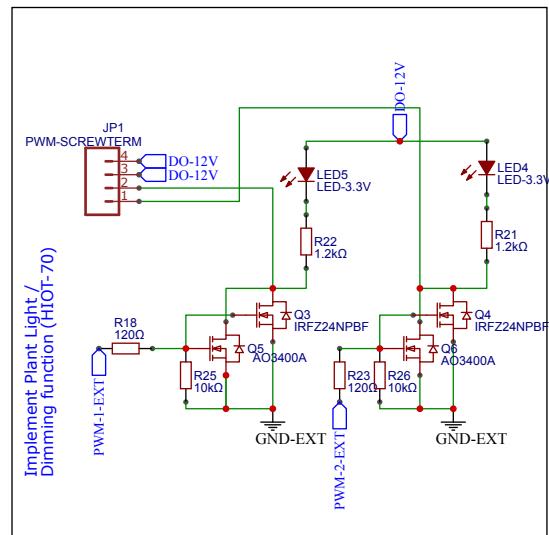
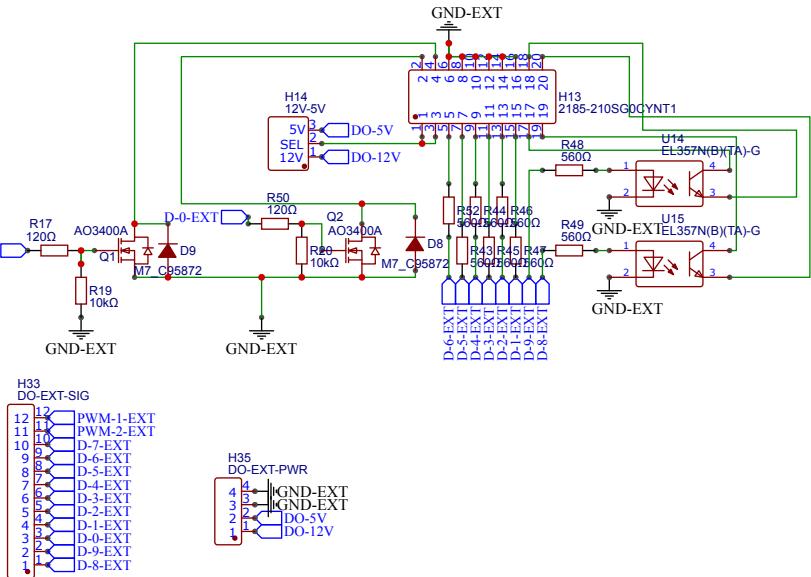


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Company:	IoT-Greenhouse	Sheet: 1/1
Date:	2023-07-16	Drawn By: paulk



TITLE: Sheet_1		REV: 1.0
嘉立创EDA	Company: Your Company	Sheet: 1/1

Date: 2023-07-21 Drawn By: paulk



**TITLE:** Sheet\_1



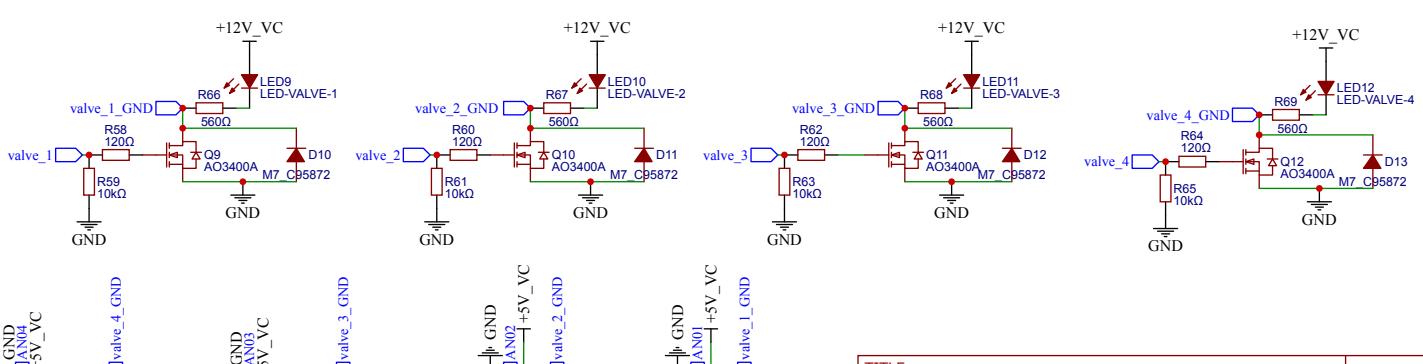
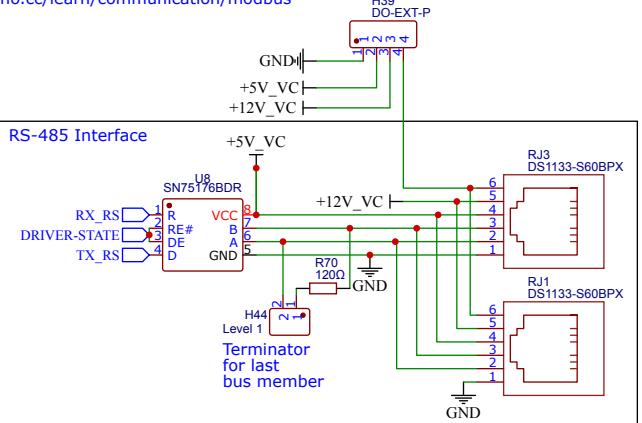
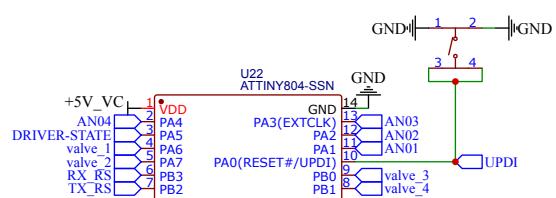
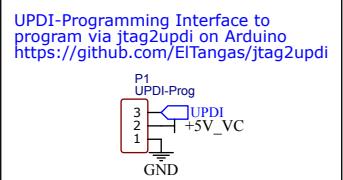
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REV: 1.0

Company: Your Company

Sheet: 1 / 1

Date: 2023-07-27 Drawn By: paulk



TITLE: 4 Valve control board		REV: 1.0
 EasyEDA	Company: Your Company	Sheet: 1/1