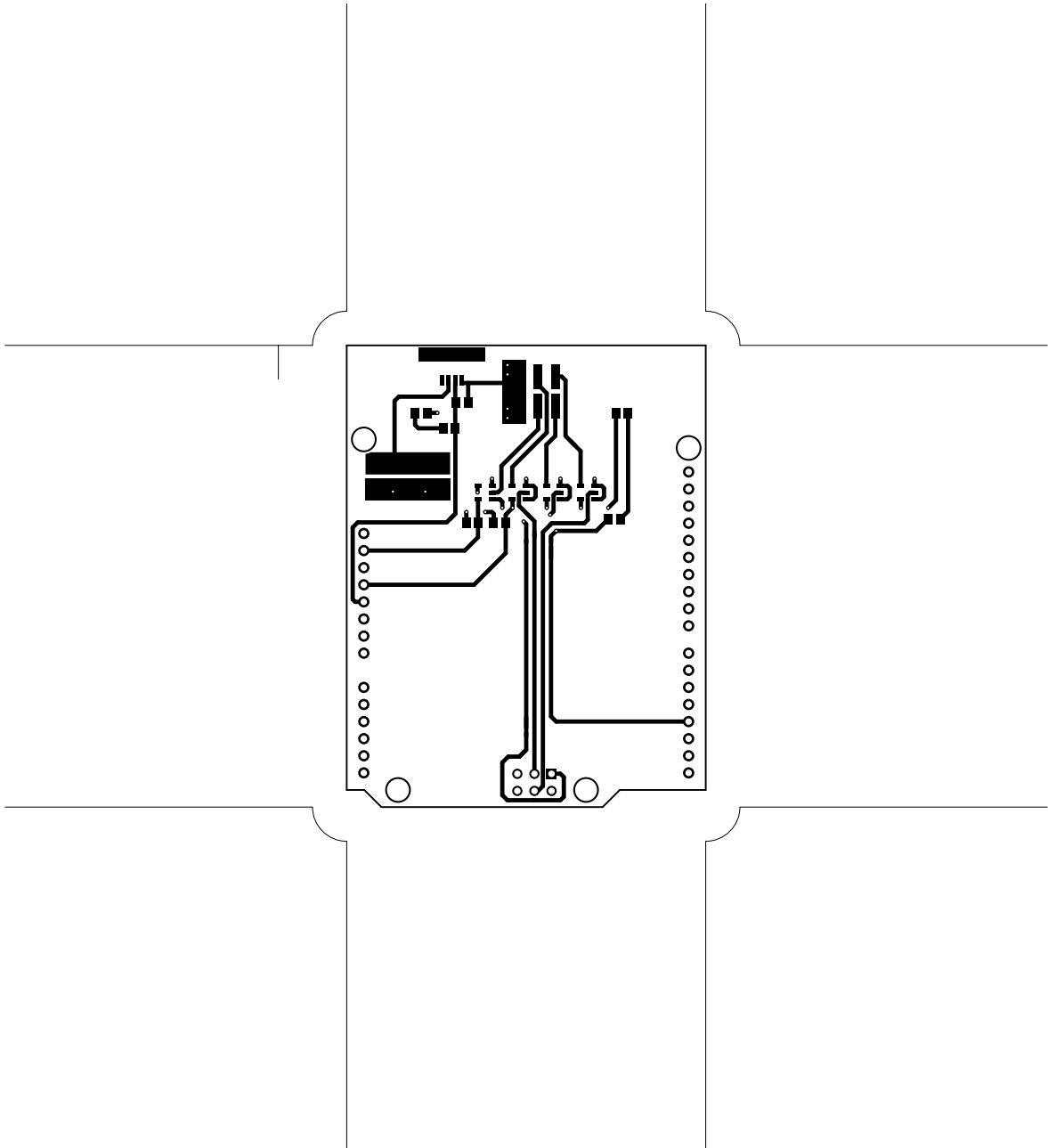
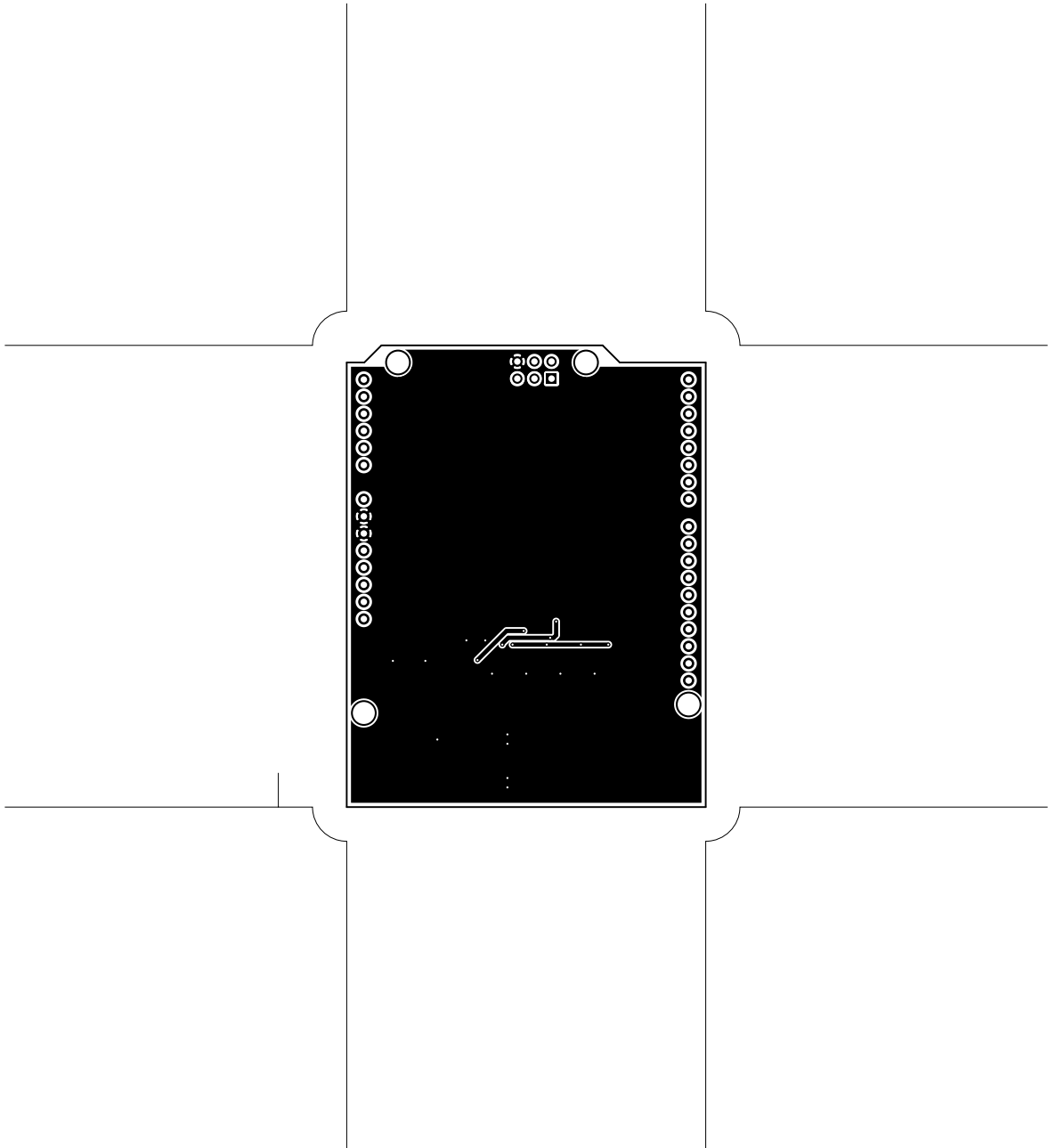
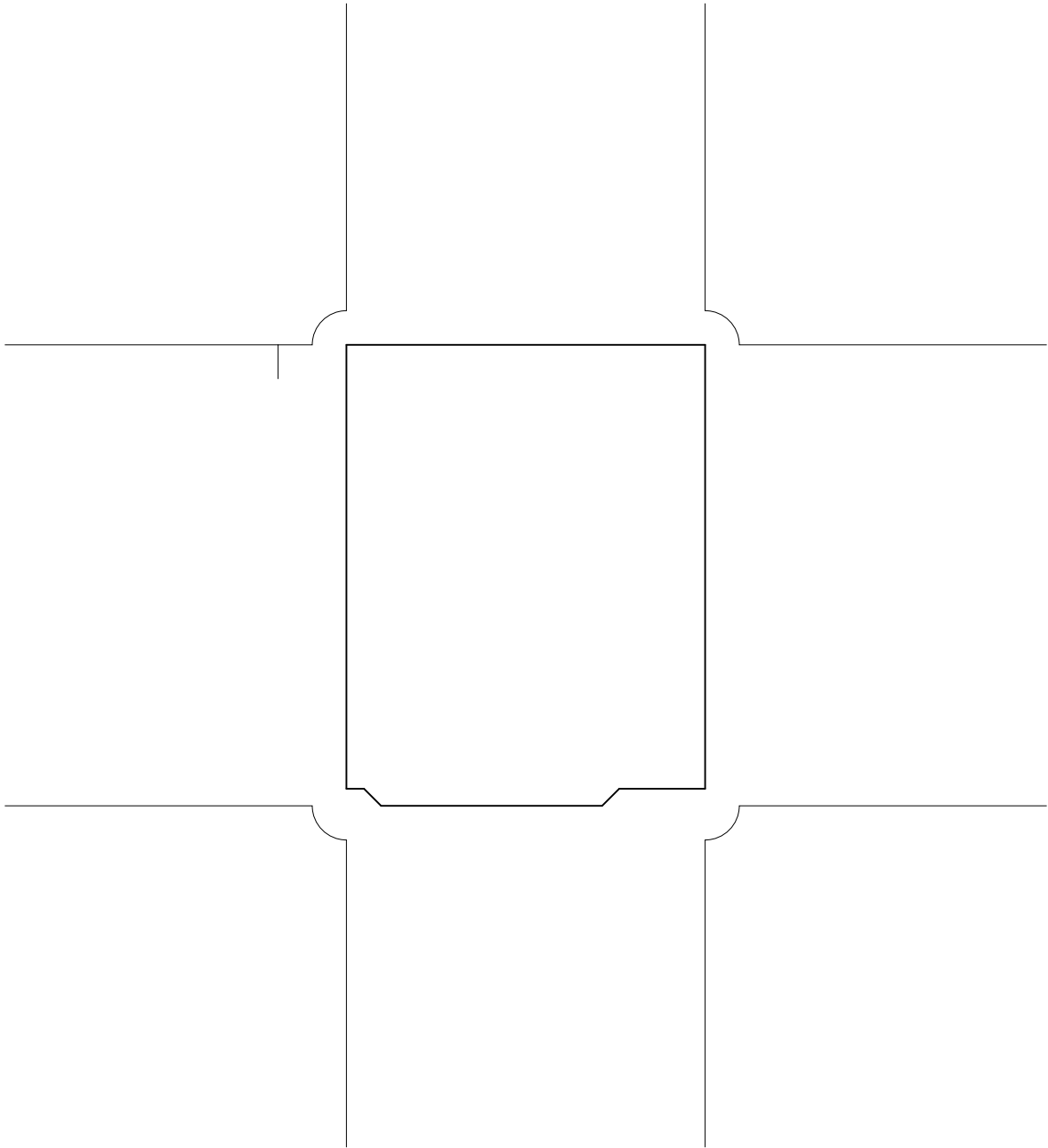


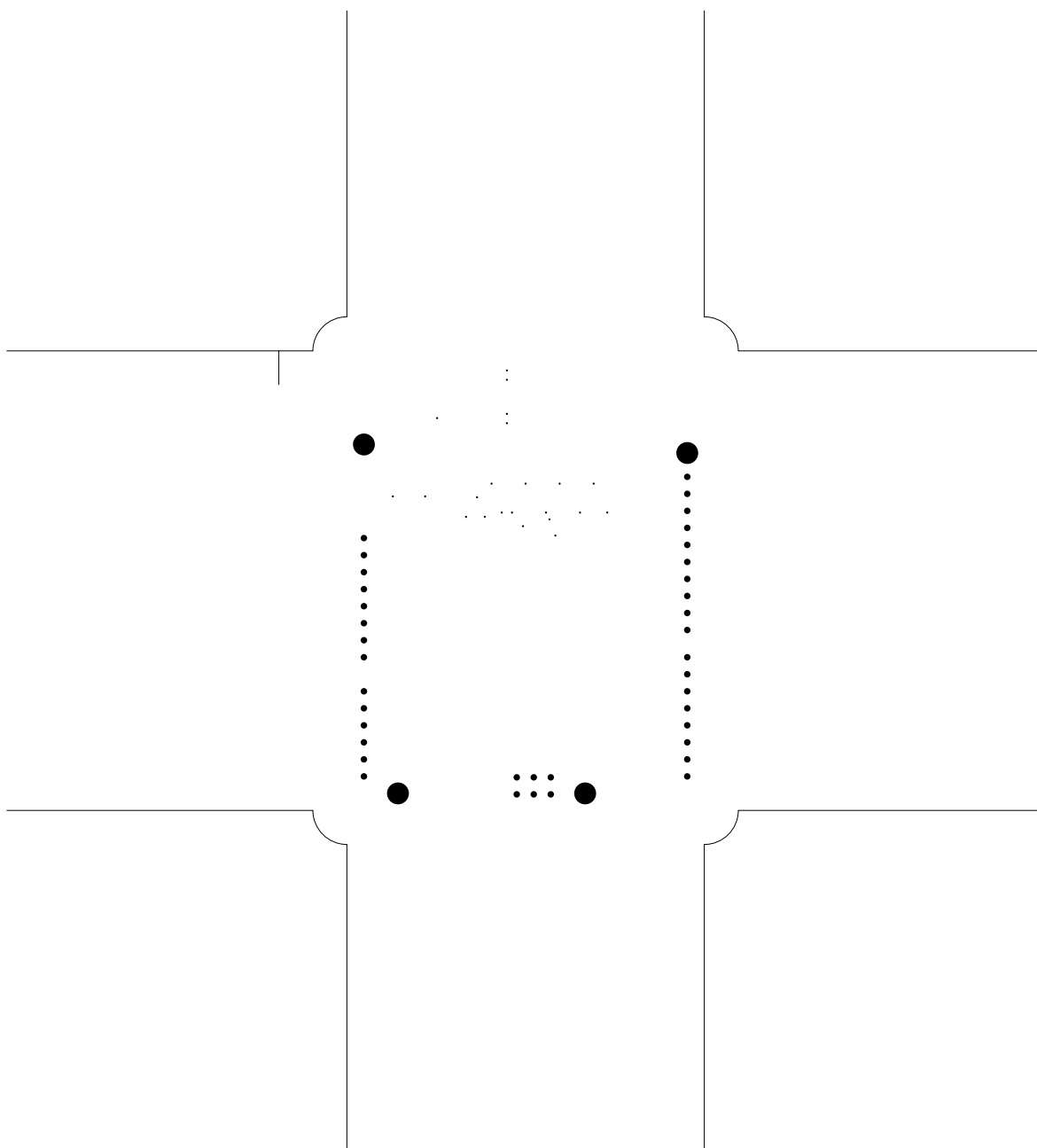
1. Table of Contents (This Page)
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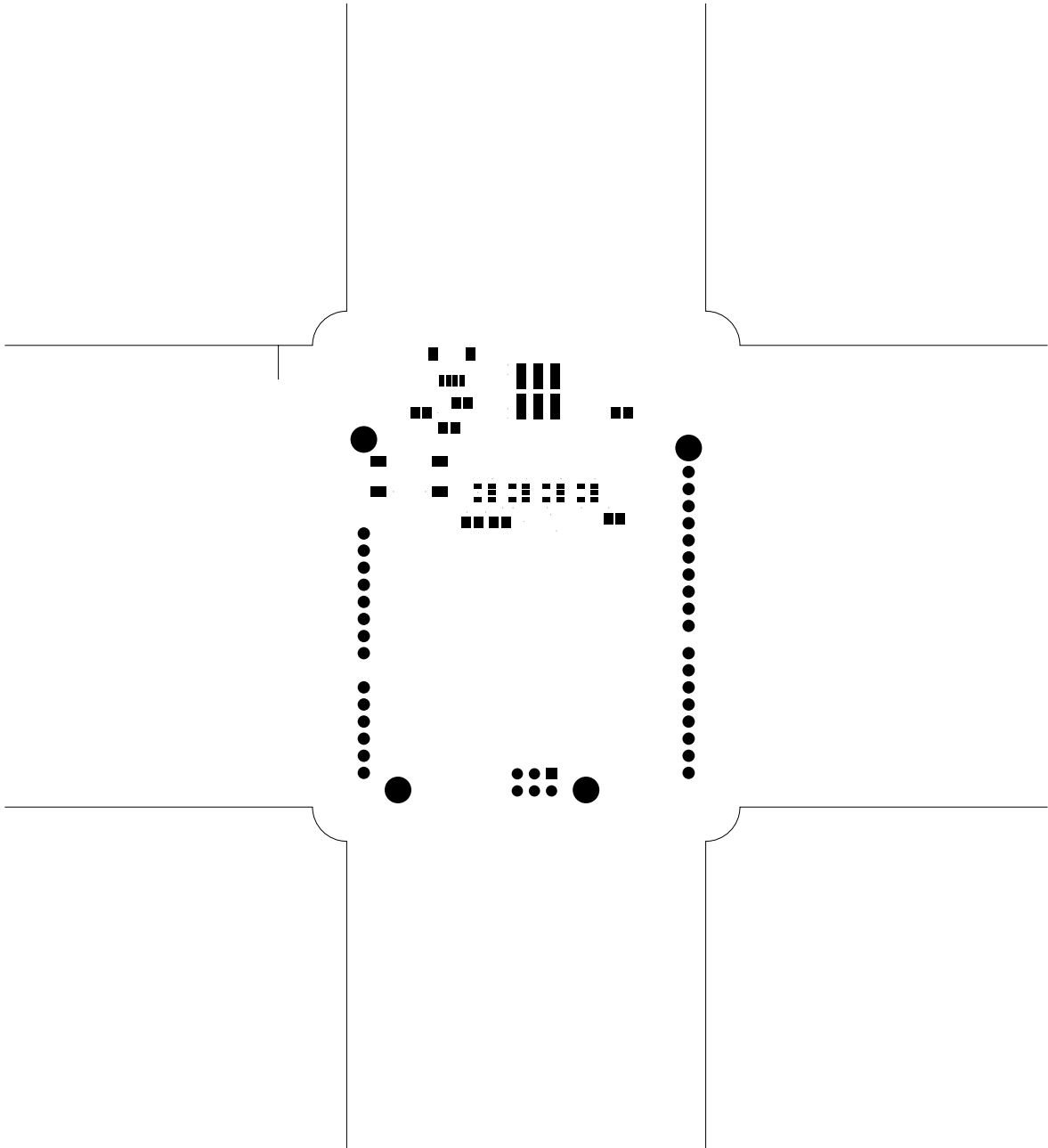
- 2. component
- 3. solder
- 4. outline
- 5. plated-drill
- 6. componentmask
- 7. soldermask
- 8. topsilk
- 9. bottomsilk
- 10. toppaste
- 11. topassembly
- 12. bottomassembly
- 13. fab

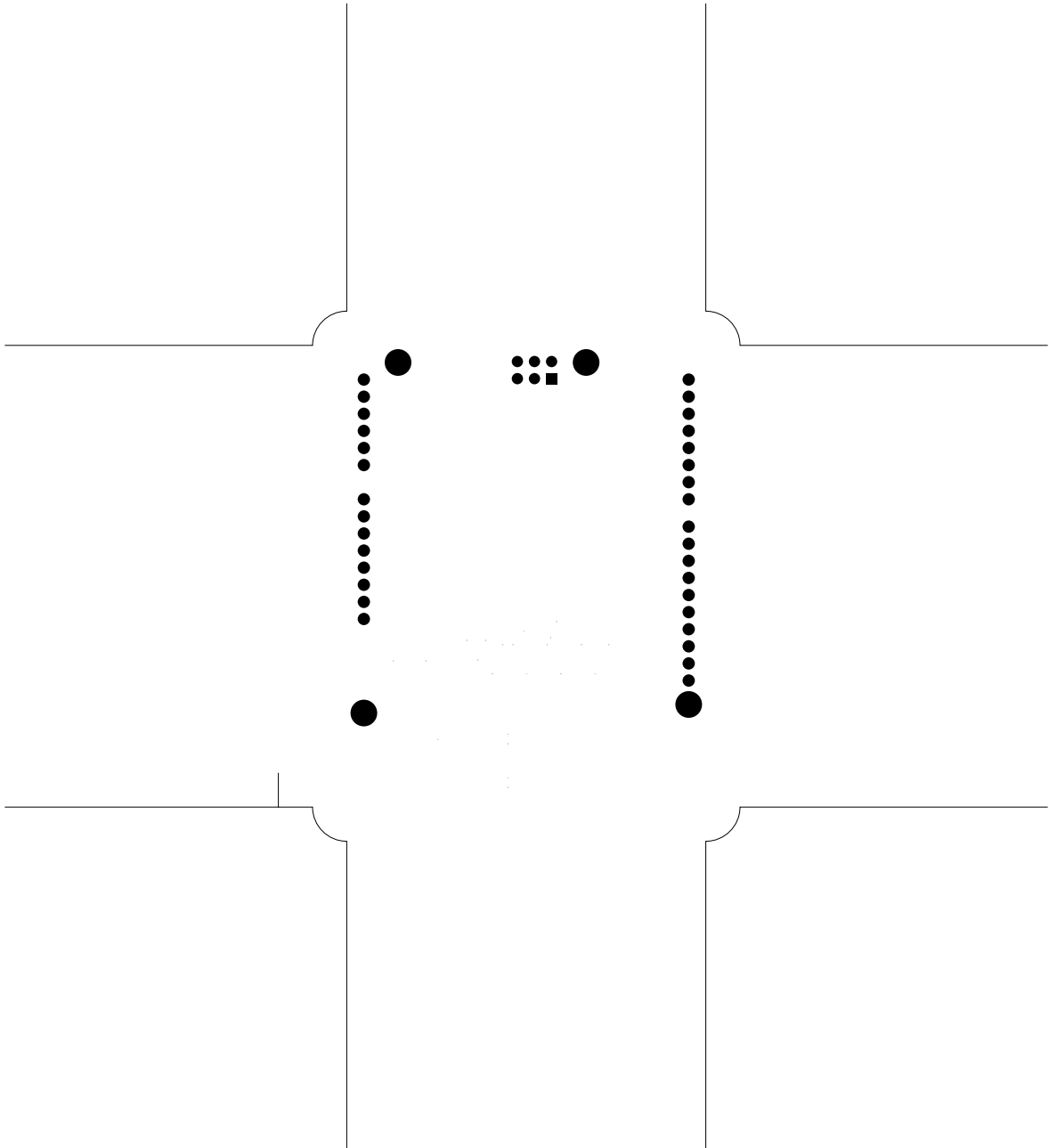


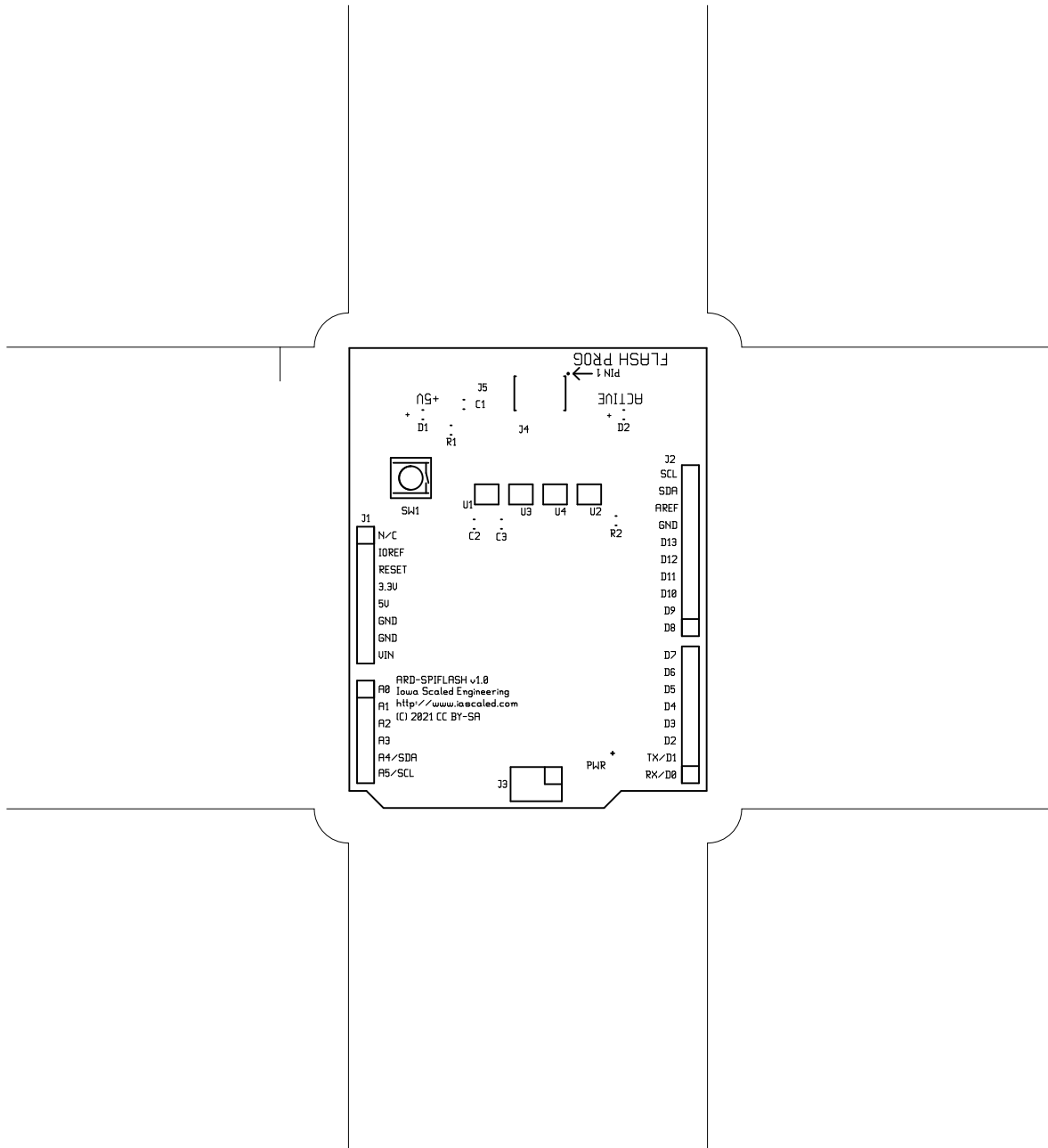


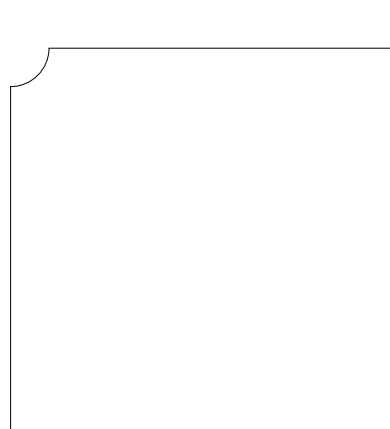
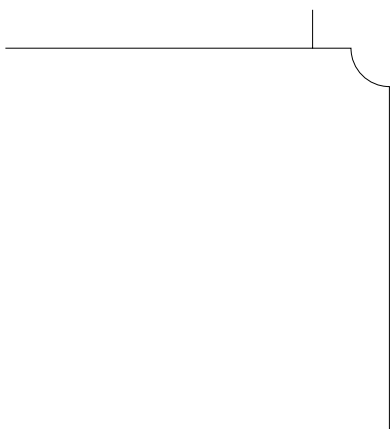
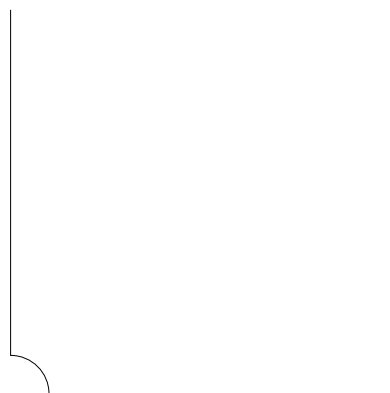
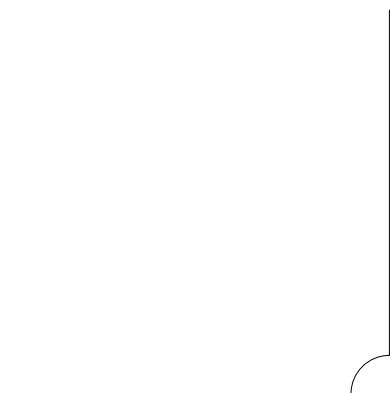


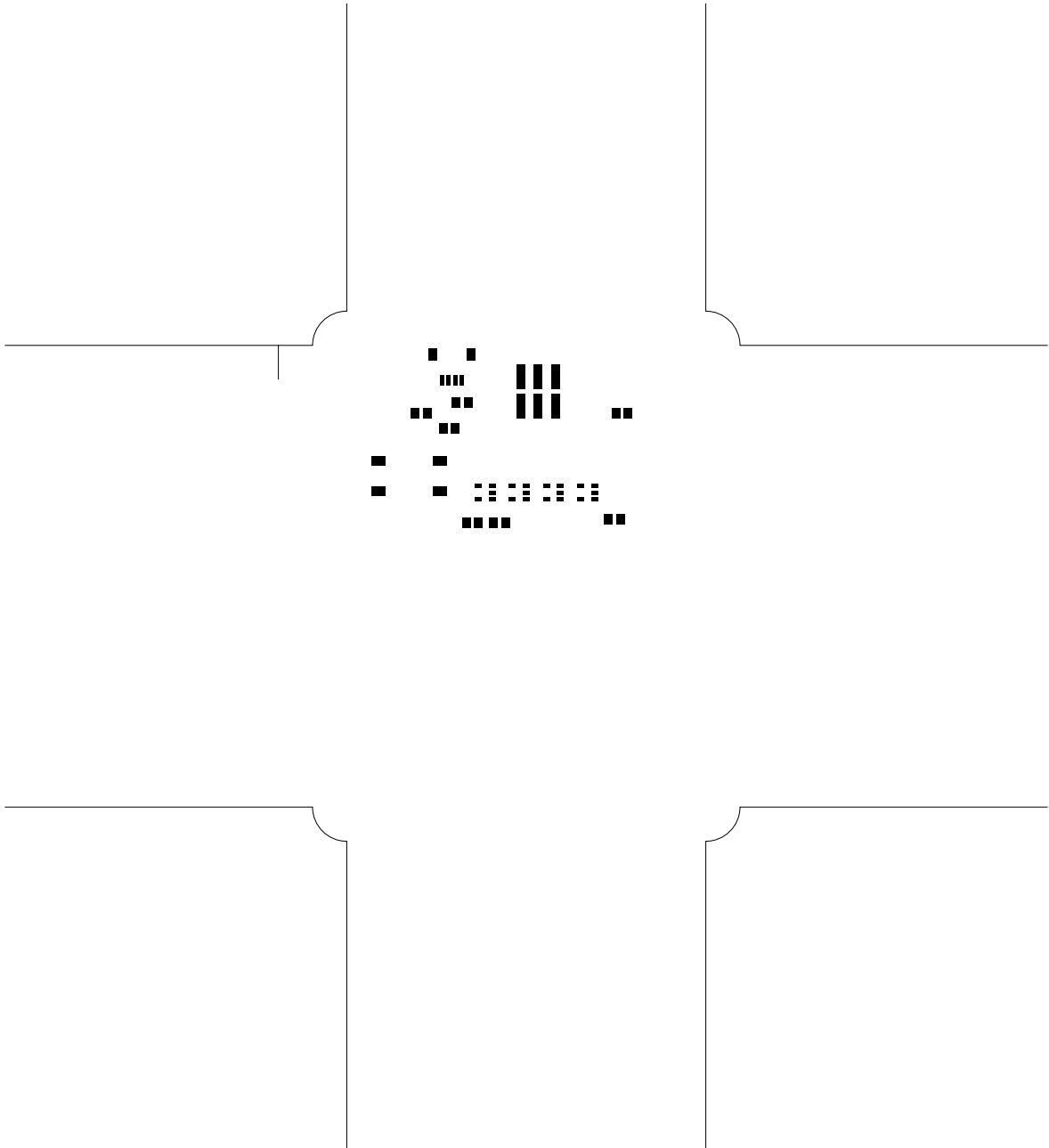


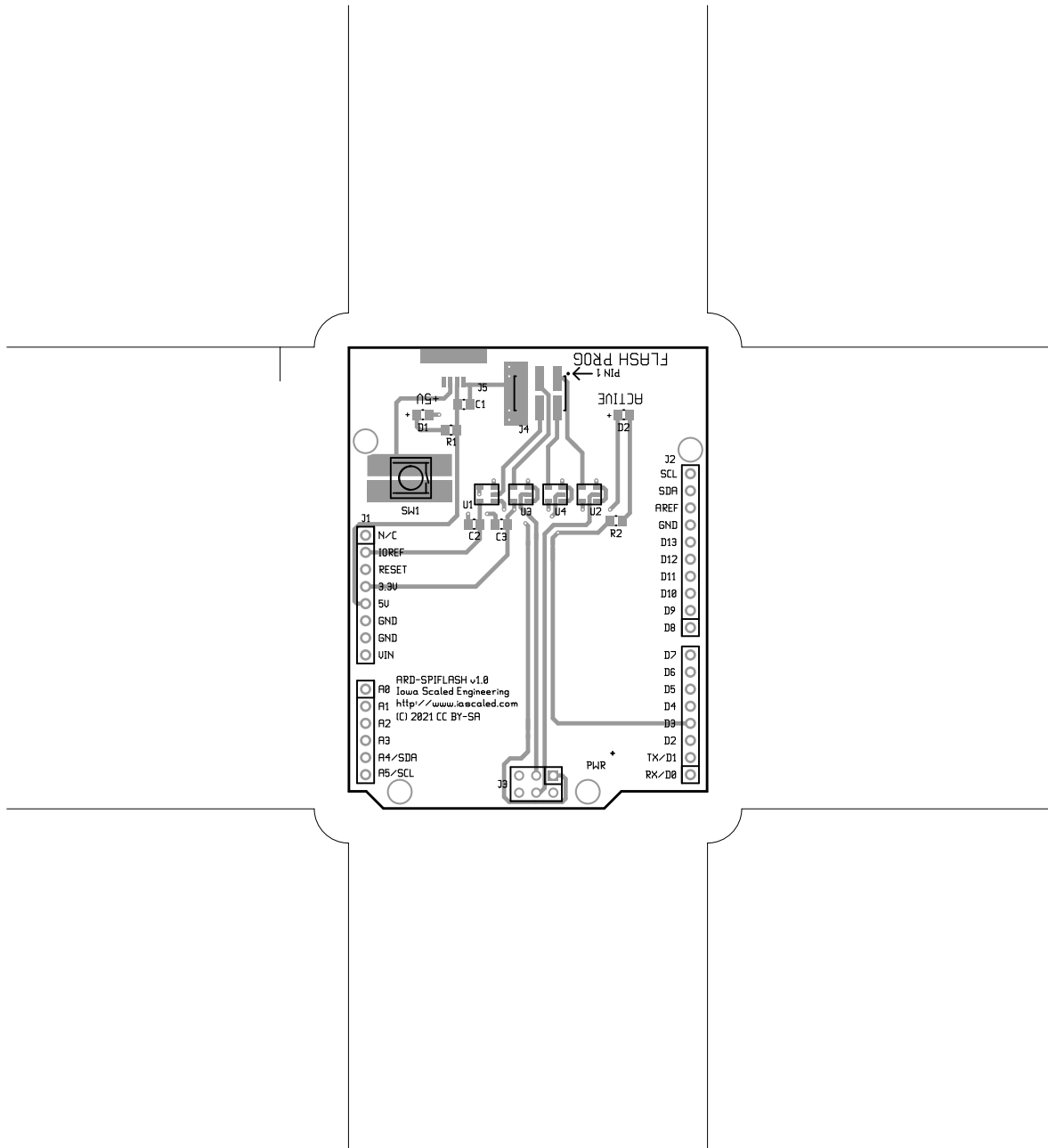


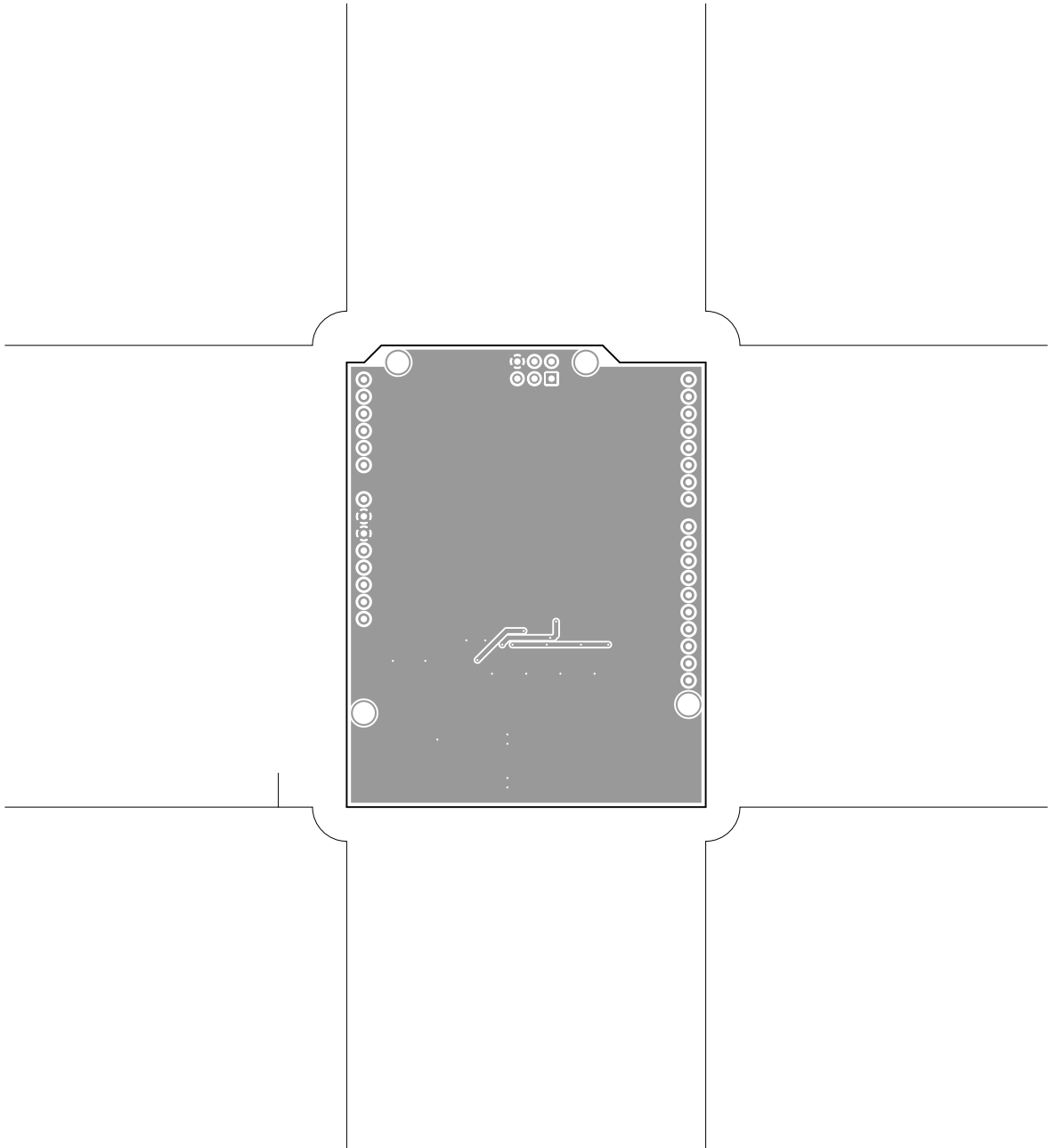












bottomassembly (mirrored), scale = 1:1.000
ard-spiflash.pcb

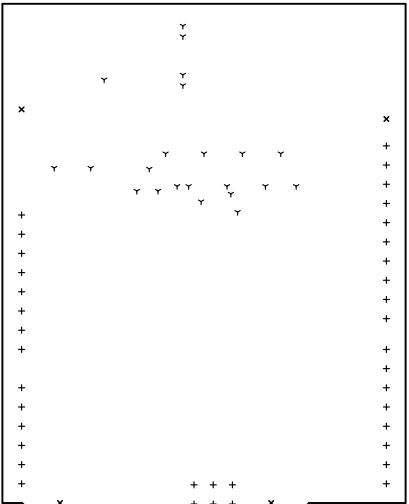
There are 3 different drill sizes used in this layout, 64 holes total

Symbol	Diam. (Inch)	Count	Plated?
Y	0.012	22	YES
+	0.038	38	YES
x	0.129	4	YES

Title: (unknown) - Fabrication Drawing

Author: Nathan D. Holmes

Date: Thu Oct 28 20:11:09 2021 UTC



Board outline is the centerline of this path