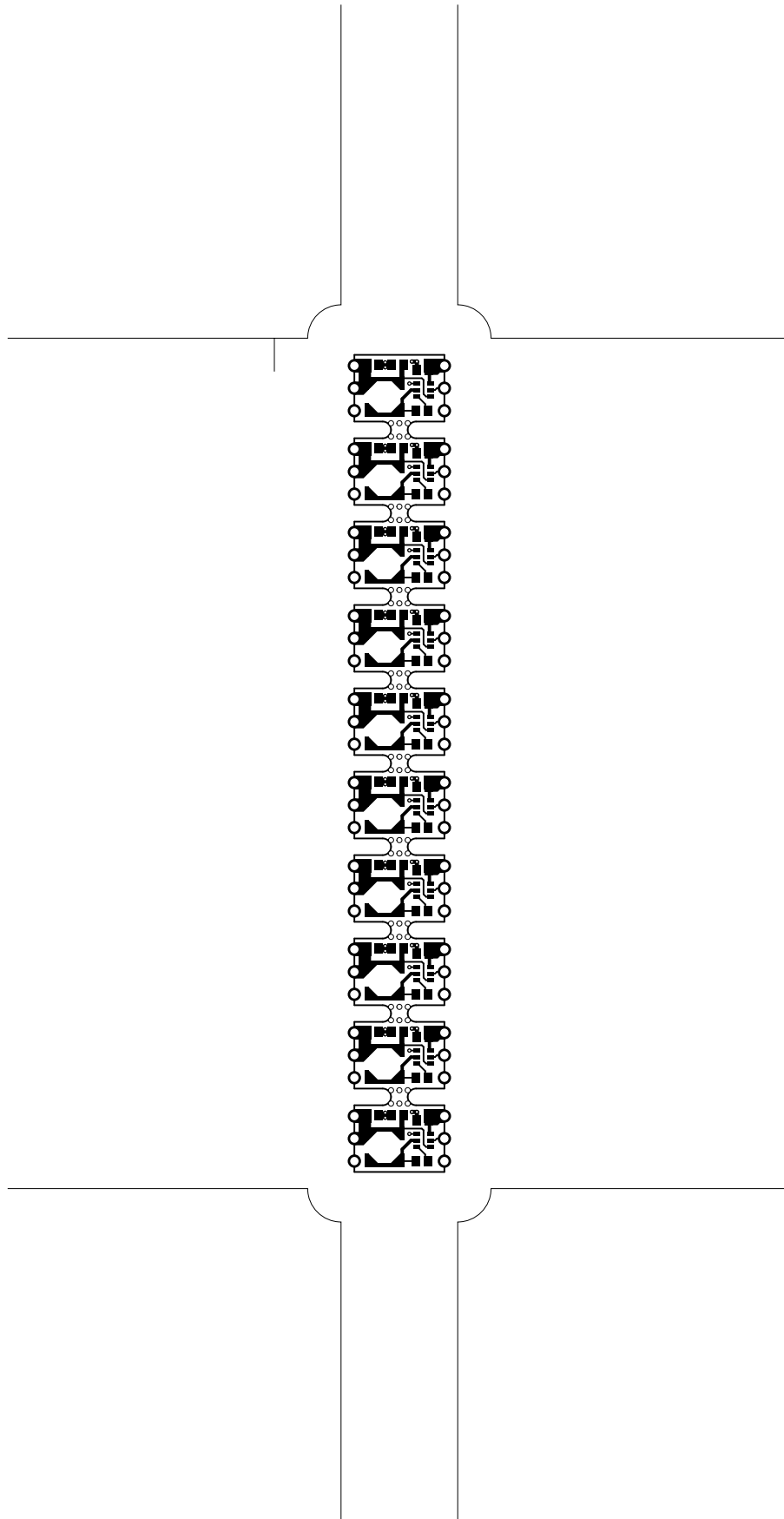
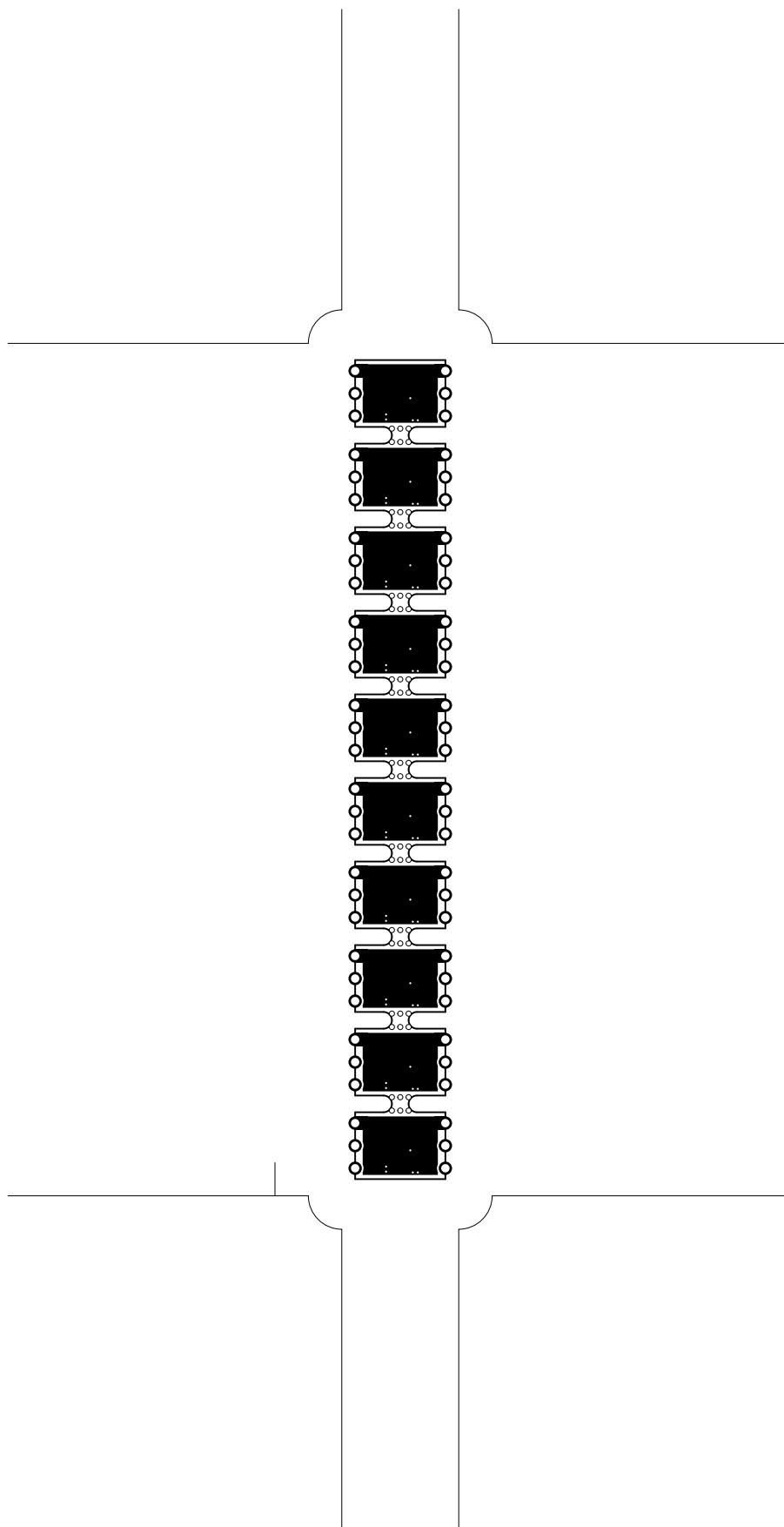


1. Table of Contents (This Page)

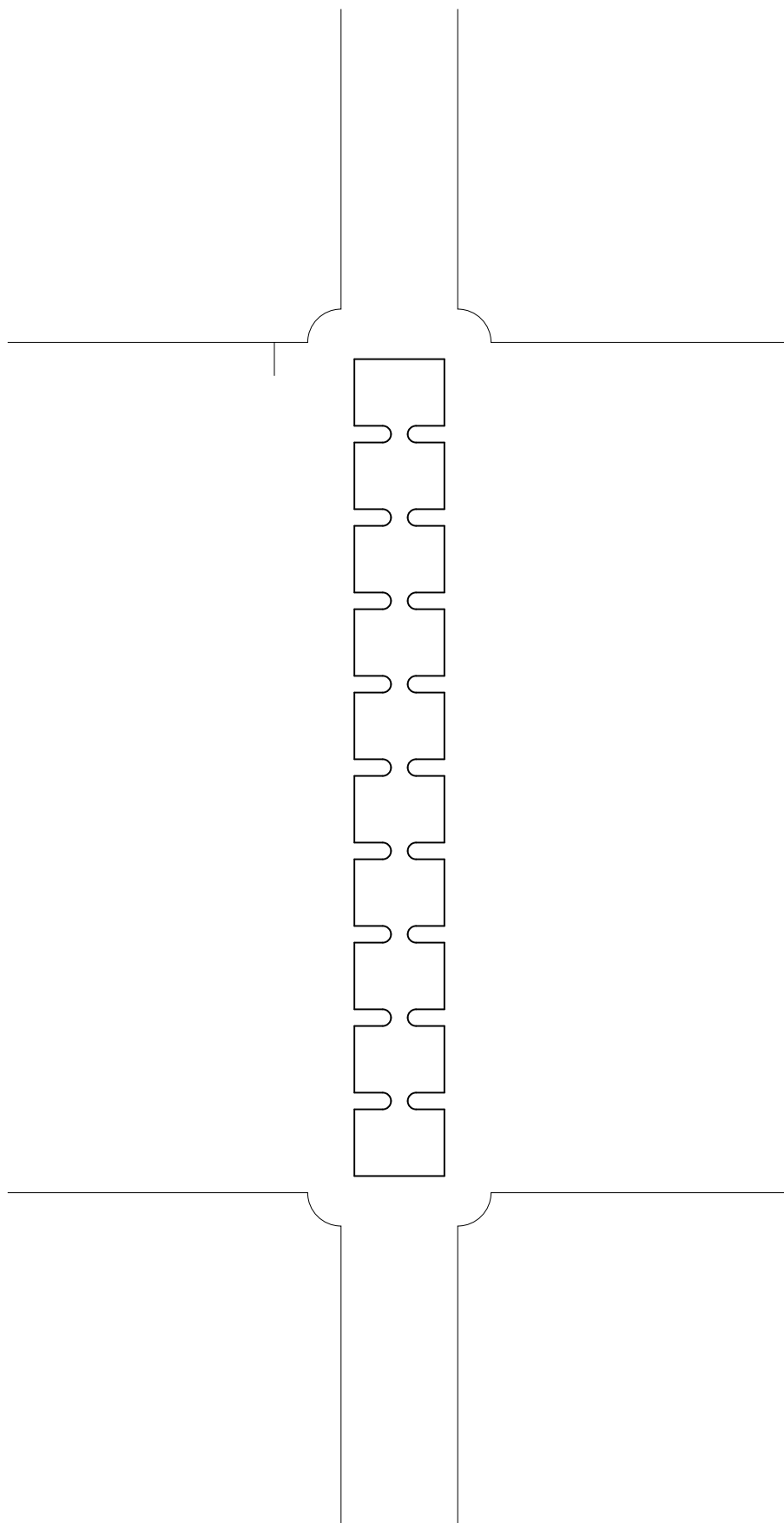
Created on Sun Aug 15 19:32:10 2021

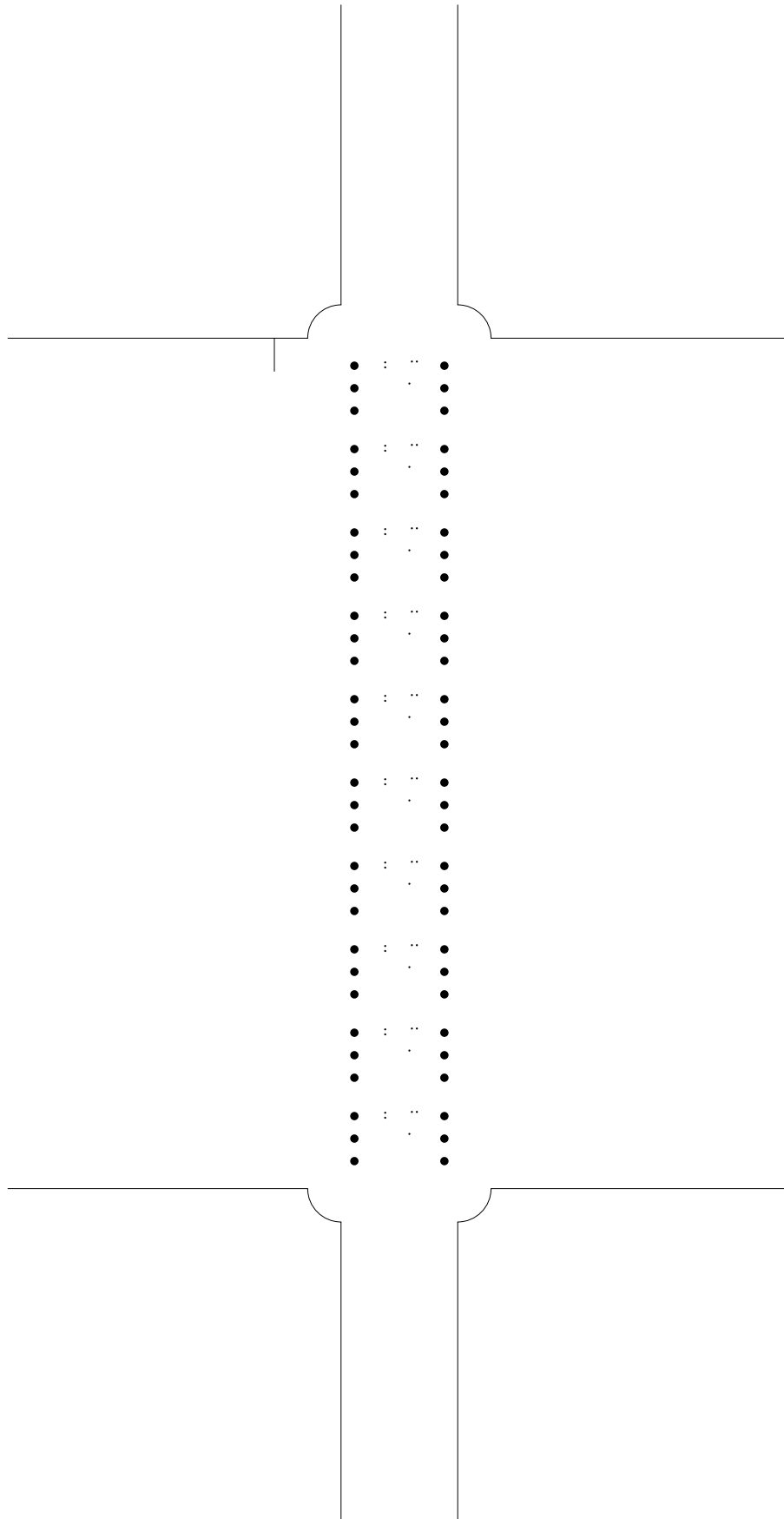
- 2. component
- 3. solder
- 4. outline
- 5. plated-drill
- 6. unplated-drill
- 7. componentmask
- 8. soldermask
- 9. topsilk
- 10. bottomsilk
- 11. toppaste
- 12. topassembly
- 13. bottomassembly
- 14. fab

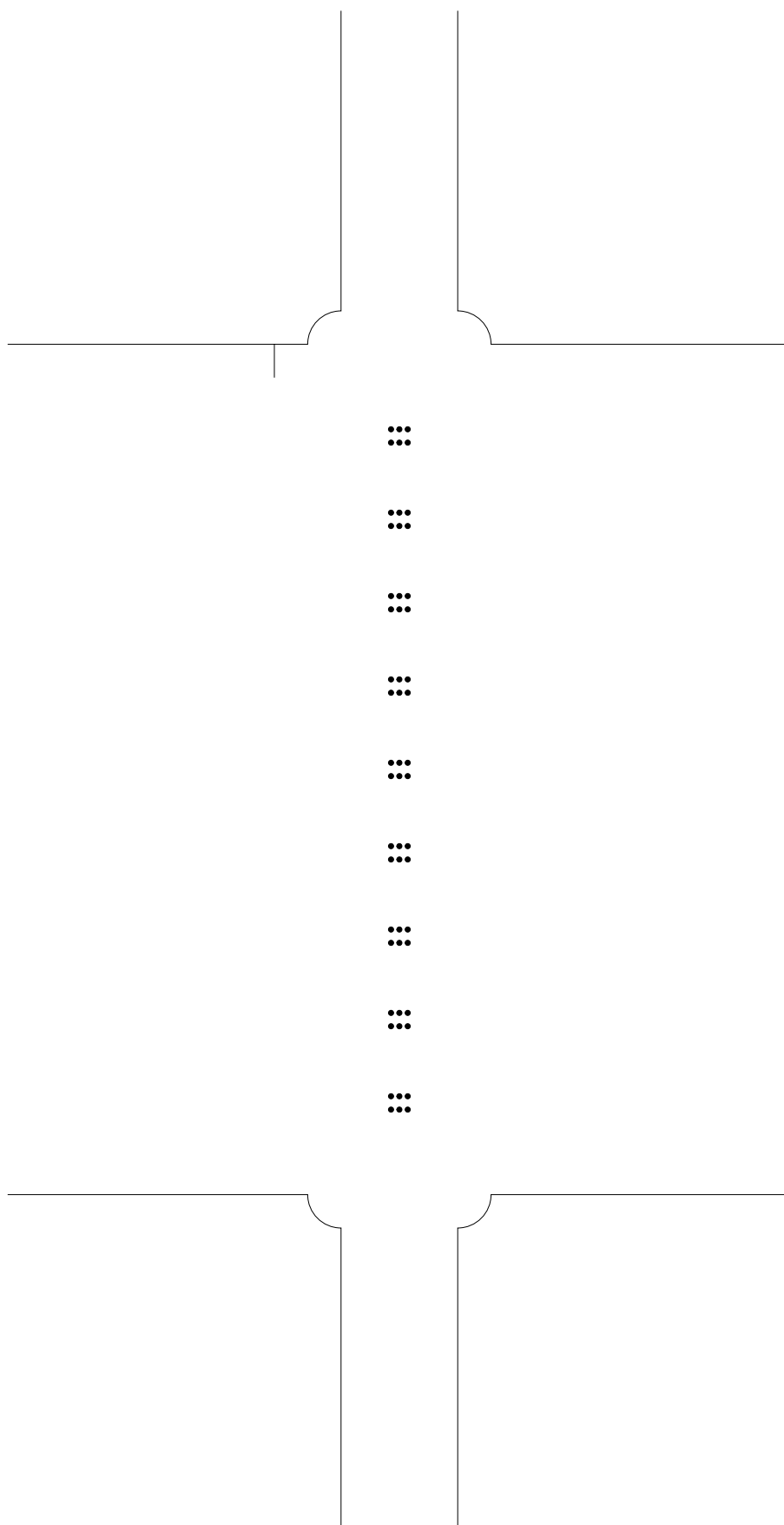


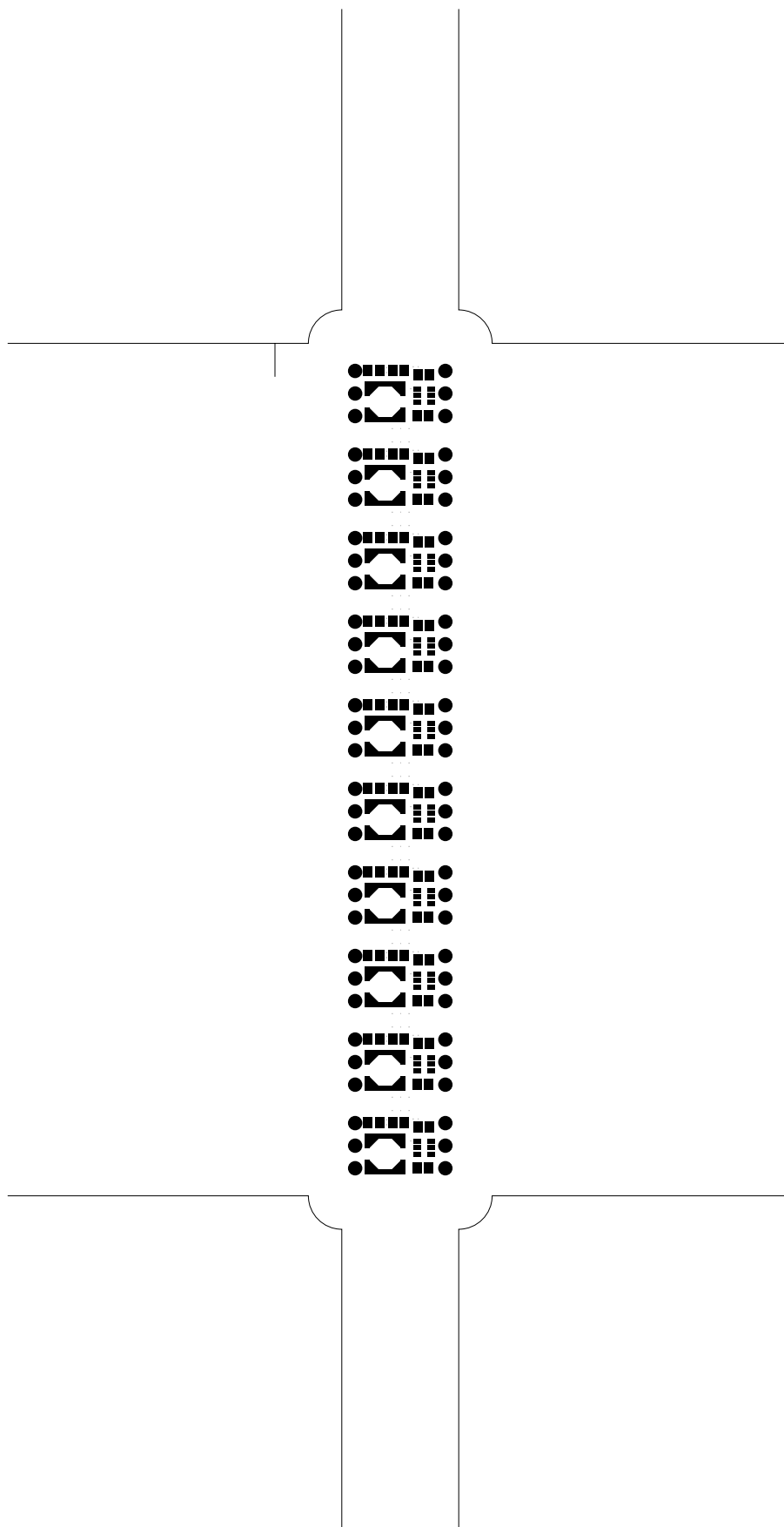


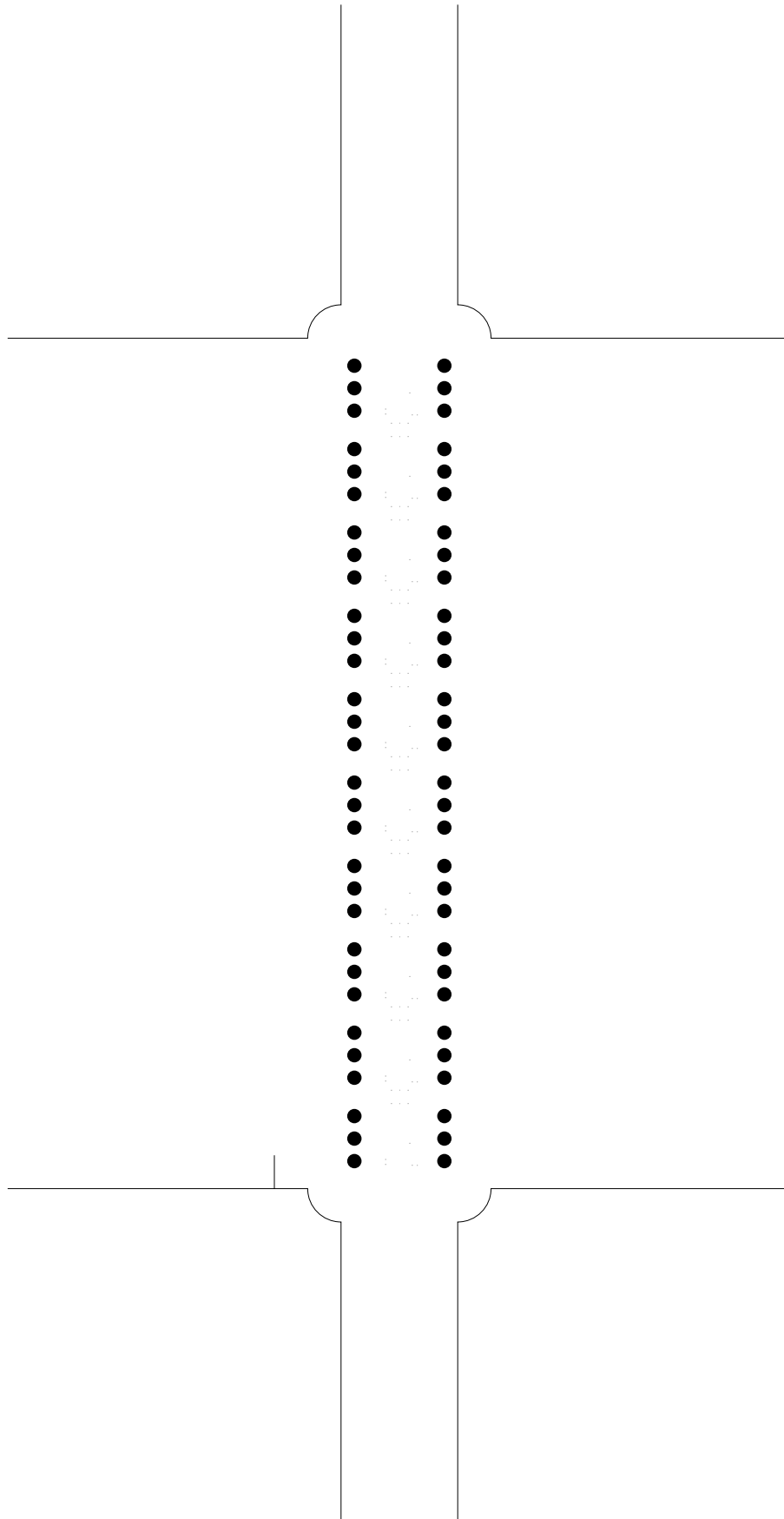
bottom (mirrored), scale = 1:1.000
ckt-ap6320x-panel.pcb



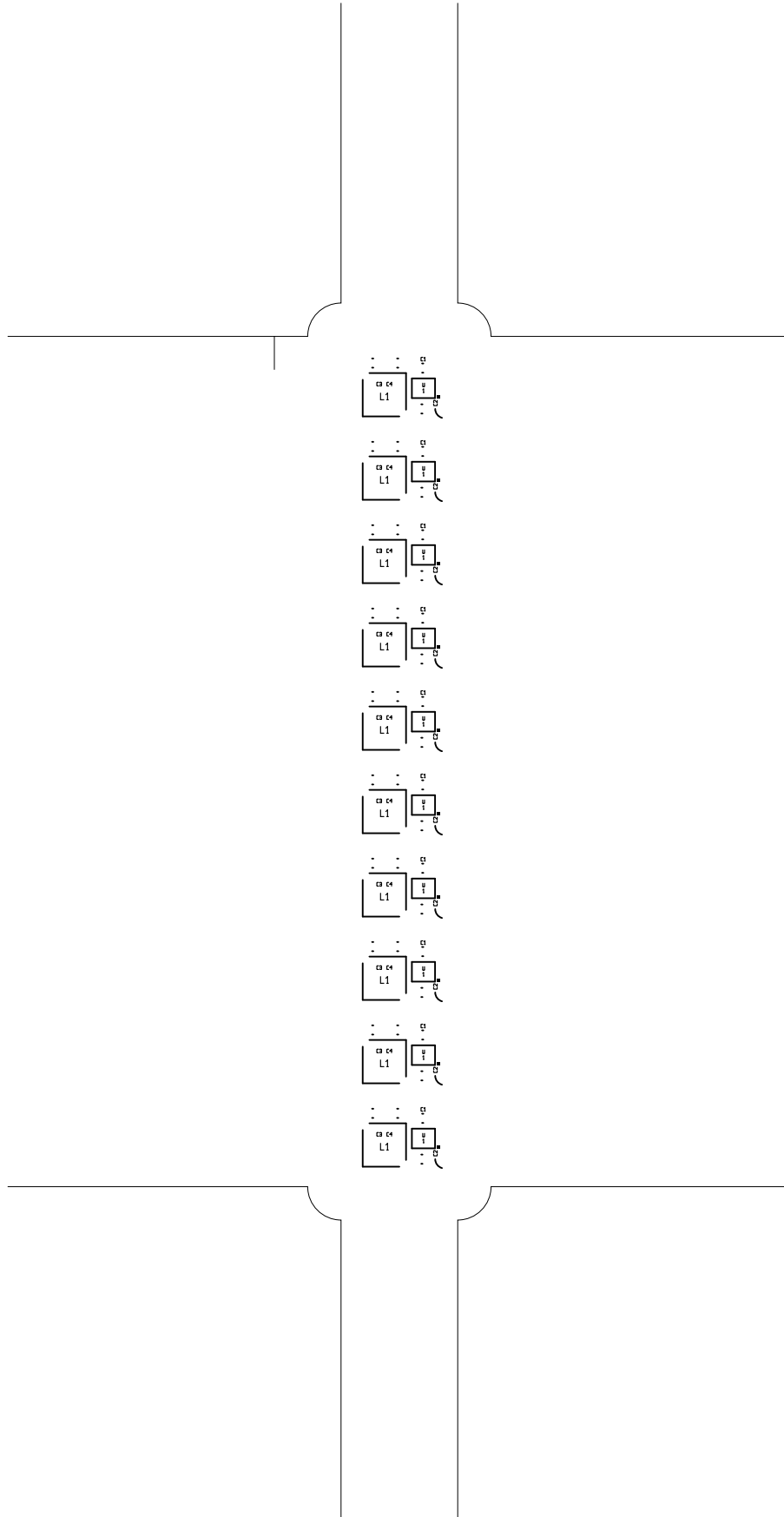


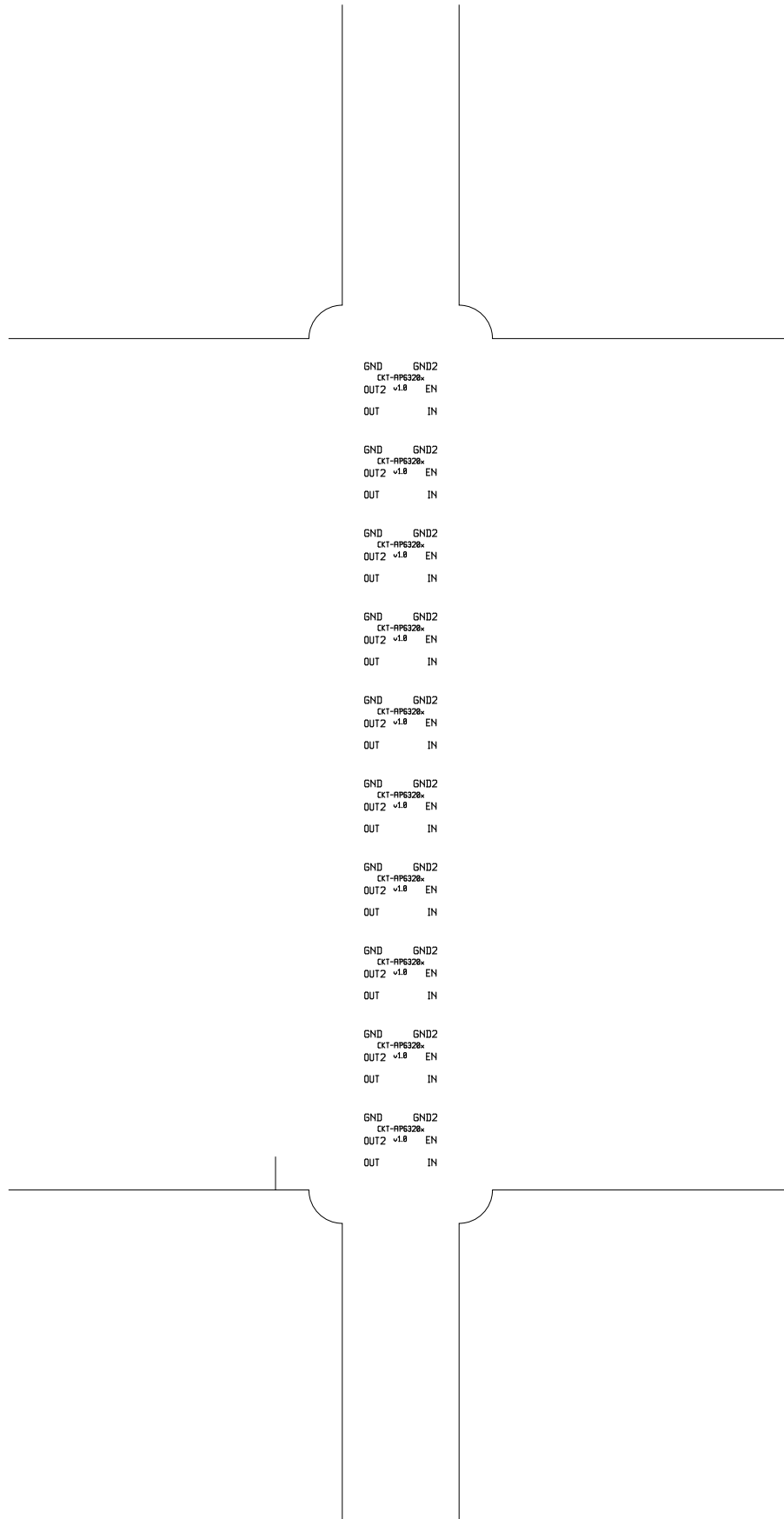


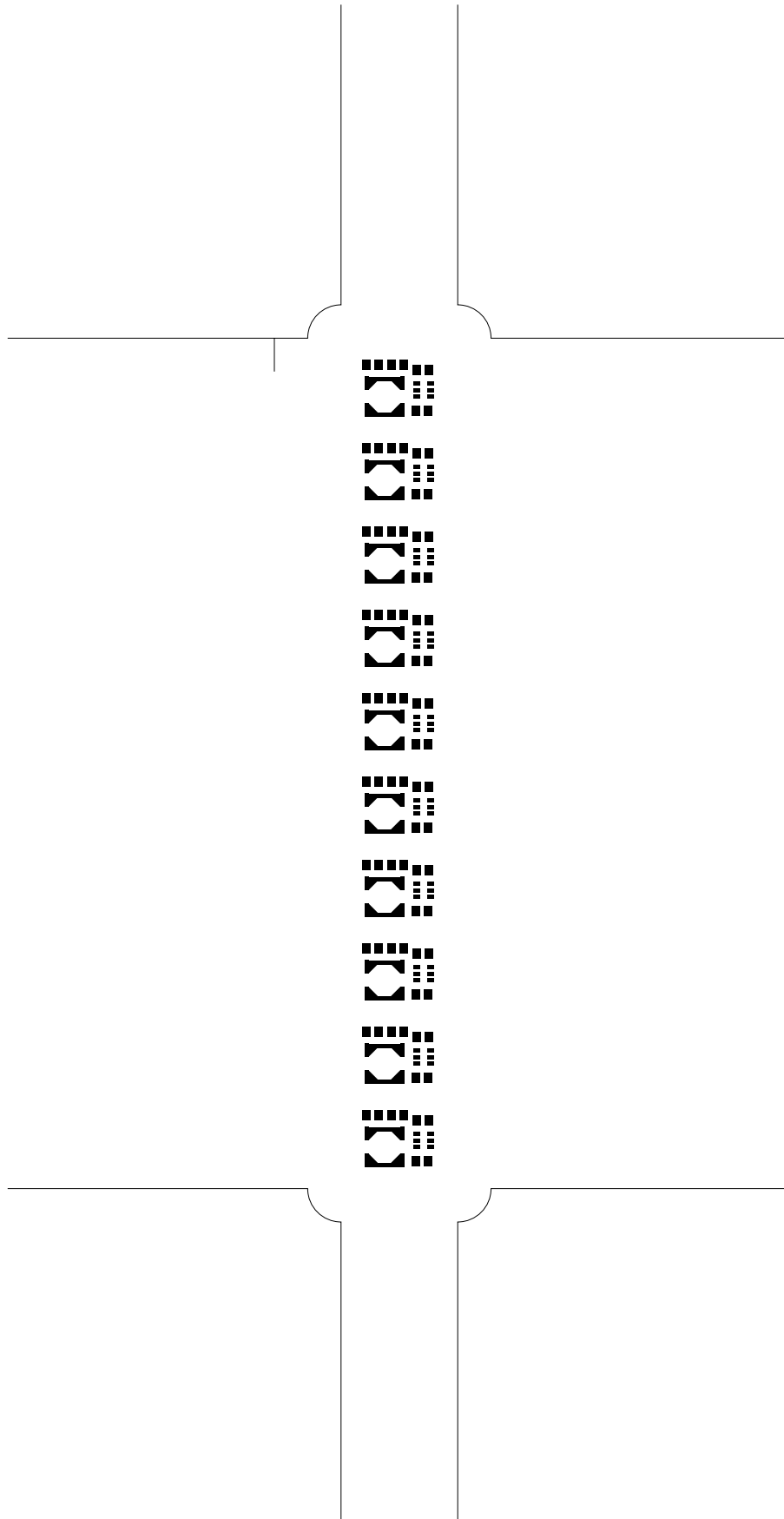


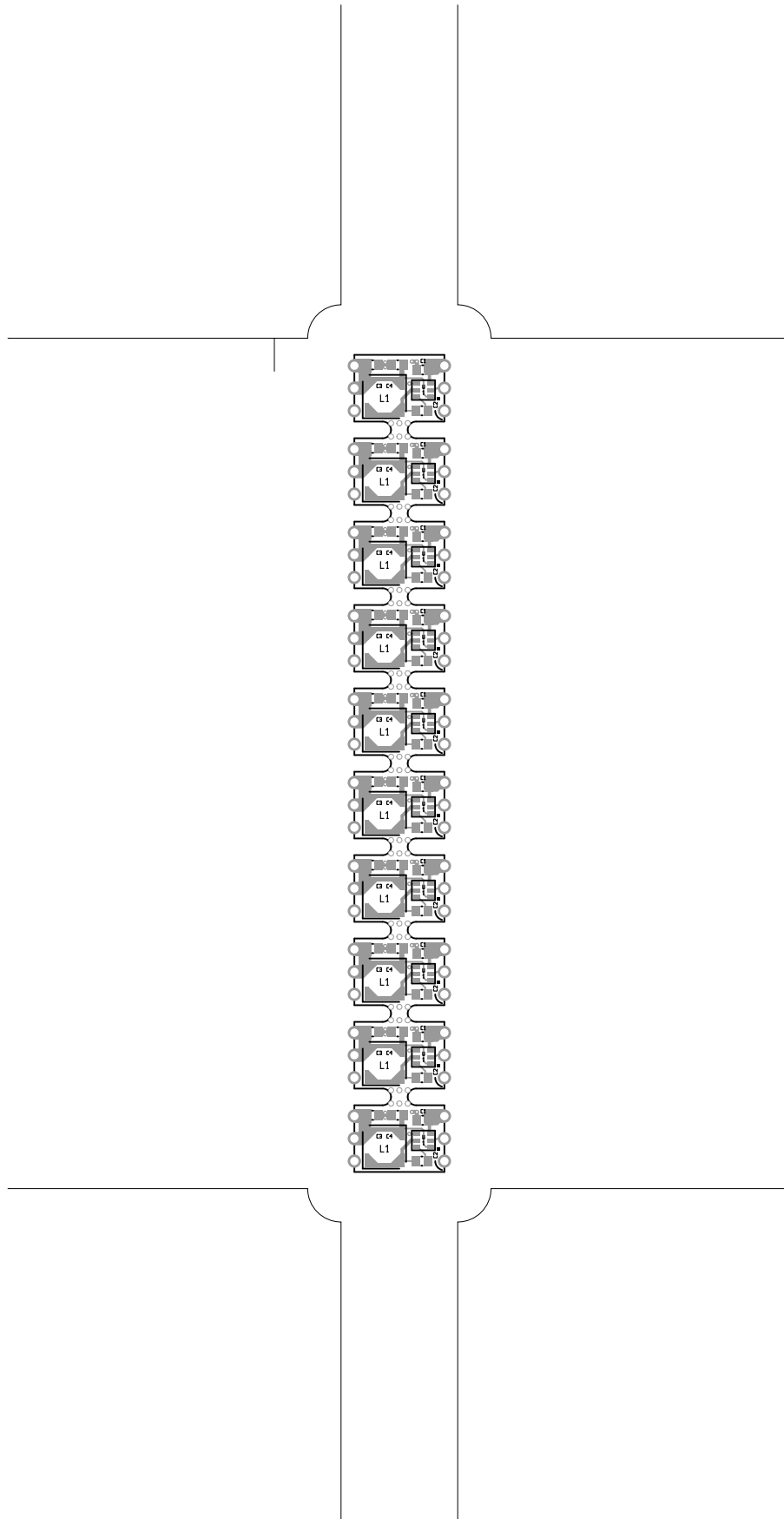


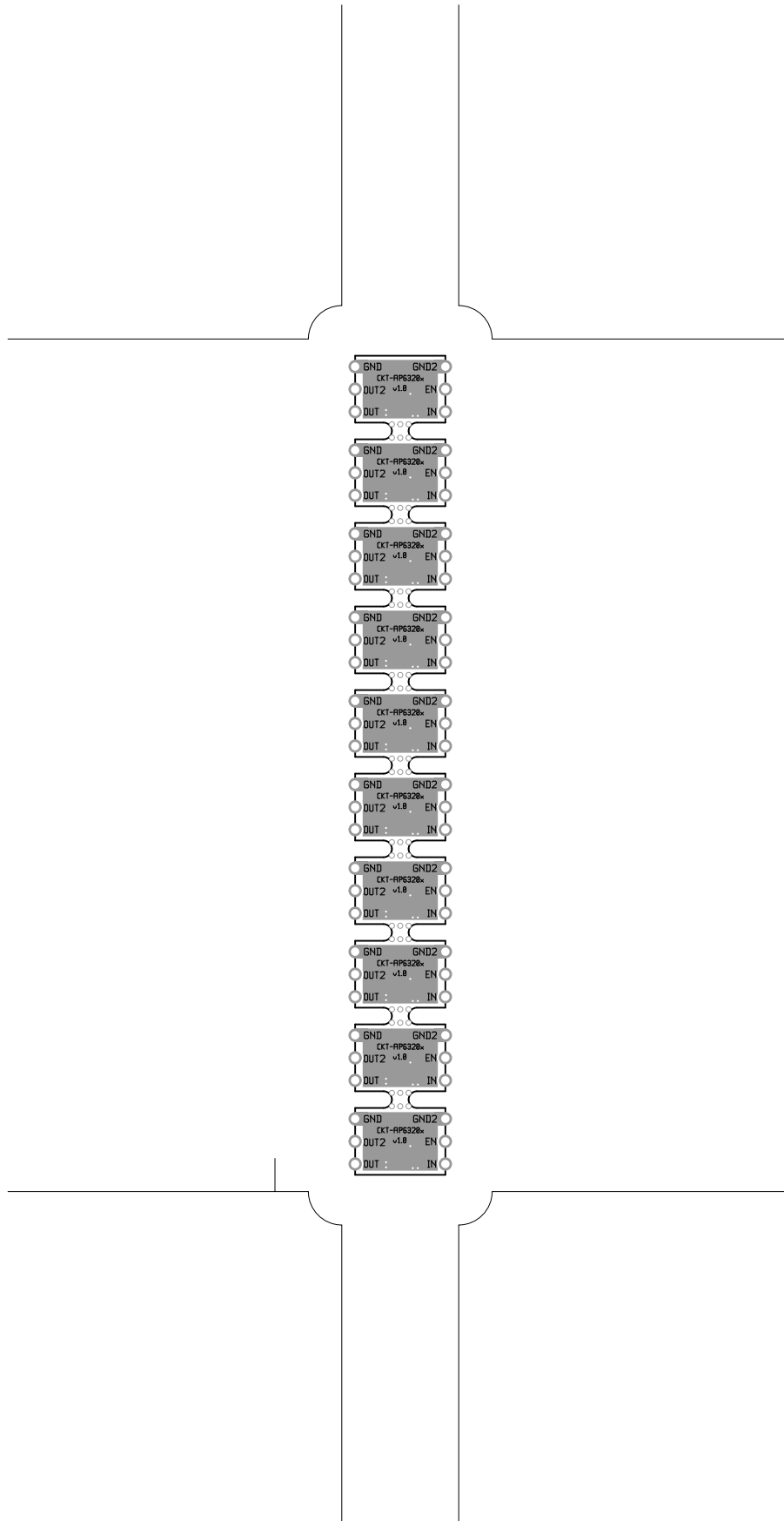
bottommask (mirrored), scale = 1:1.000
ckt-ap6320x-panel.pcb







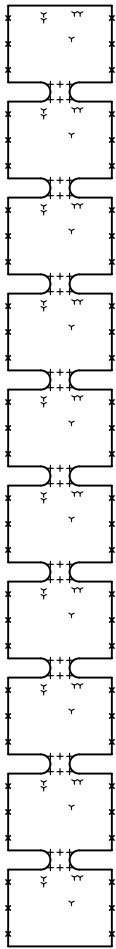




bottomassembly (mirrored), scale = 1:1.000
ckt-ap6320x-panel.pcb

There are 3 different drill sizes used in this layout, 164 holes total

Symbol	Diam. (Inch)	Count	Plated?	
Y	0.012	50	YES	Title: (unknown) - Fabrication Drawing
+	0.031	54	NO	Author: Nathan D. Holmes
x	0.050	60	YES	Date: Mon Aug 16 01:32:10 2021 UTC



Board outline is the centerline of this path