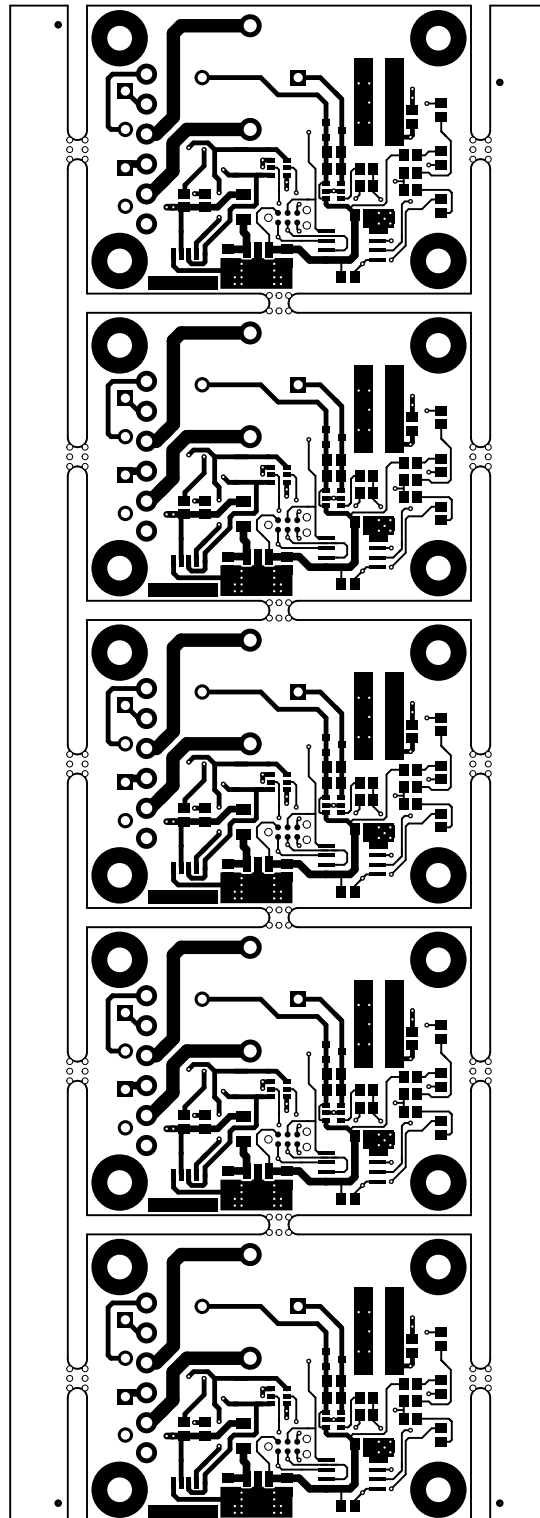
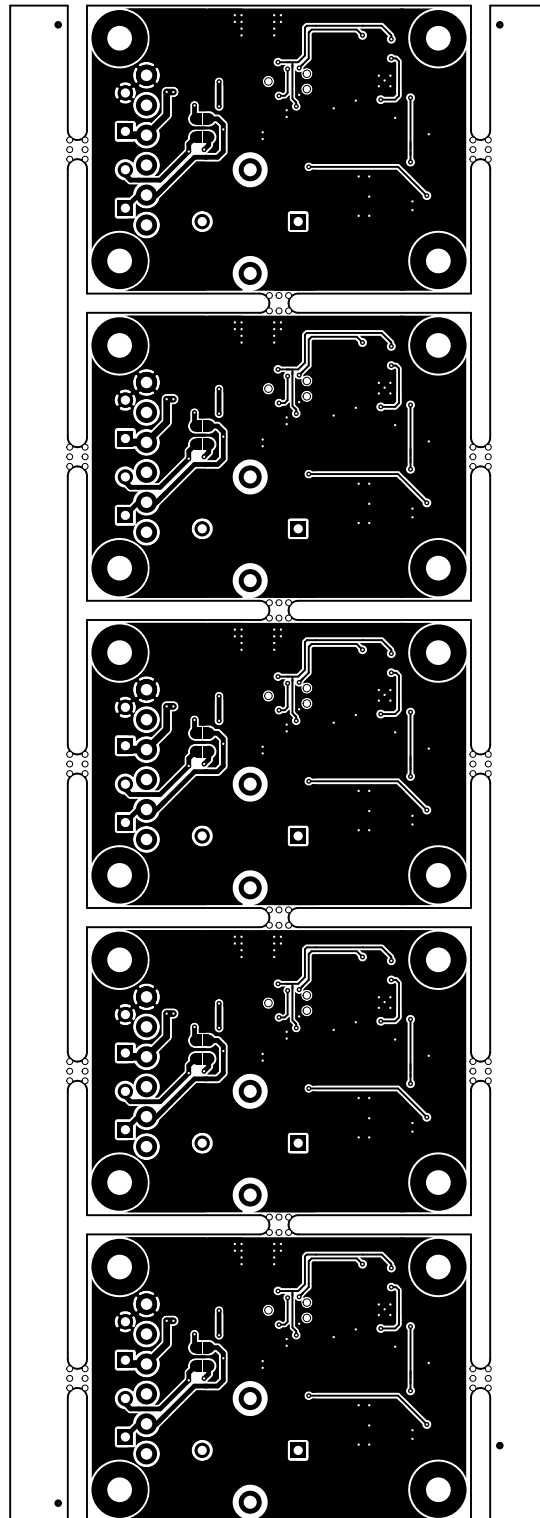


1. Table of Contents (This Page)

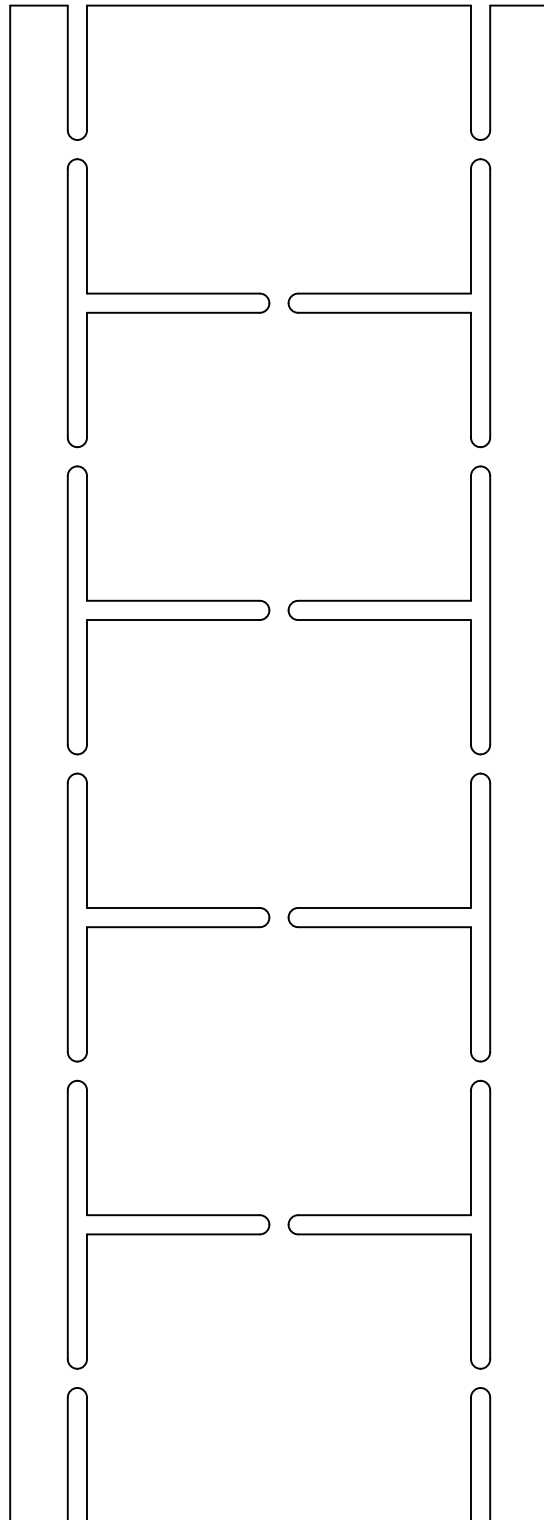
Created on Sat Oct 15 21:19:16 2022

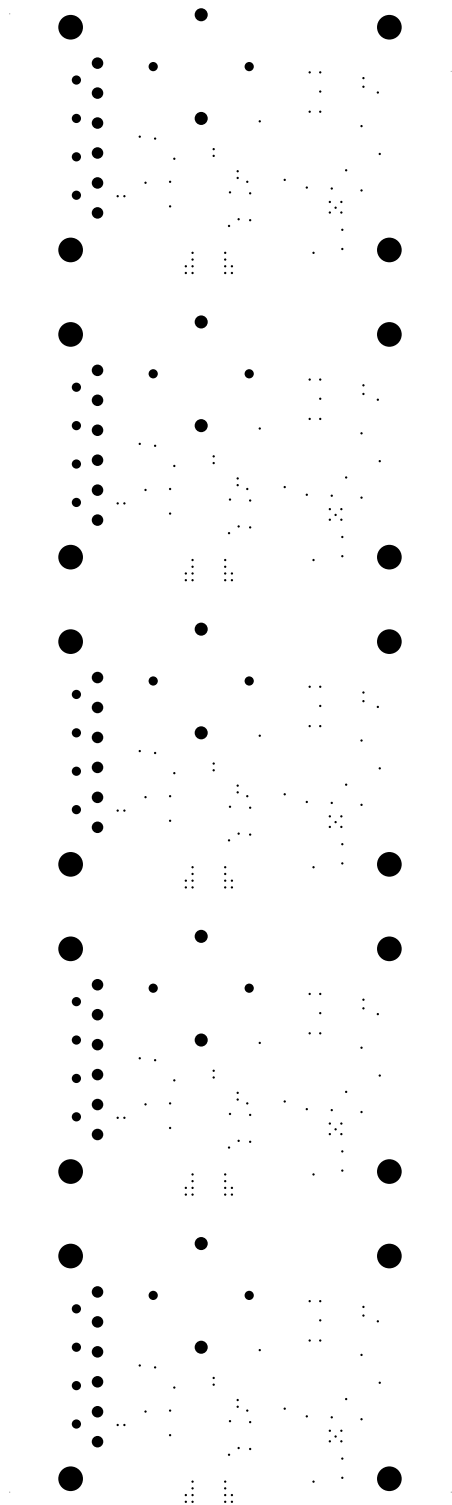
- 2. top
- 3. bottom
- 4. outline
- 5. plated-drill
- 6. unplated-drill
- 7. componentmask
- 8. soldermask
- 9. topsilk
- 10. bottomsilk
- 11. toppaste
- 12. bottompaste
- 13. topassembly
- 14. bottomassembly
- 15. fab

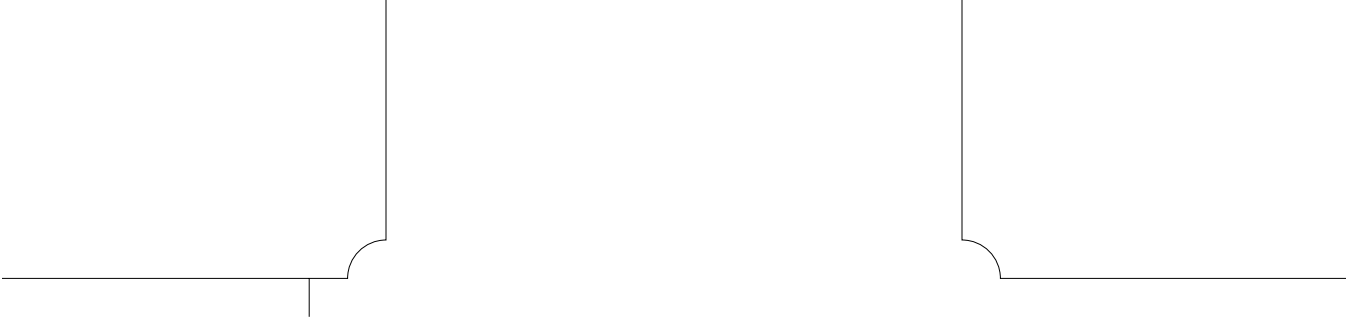


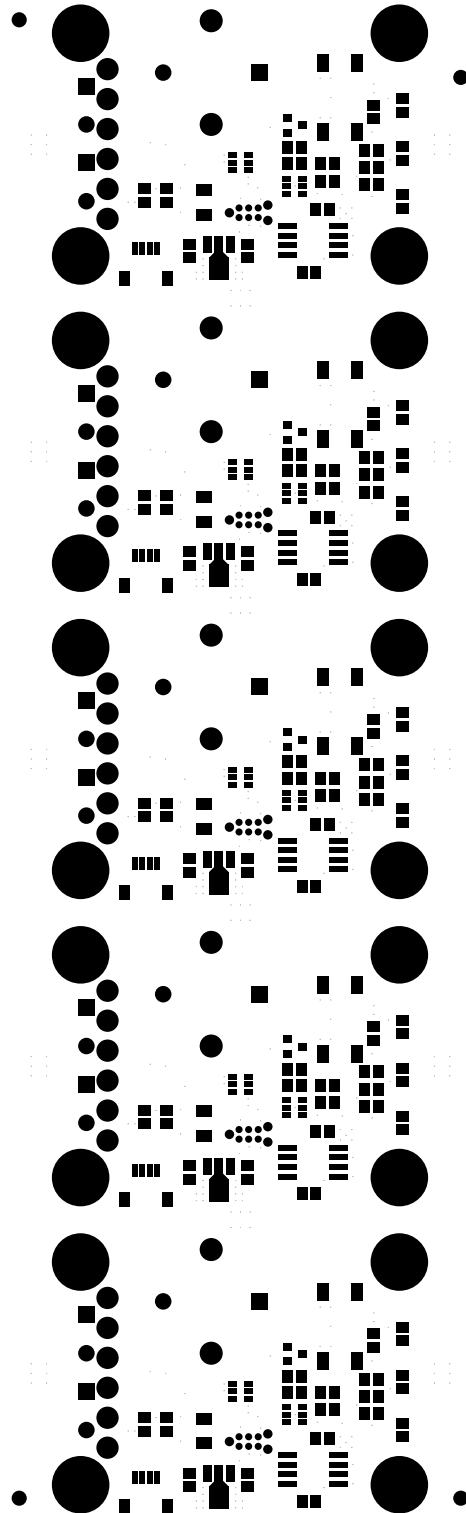


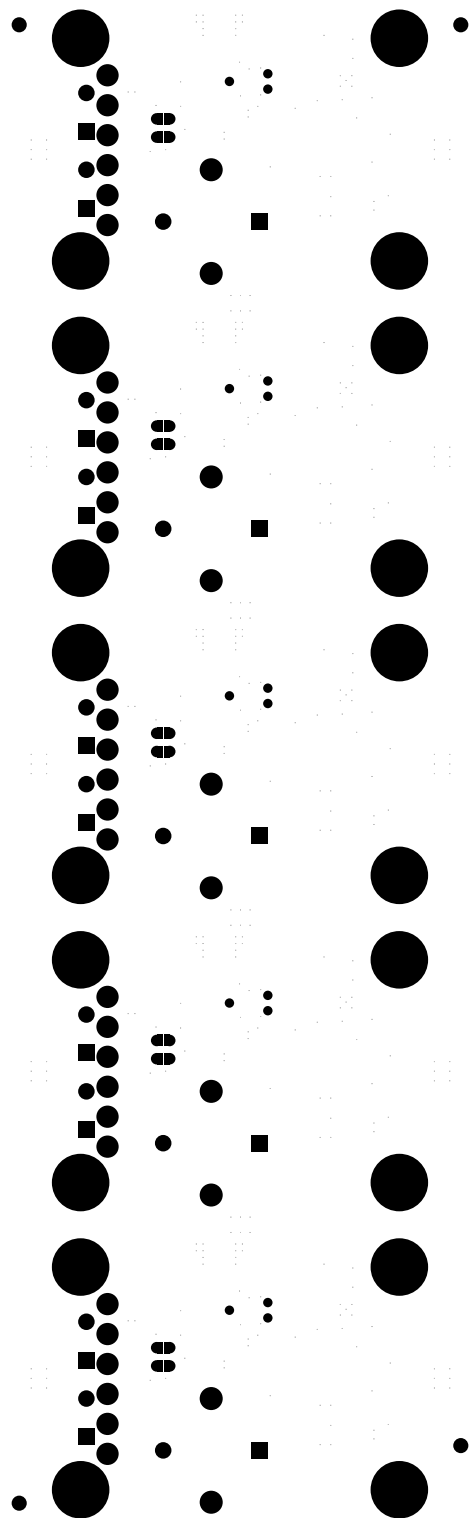
bottom (mirrored), scale = 1:1.000
ckt-bd1-array.pcb

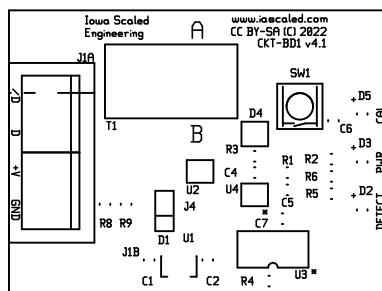
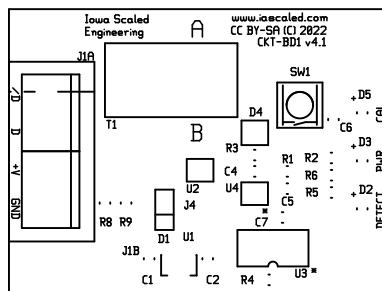
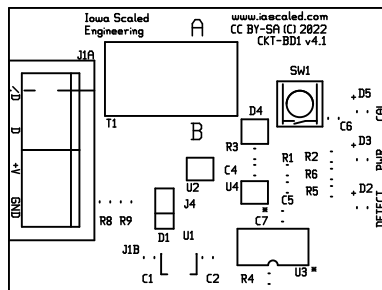
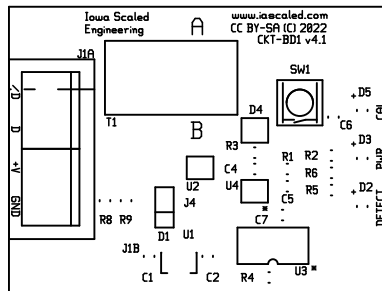
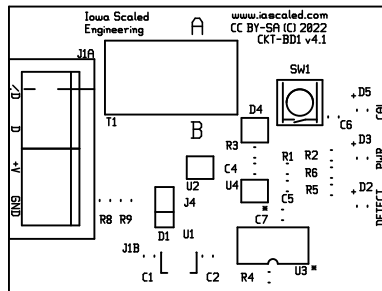












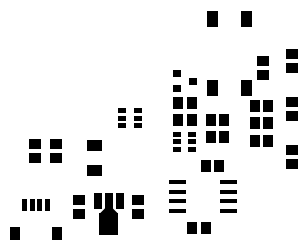
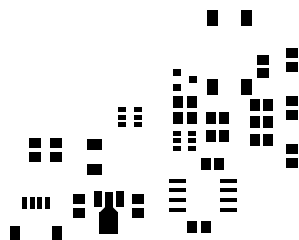
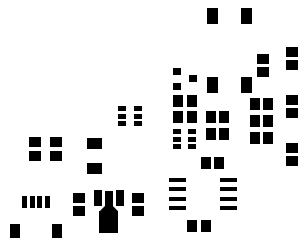
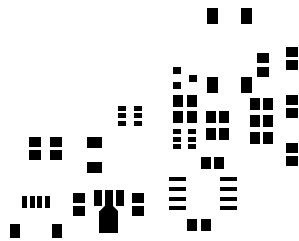
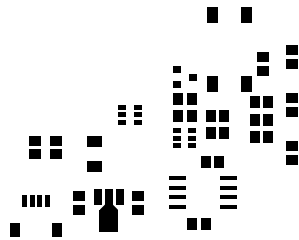
JP2 10k PULL-UP FOR /D
JP1 10k PULL-UP FOR D

JP2 10k PULL-UP FOR /D
JP1 10k PULL-UP FOR D

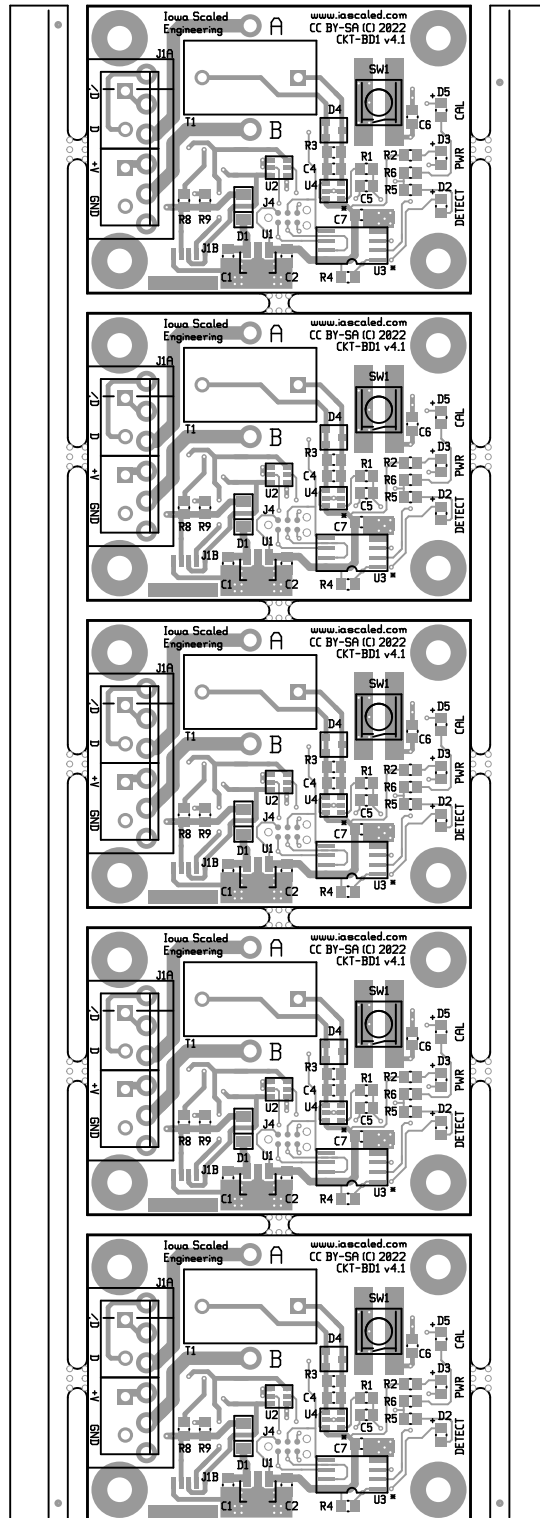
JP2 10k PULL-UP FOR /D
JP1 10k PULL-UP FOR D

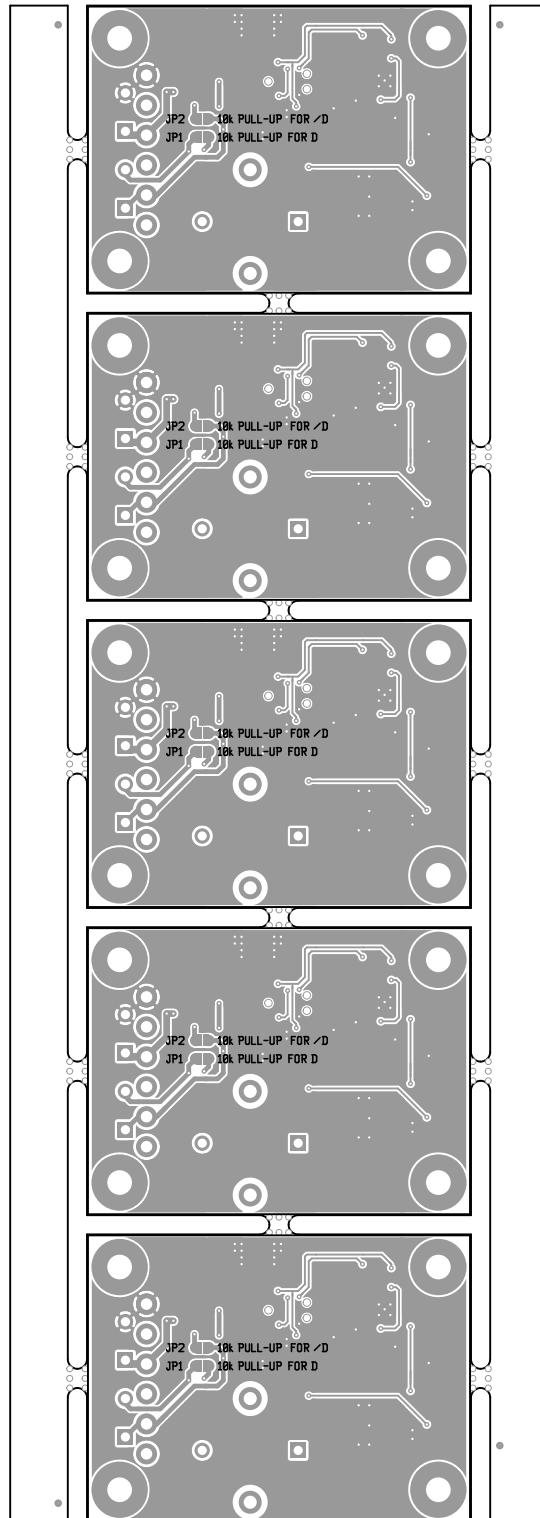
JP2 10k PULL-UP FOR /D
JP1 10k PULL-UP FOR D

JP2 10k PULL-UP FOR /D
JP1 10k PULL-UP FOR D





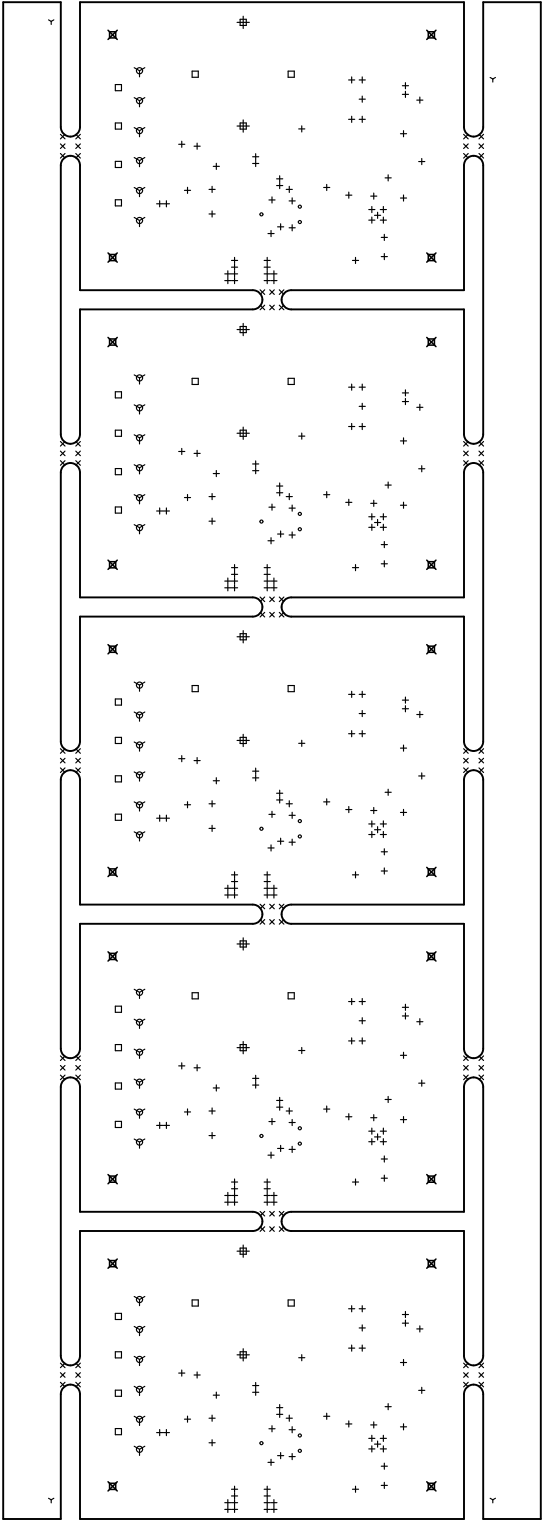




There are 8 different drill sizes used in this layout, 463 holes total

Symbol	Diam. (Inch)	Count	Plated?
▽	0.000	4	YES
+	0.012	270	YES
×	0.031	84	NO
°	0.039	15	NO
□	0.048	30	YES
▽	0.060	30	YES
⊕	0.068	10	YES
⊗	0.129	20	YES

Title: (unknown) - Fabrication Drawing
Author: Nathan D. Holmes
Date: Sun Oct 16 03:19:16 2022 UTC



Board outline is the centerline of this path