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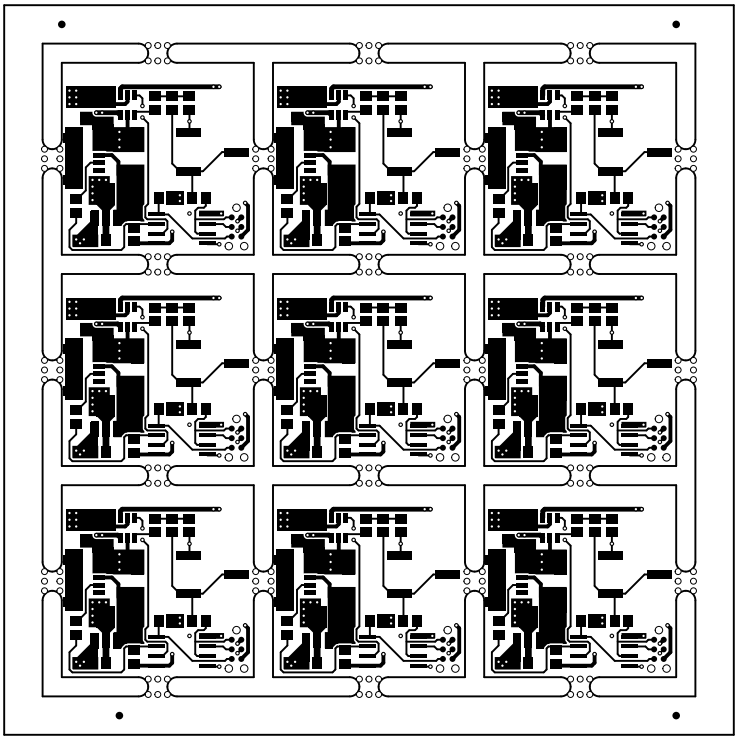
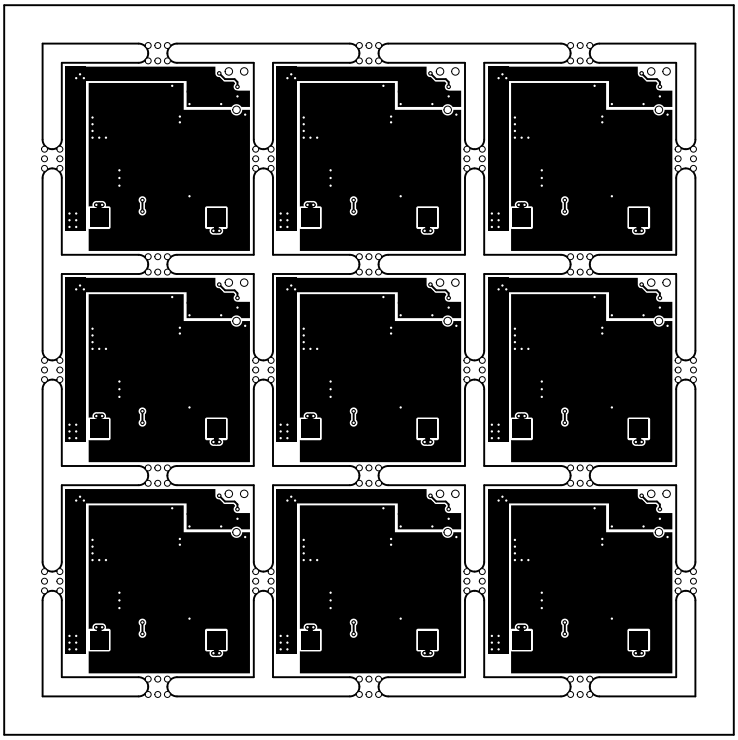


top, scale = 1:1.000
ckt-dingdong-array.pcb



bottom (mirrored), scale = 1:1.000
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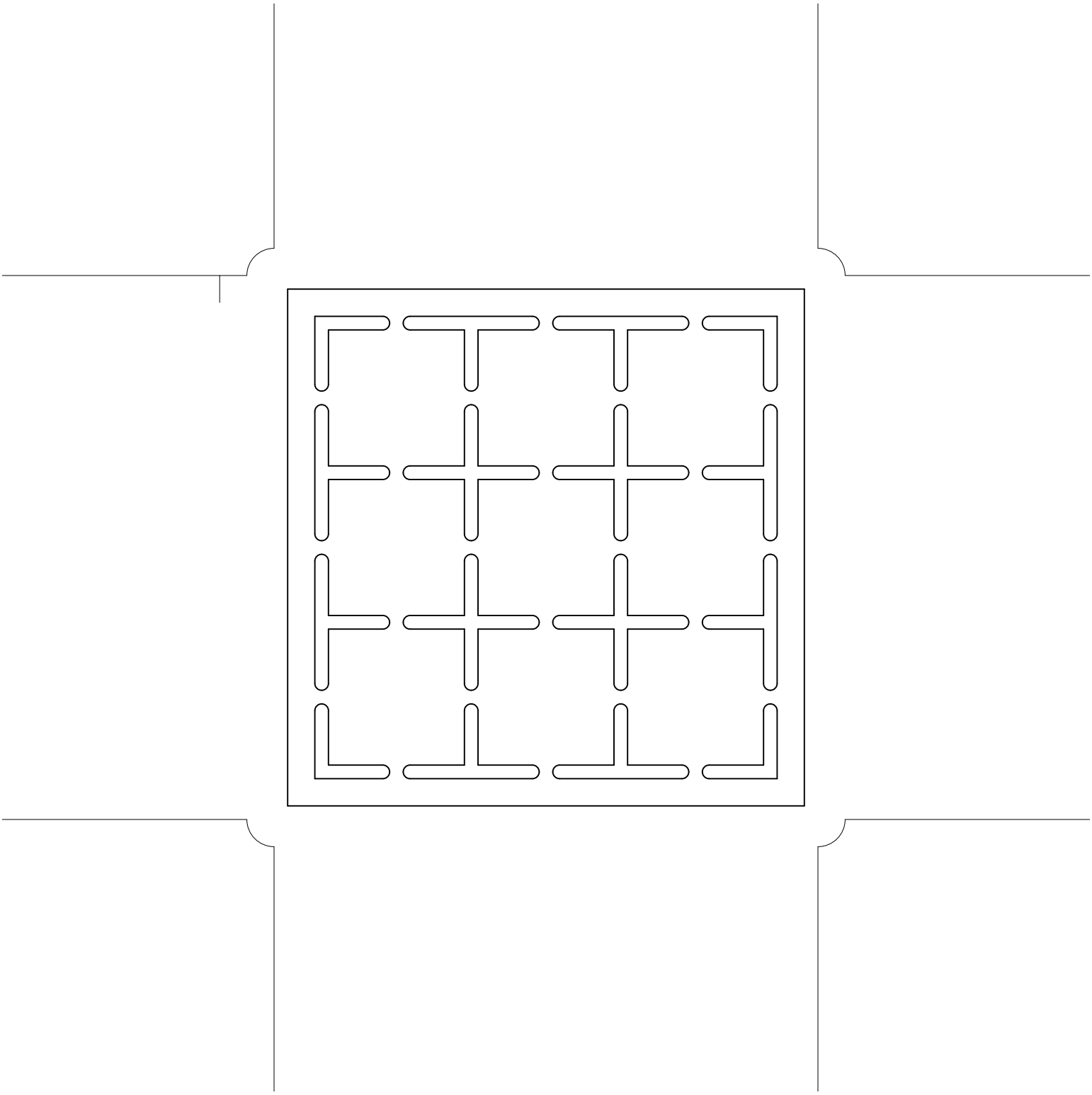

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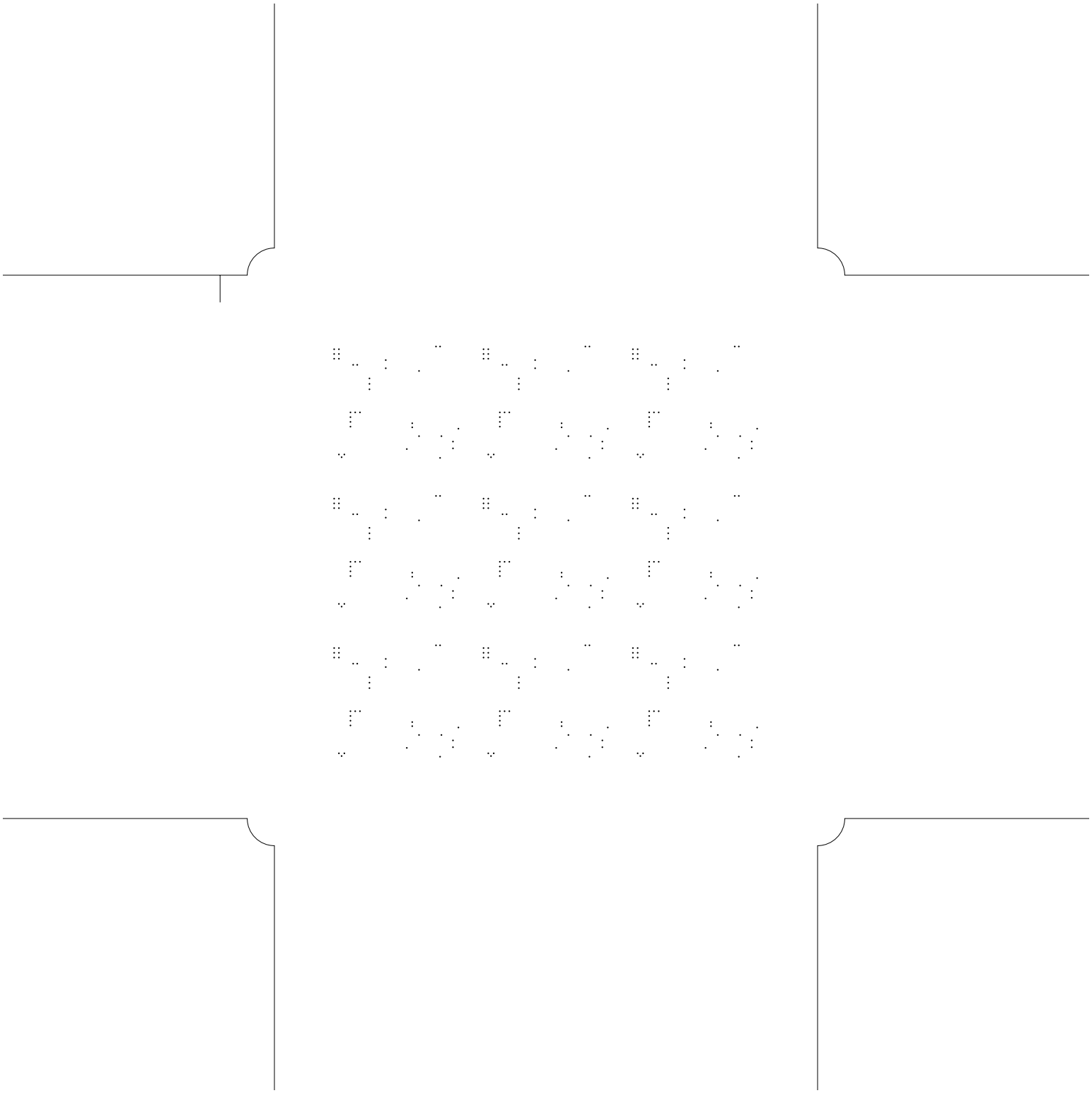

plated-drill, scale = 1:1.000
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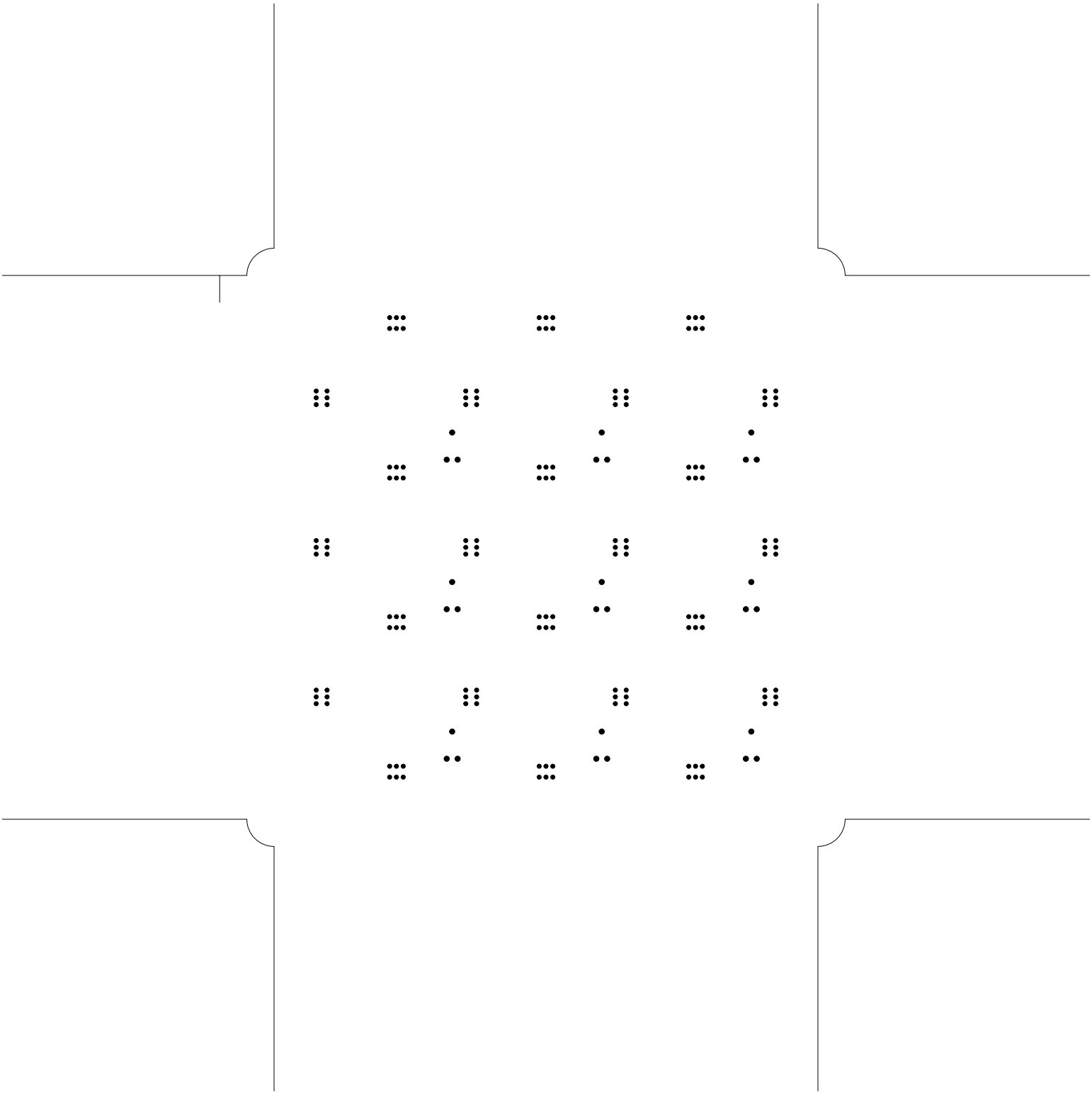

unplated-drill, scale = 1:1.000
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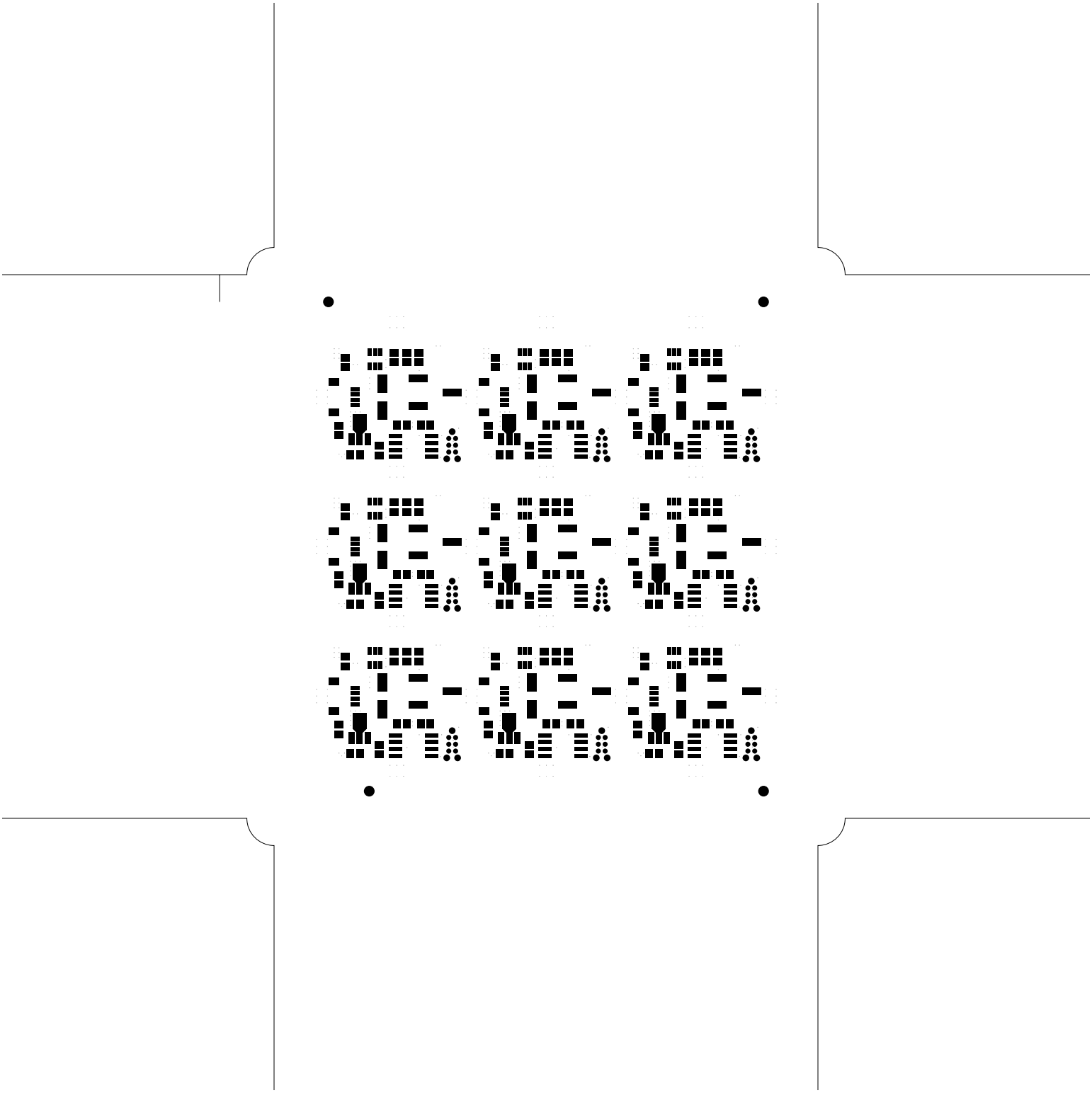

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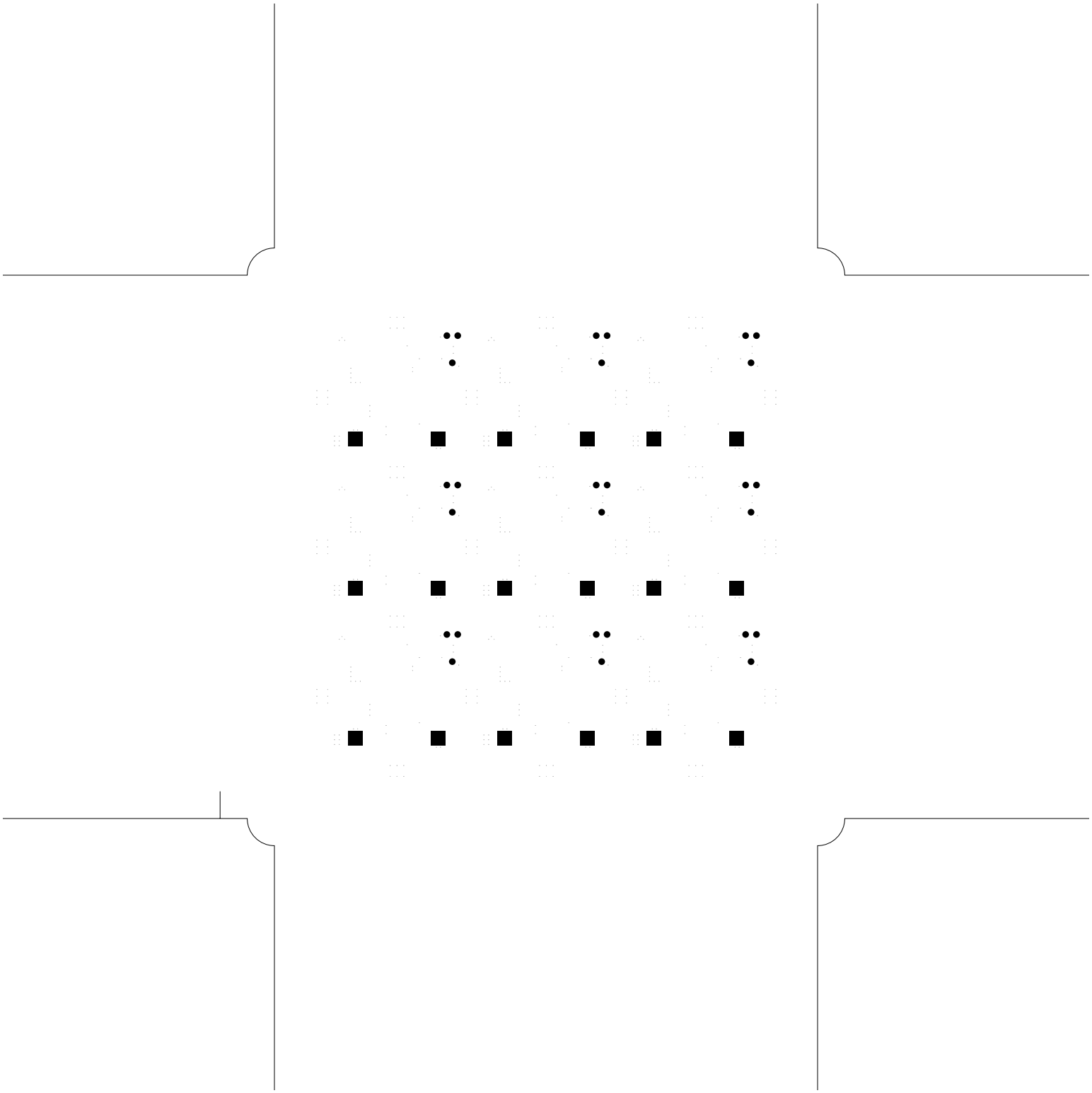
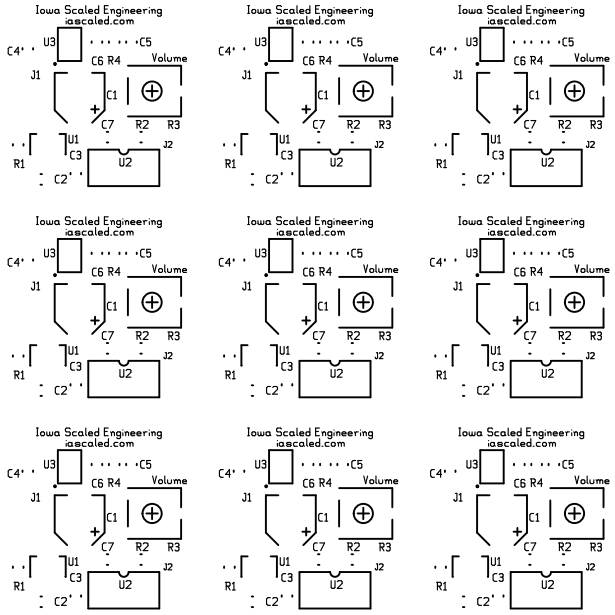
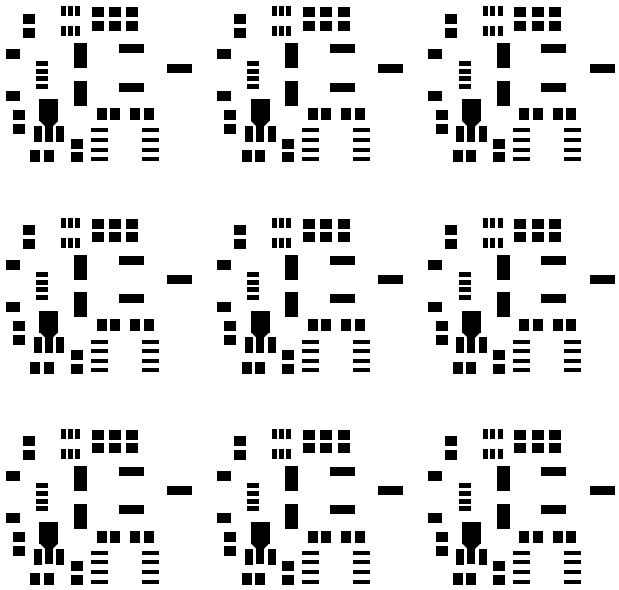


bottommask (mirrored), scale = 1:1.000
ckt-dingdong-array.pcb



topsilk, scale = 1:1.000
ckt-dingdong-array.pcb

bottomsilk (mirrored), scale = 1:1.000
ckt-dingdong-array.pcb



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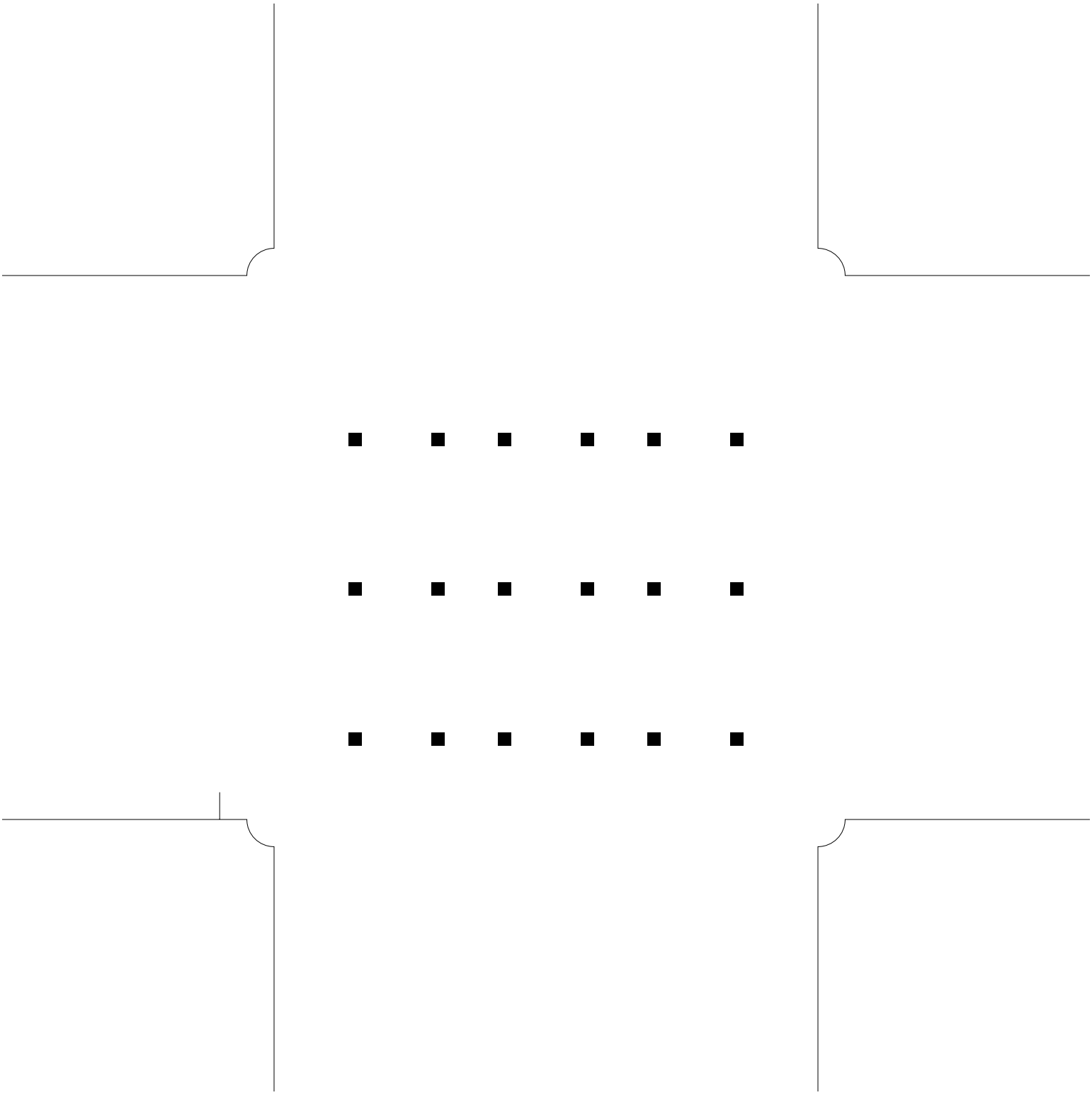

bottompaste (mirrored), scale = 1:1.000
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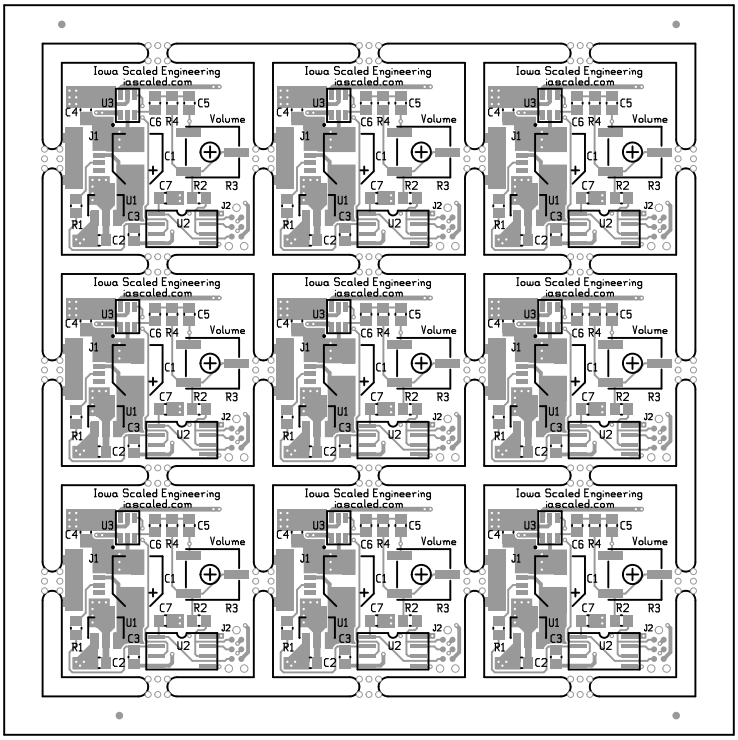
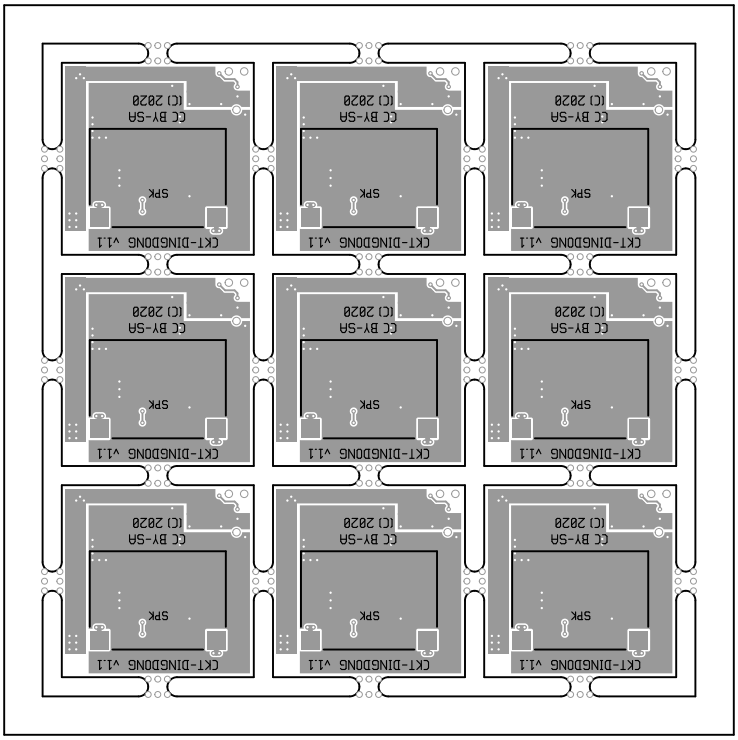


topassembly, scale = 1:1.000
ckt-dingdong-array.pcb

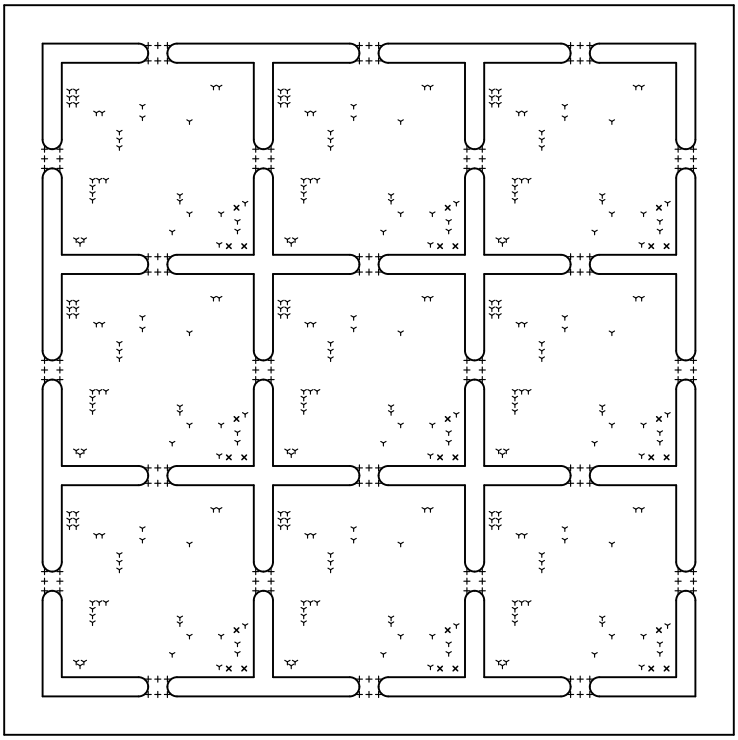


bottomassembly (mirrored), scale = 1:1.000
ckt-dingdong-array.pcb

There are 3 different drill sizes used in this layout, 477 holes total

Symbol	Diam. (Inch)	Count	Plated?
˟	0.012	306	YES
+	0.031	144	NO
×	0.039	27	NO

Title: (unknown) - Fabrication Drawing
Author: Michael Petersen
Date: Sun May 15 03:11:04 2022 UTC



Board outline is the centerline of this path

fab, scale = 1:1.000
ckt-dingdong-array.pcb