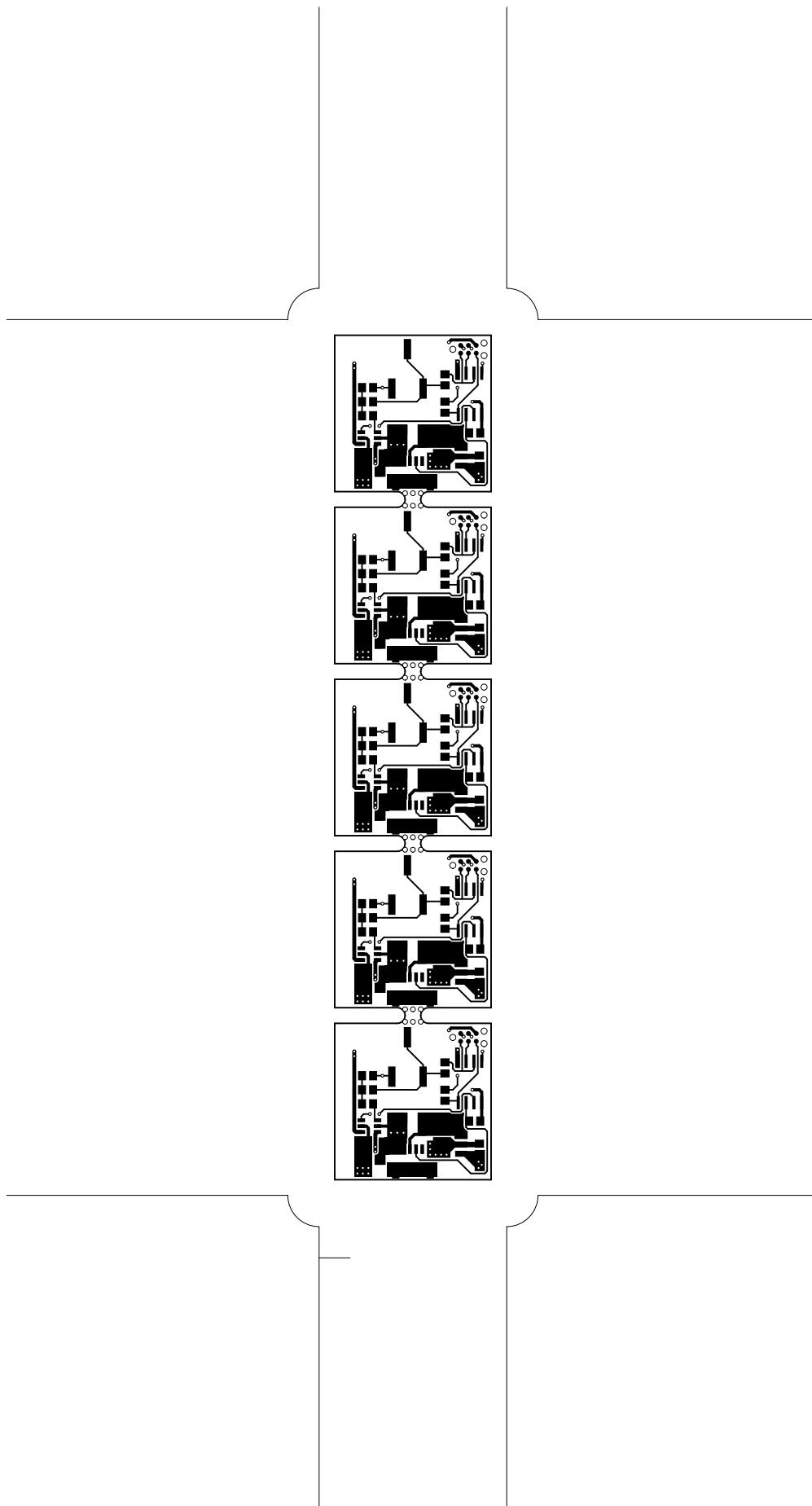
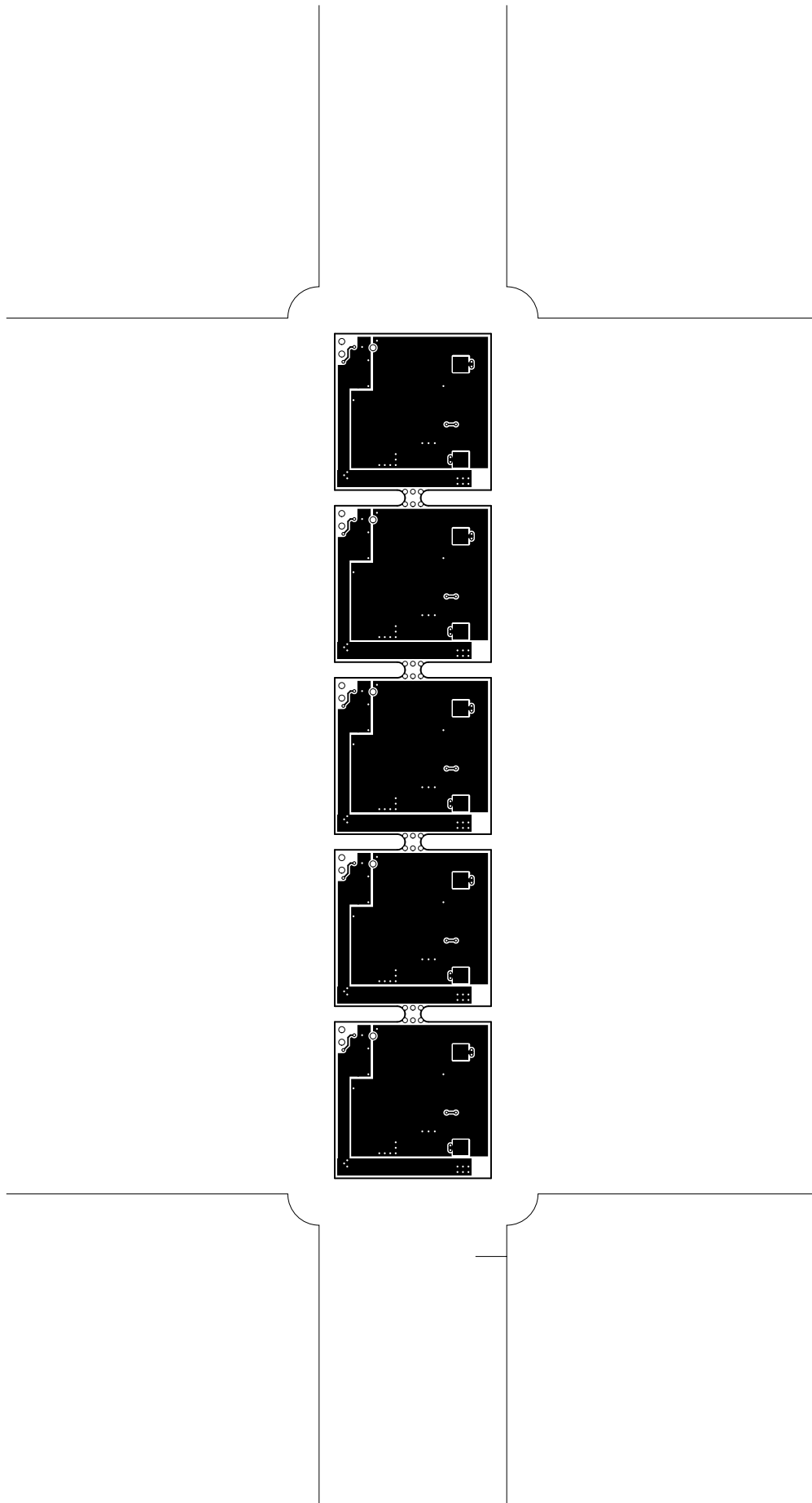


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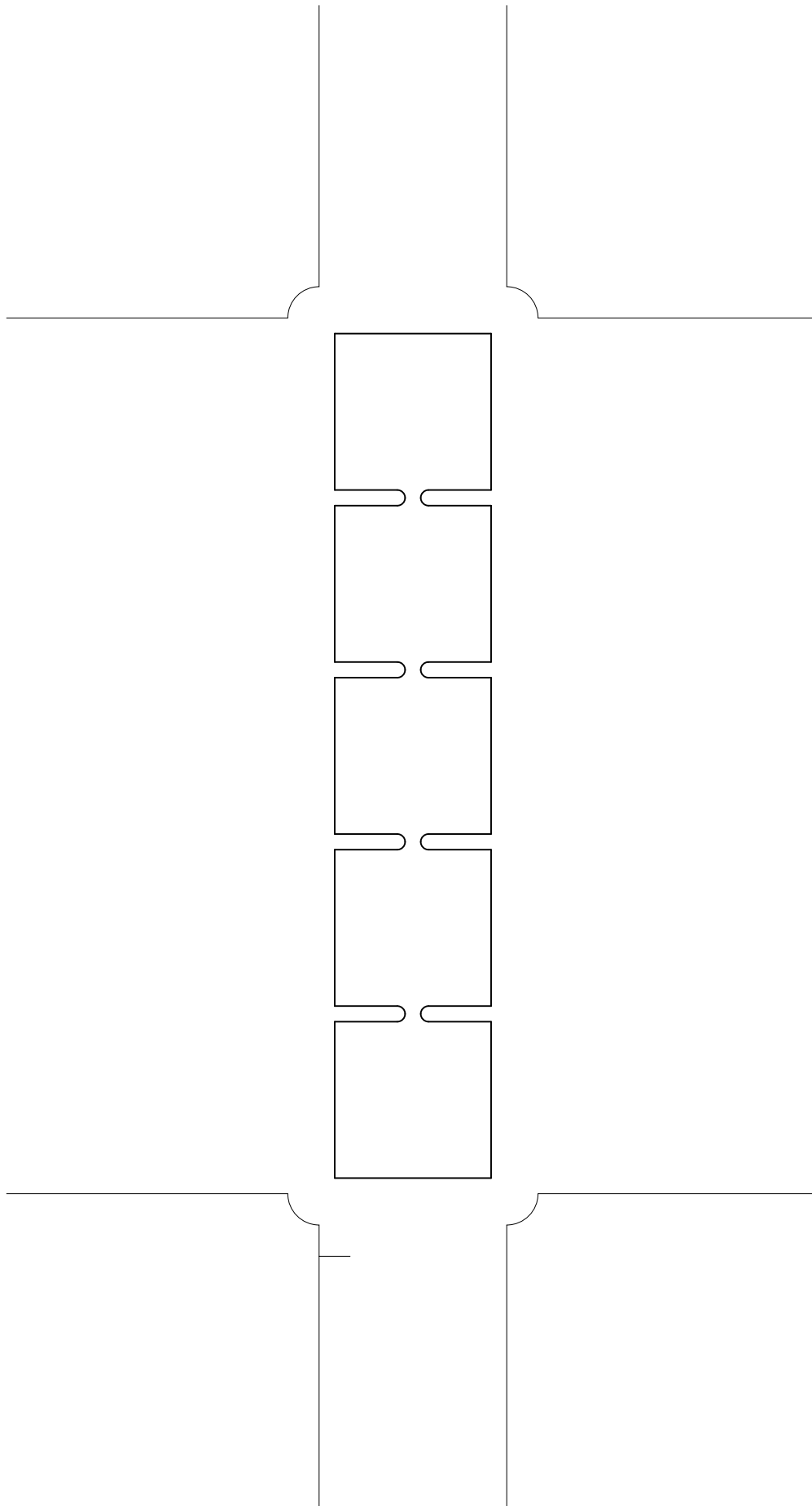
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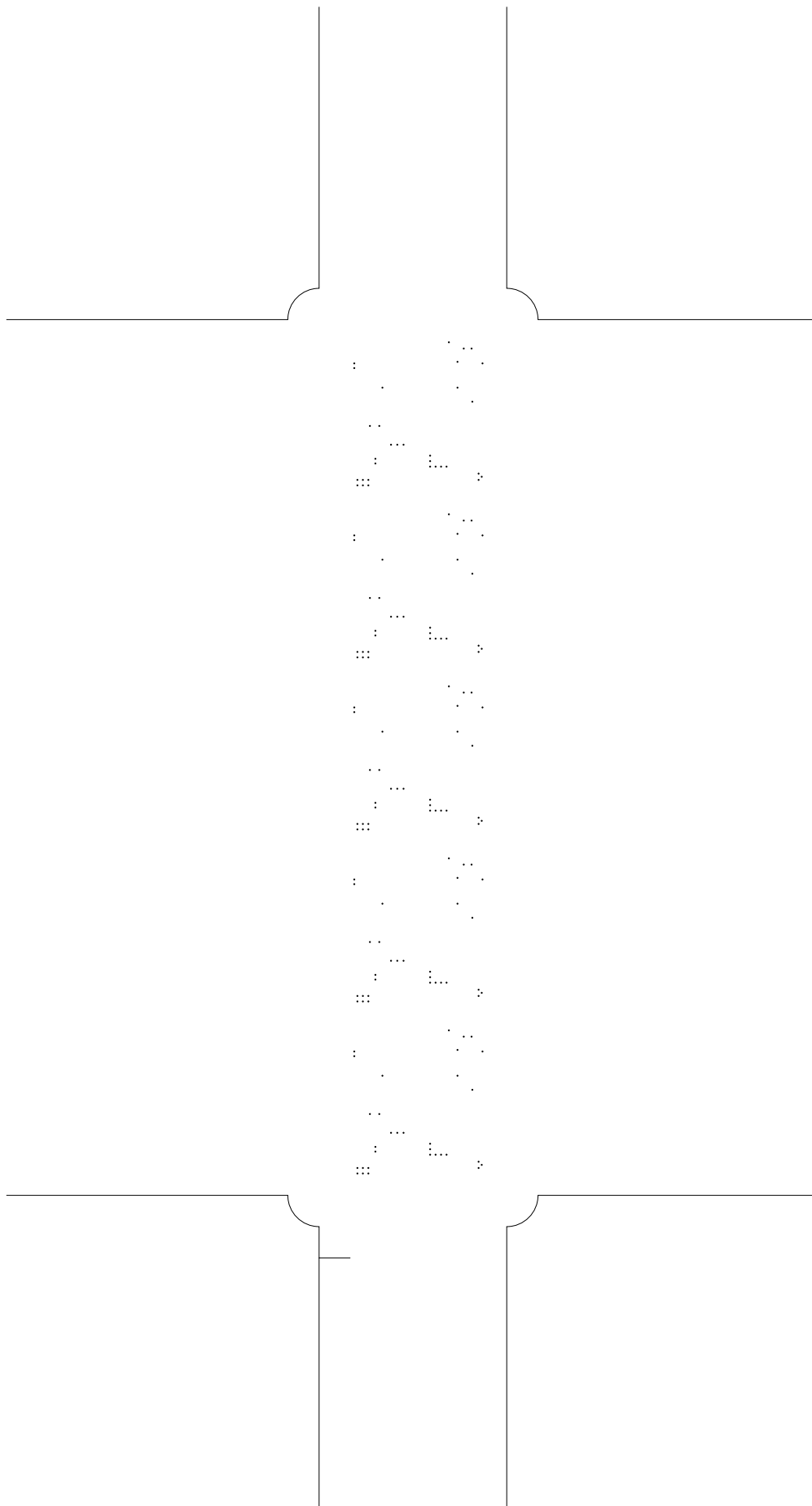
- 2. top
- 3. bottom
- 4. outline
- 5. plated-drill
- 6. unplated-drill
- 7. componentmask
- 8. soldermask
- 9. topsilk
- 10. bottomsilk
- 11. toppaste
- 12. bottompaste
- 13. topassembly
- 14. bottomassembly
- 15. fab

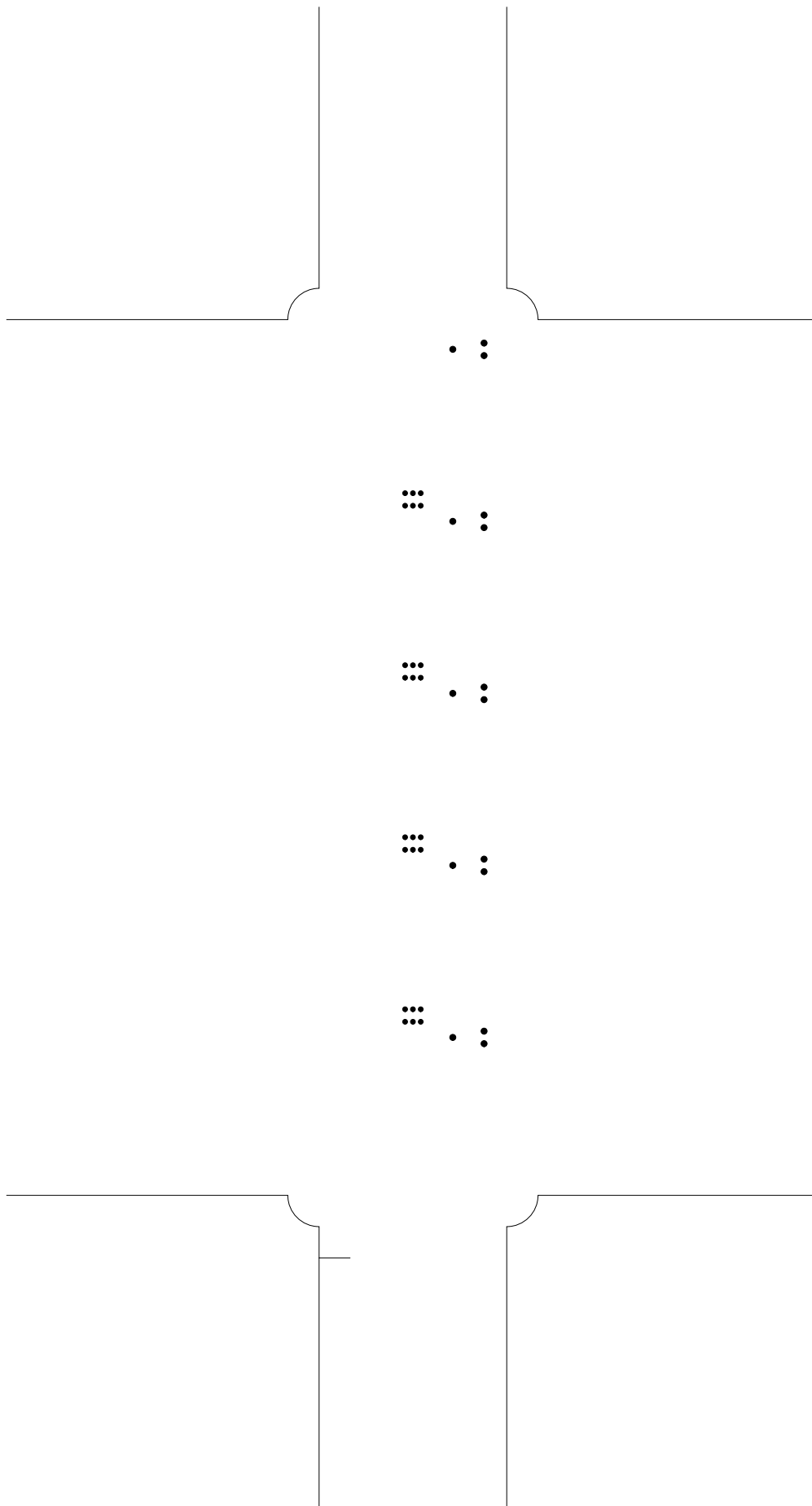


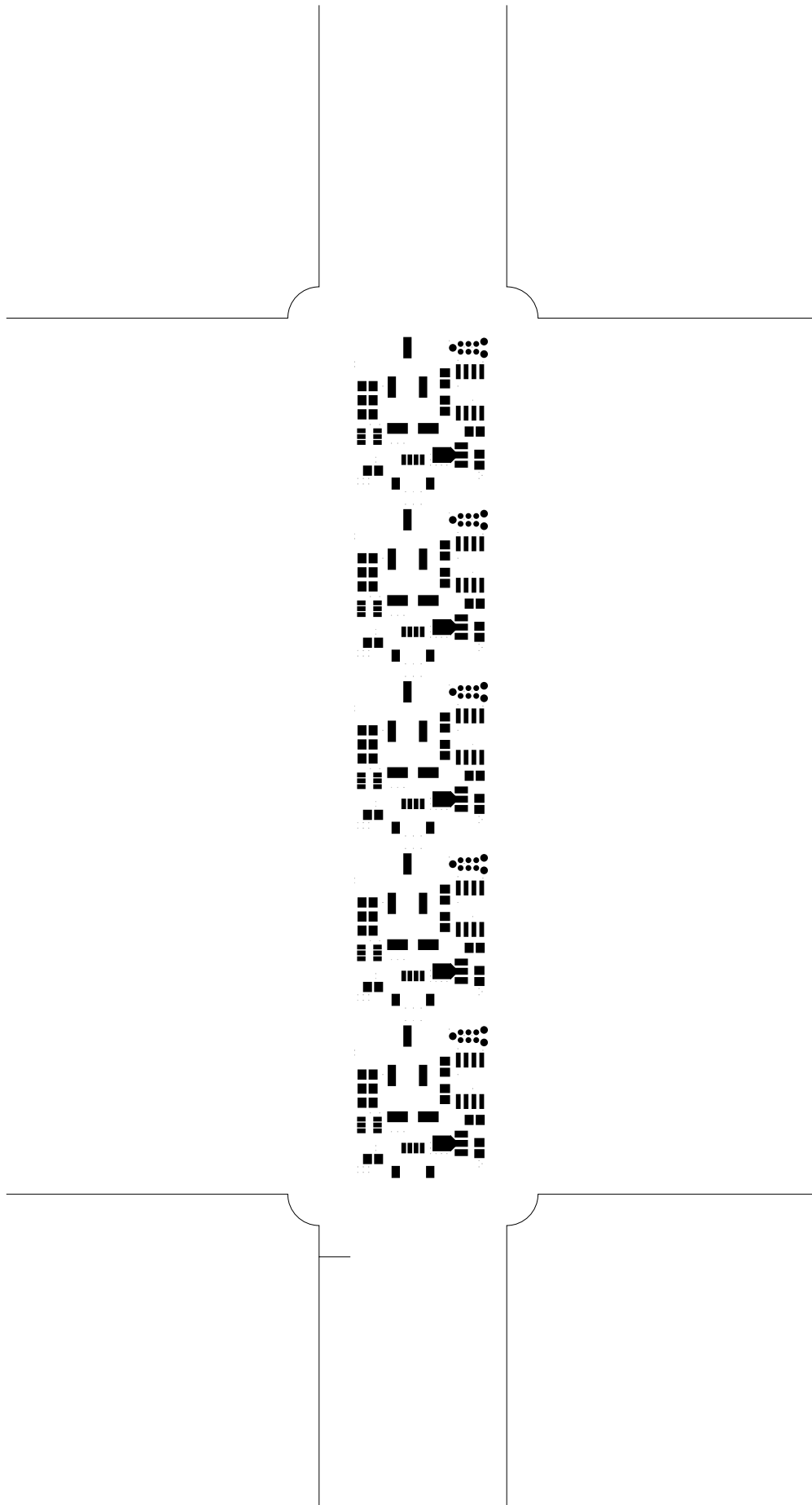


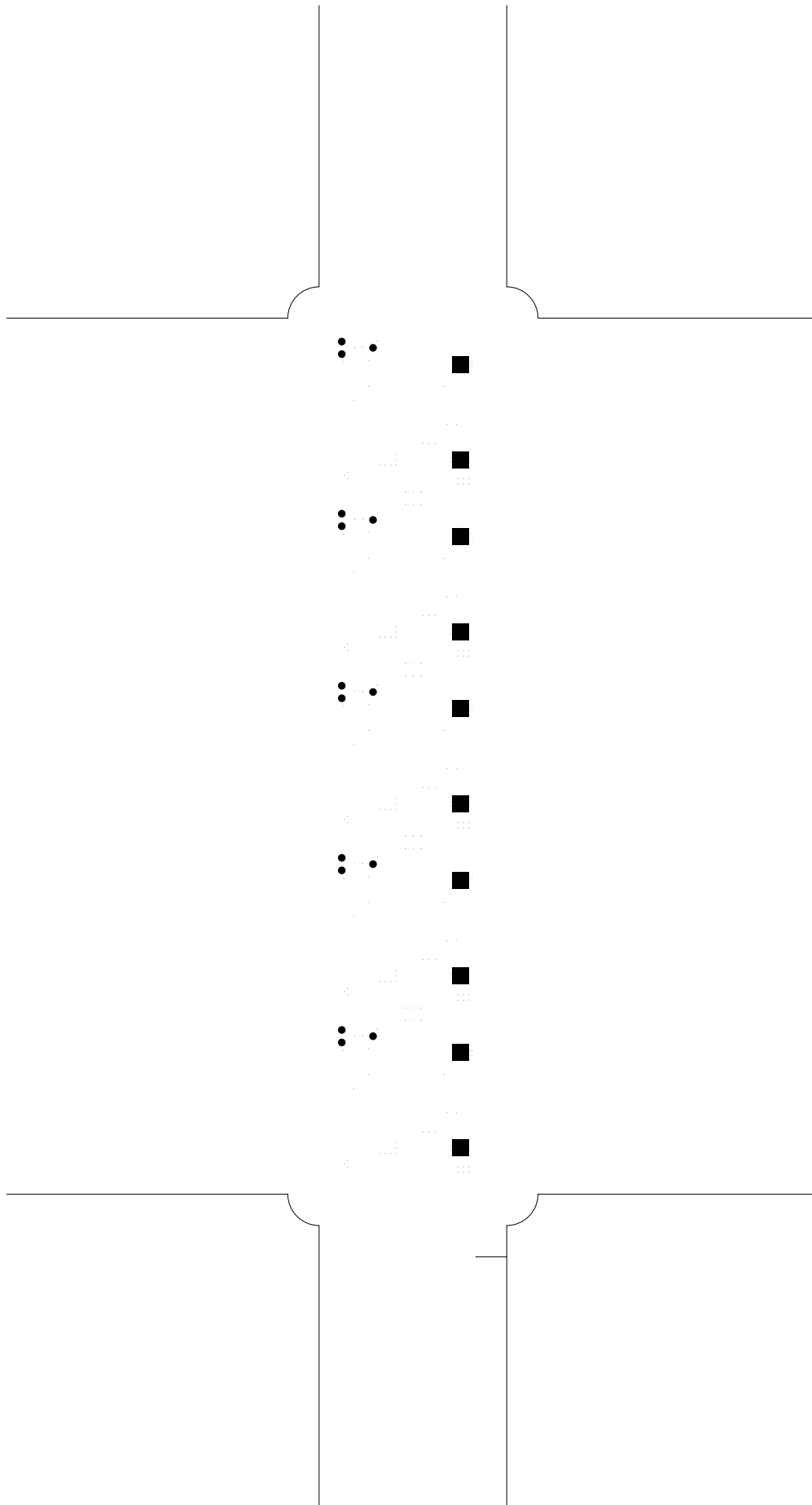
bottom (mirrored), scale = 1:1.000
ckt-dingdong-panel.pcb

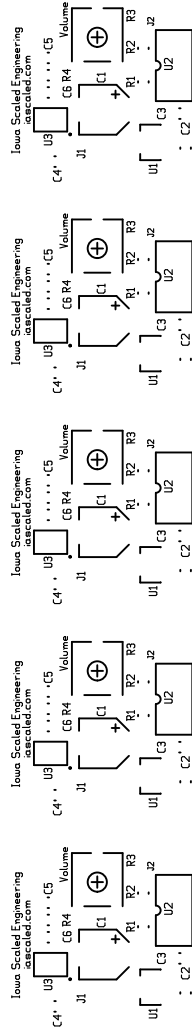


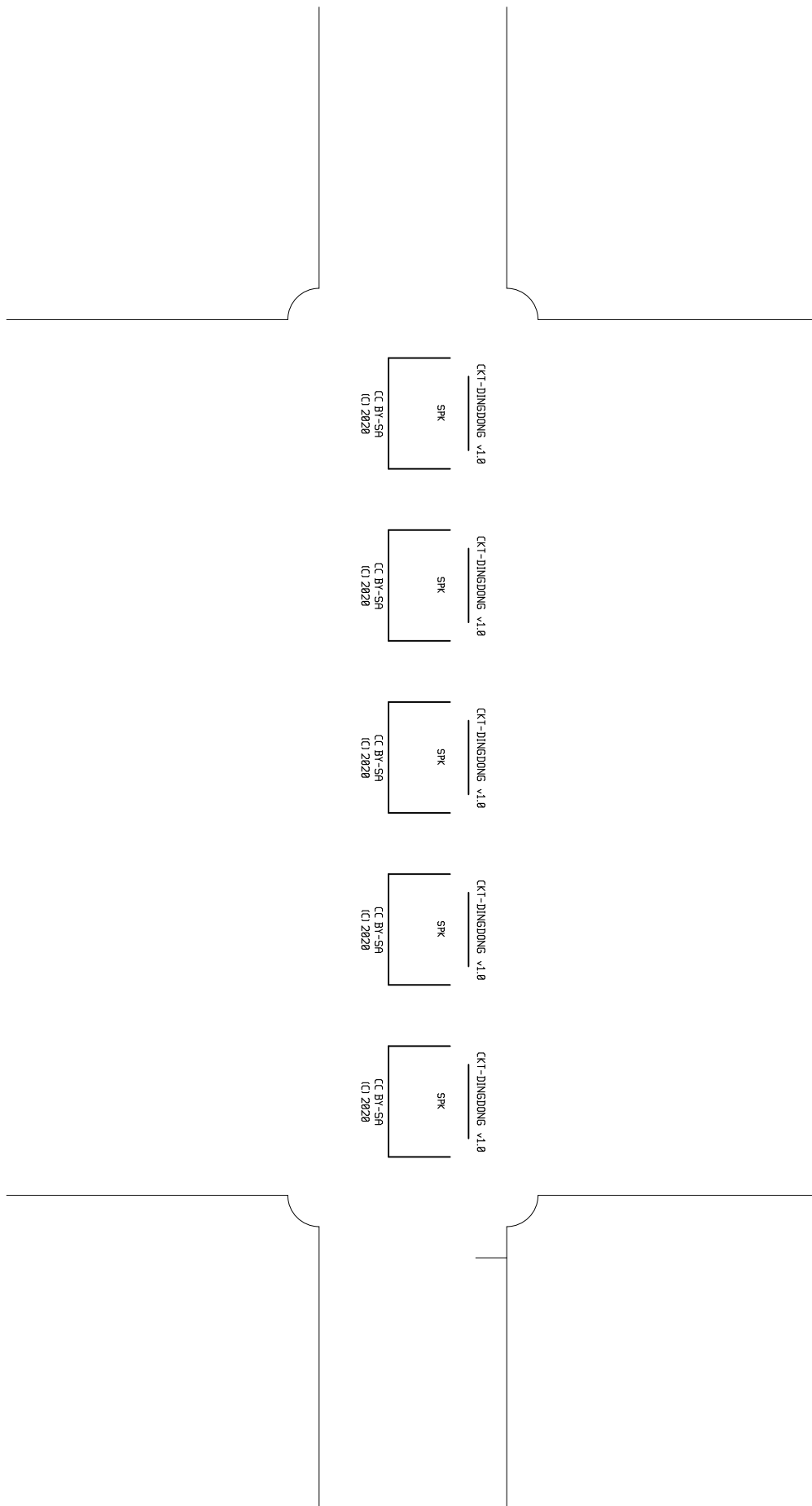




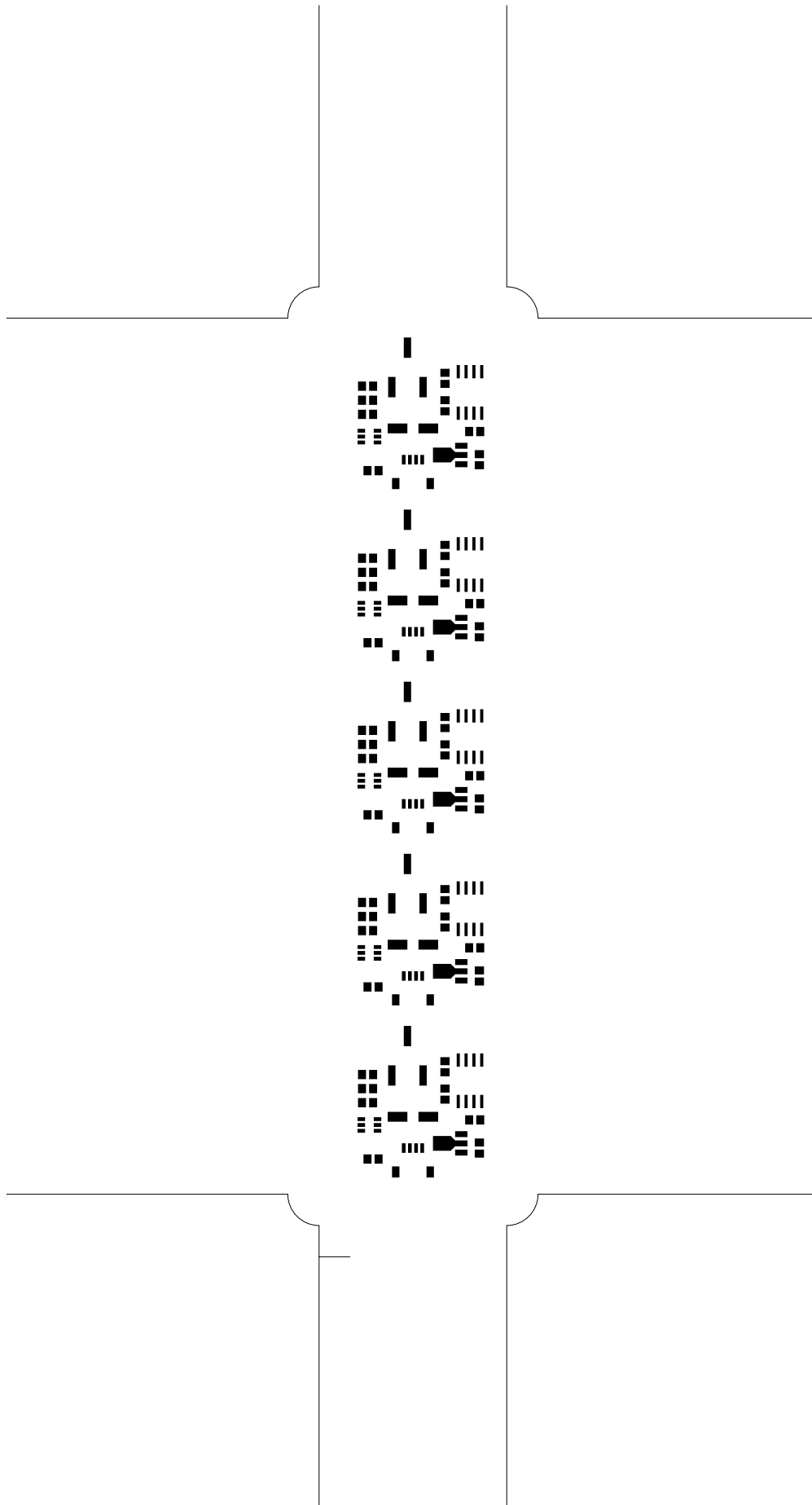


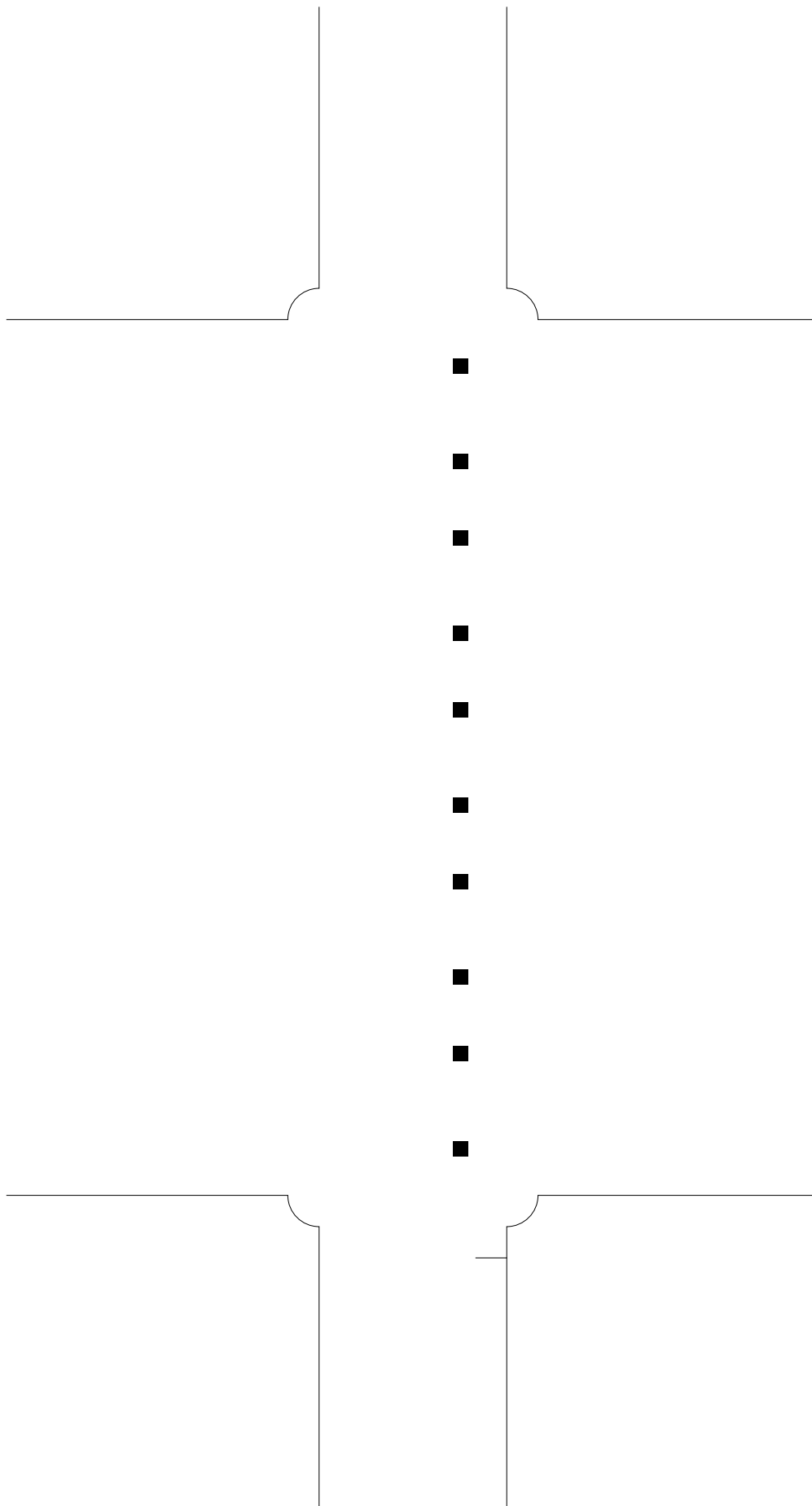


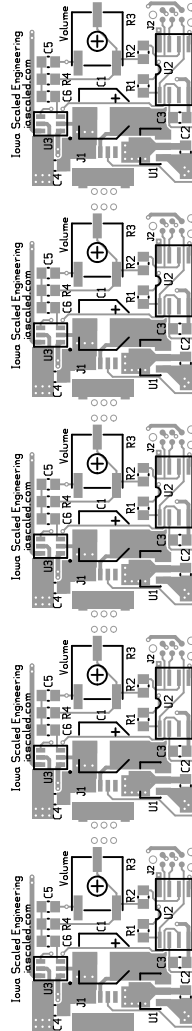


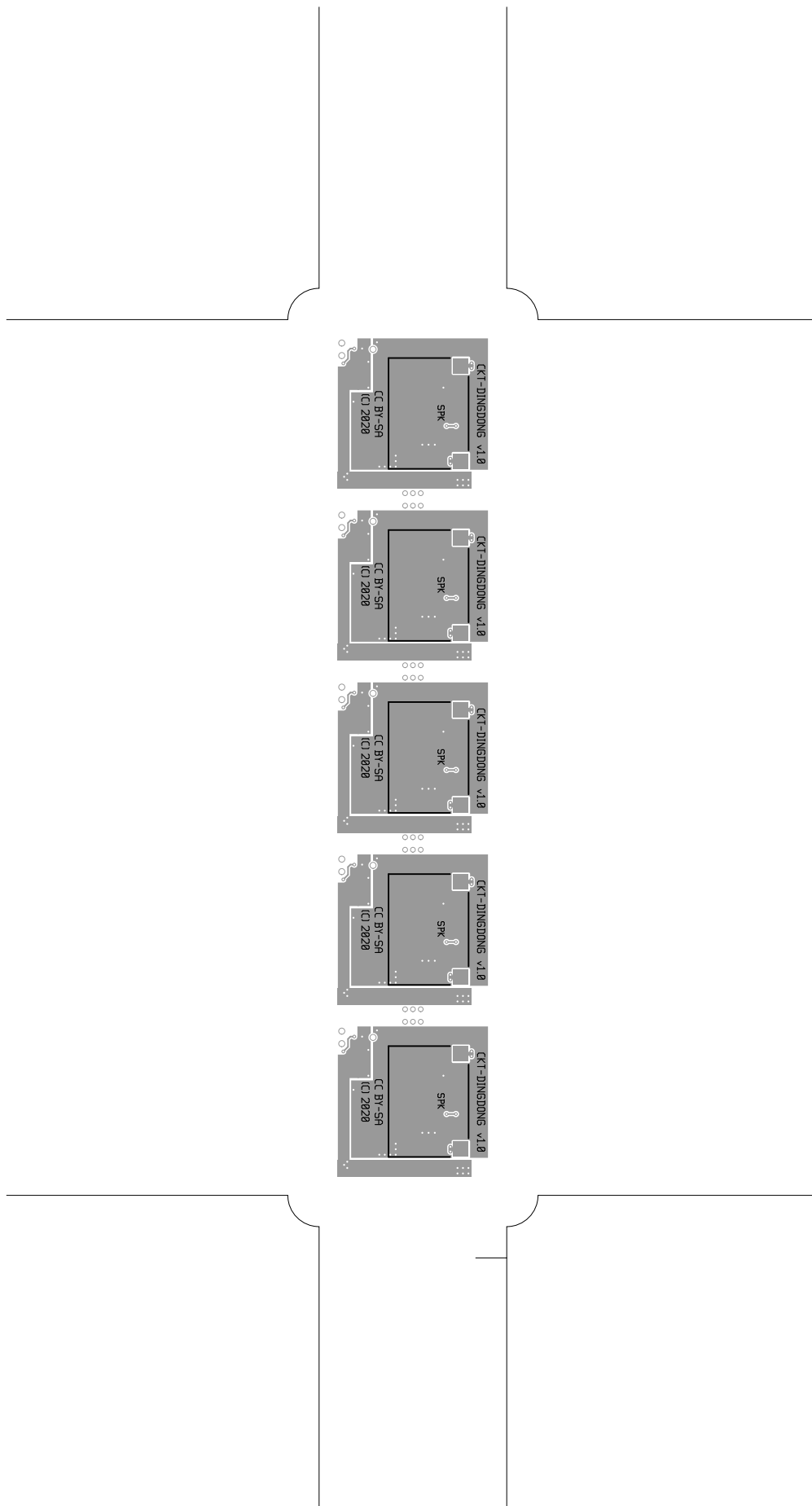


bottomsilk (mirrored), scale = 1:1.000
ckt-dingdong-panel.pcb



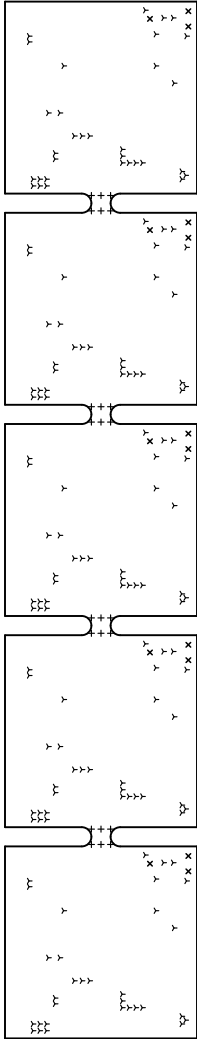






There are 3 different drill sizes used in this layout, 199 holes total

Symbol	Diam. (Inch)	Count	Plated?	Title: (unknown) - Fabrication Drawing
y	0.012	160	YES	Author: Michael Petersen
+	0.031	24	NO	Date: Thu 20 Aug 2020 03:04:09 AM GMT UTC
x	0.039	15	NO	



Board outline is the centerline of this path