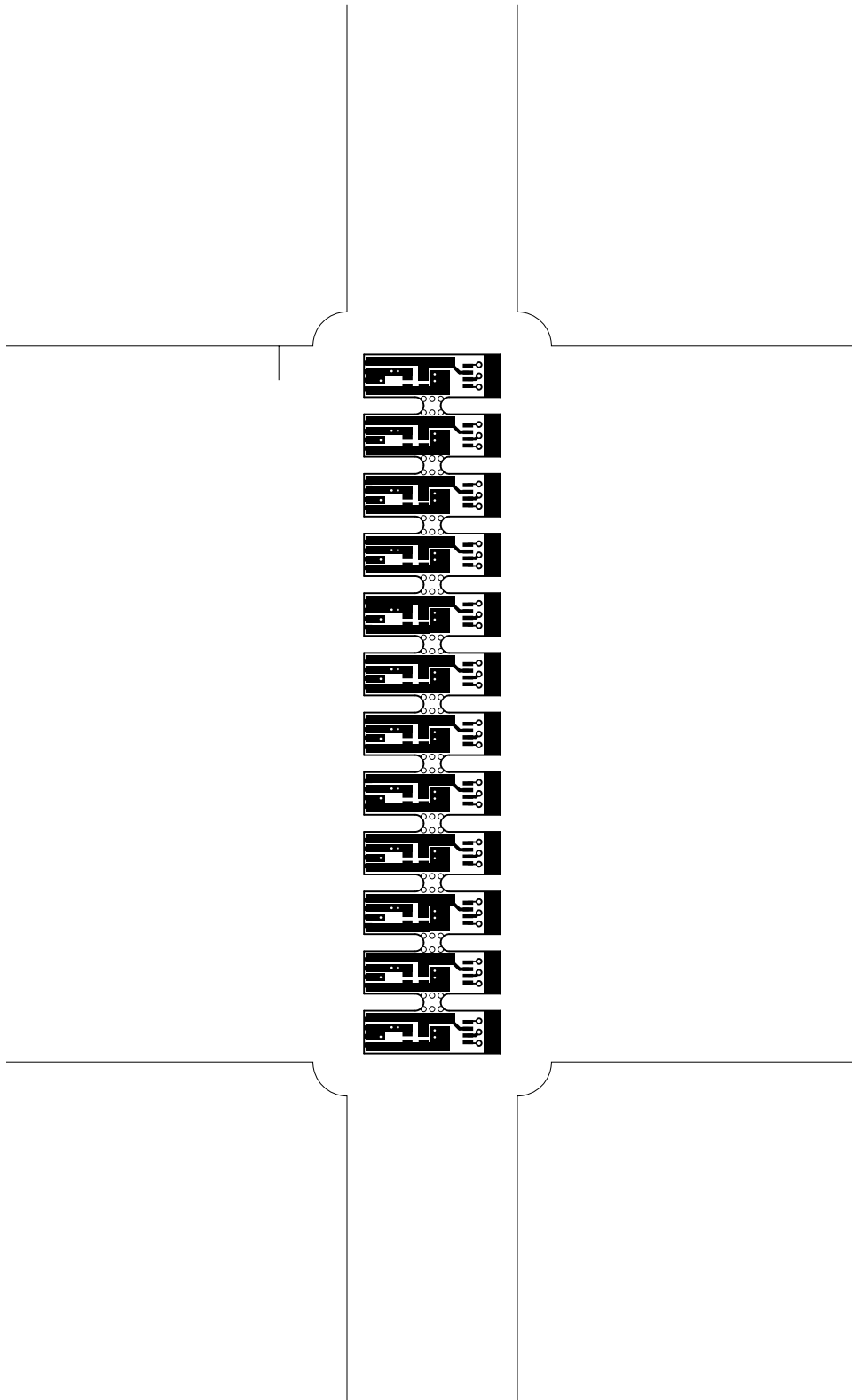
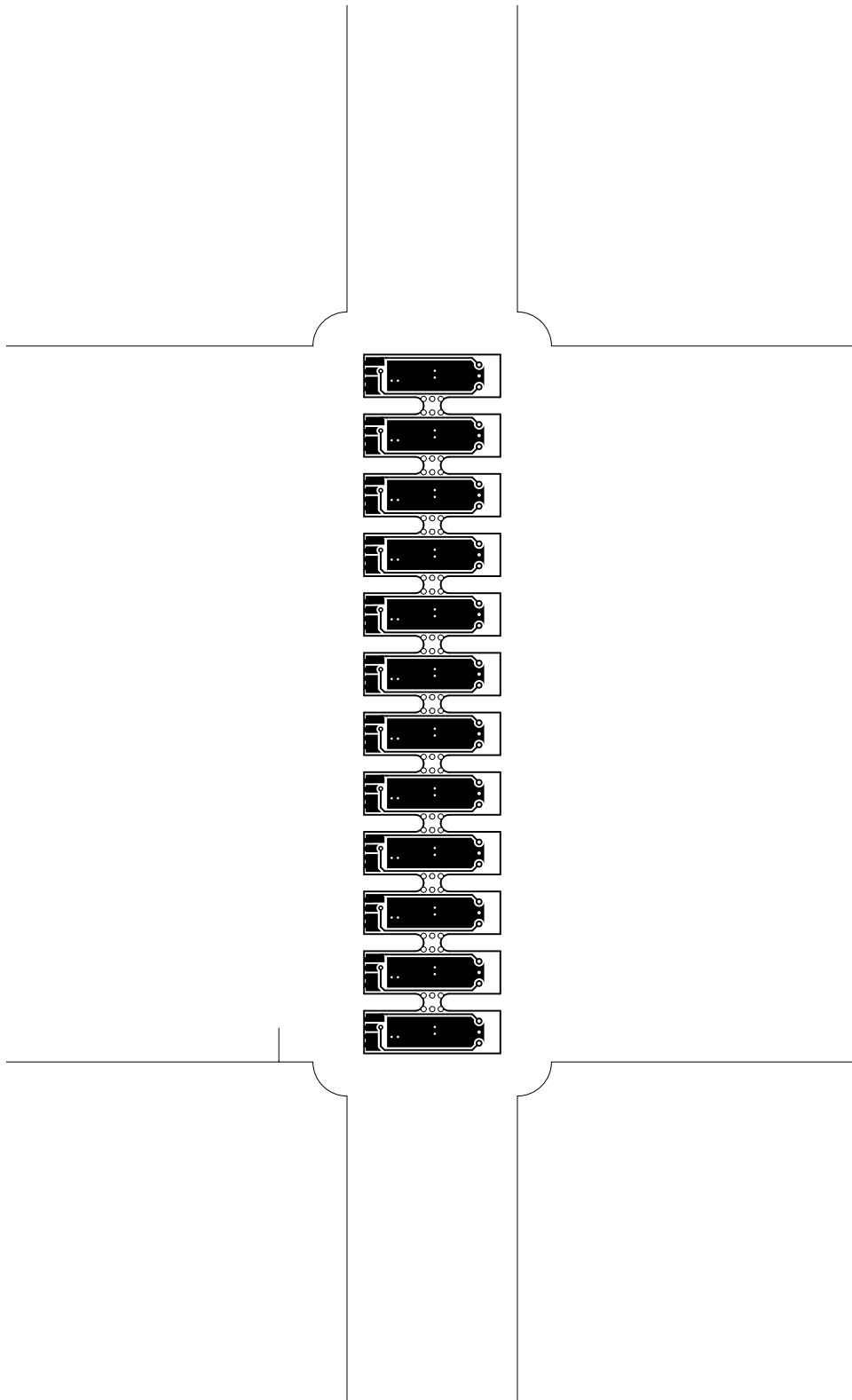


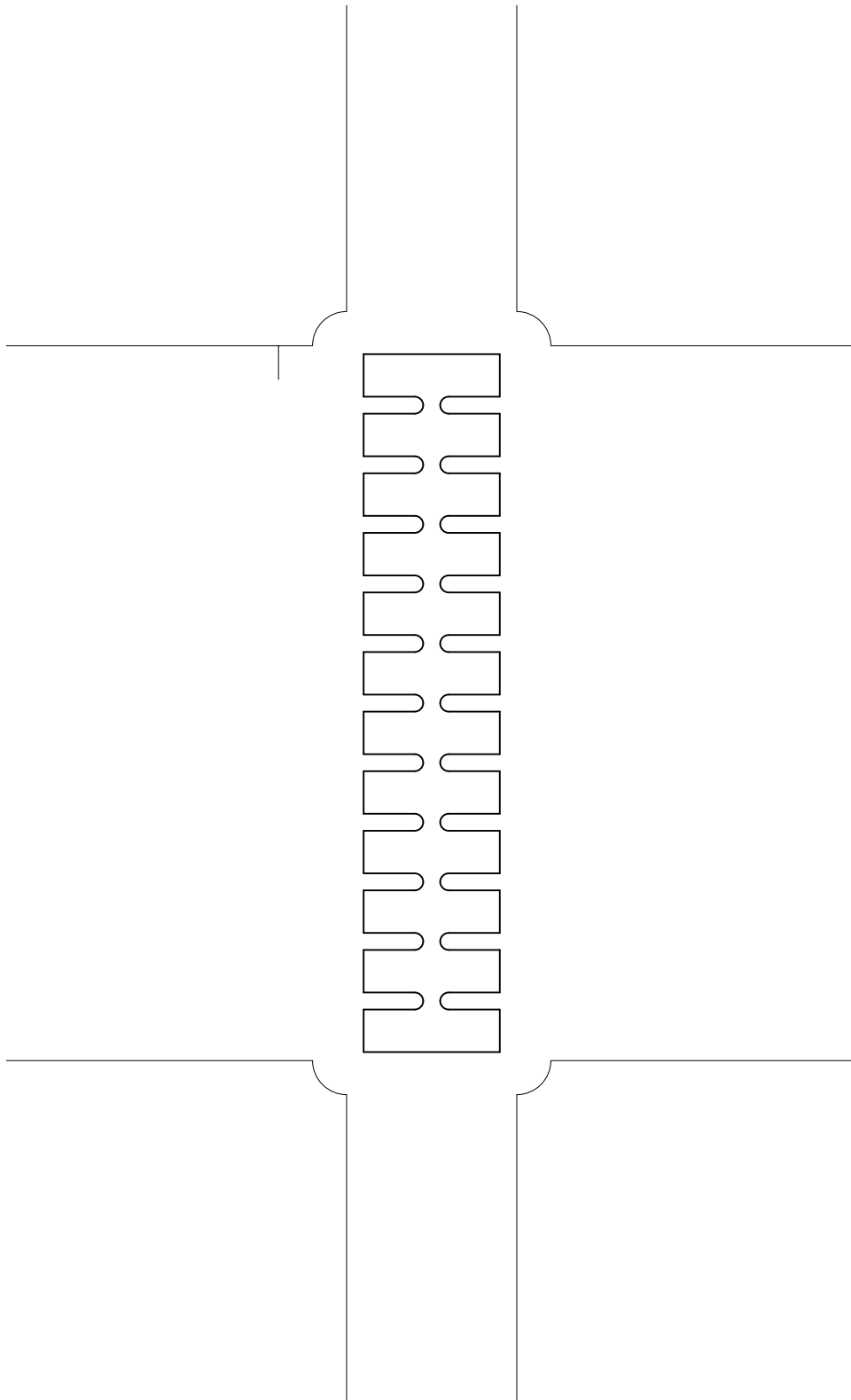
# 1. Table of Contents (This Page)

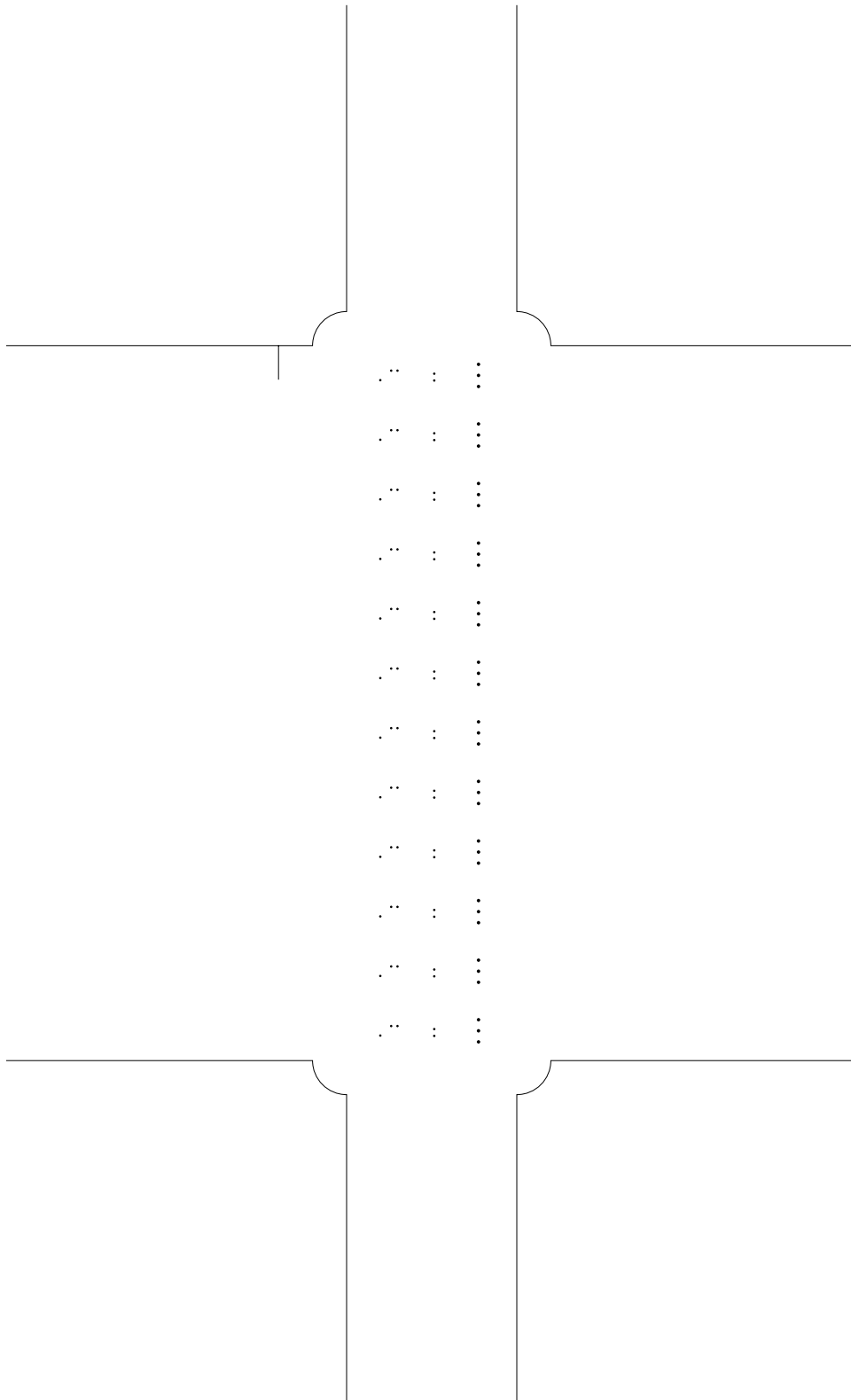
Created on Tue Jan 2 19:51:44 2018

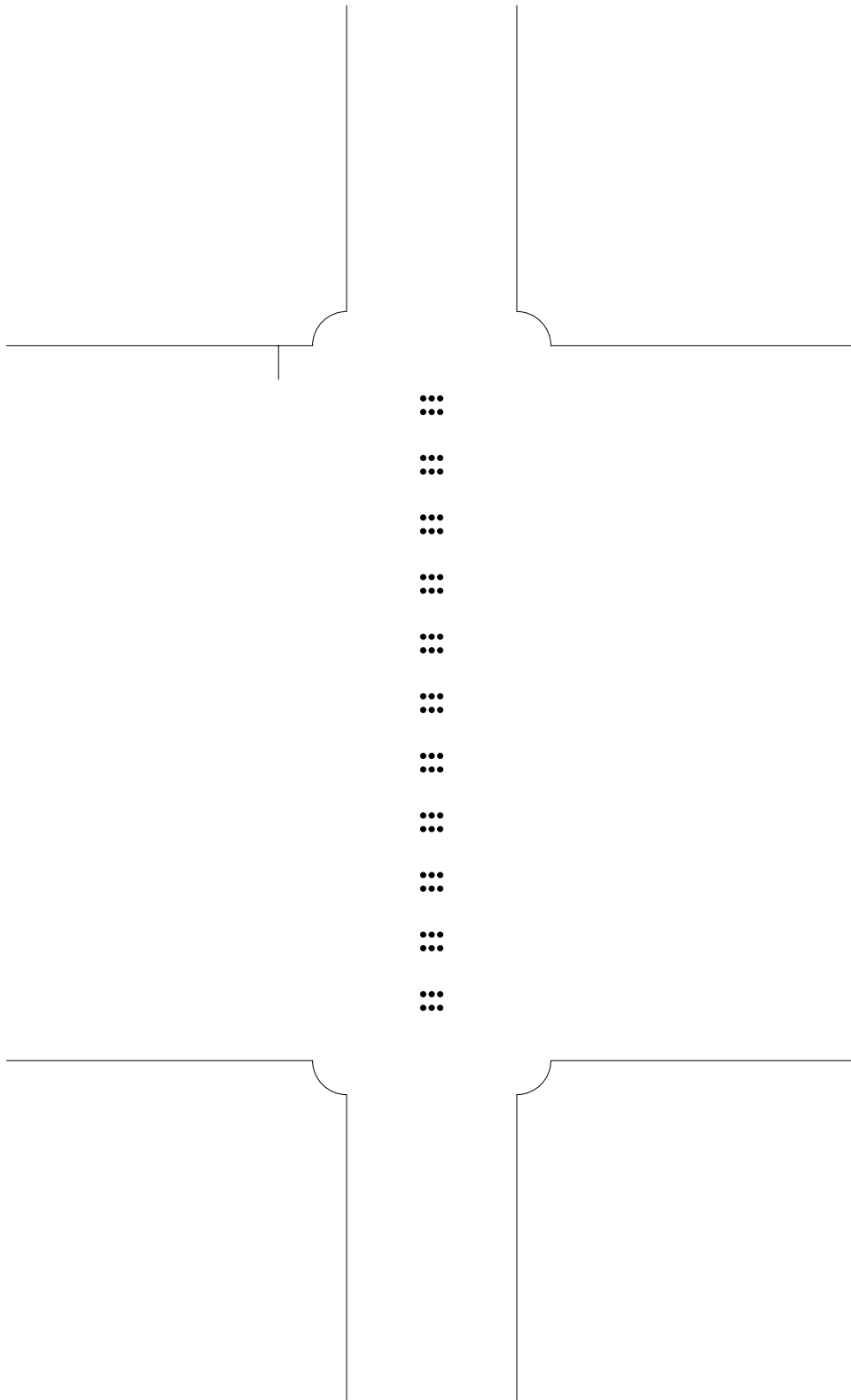
2. top
3. bottom
4. outline
5. plated-drill
6. unplated-drill
7. componentmask
8. soldermask
9. topsilk
10. bottomsilk
11. toppaste
12. topassembly
13. bottomassembly
14. fab

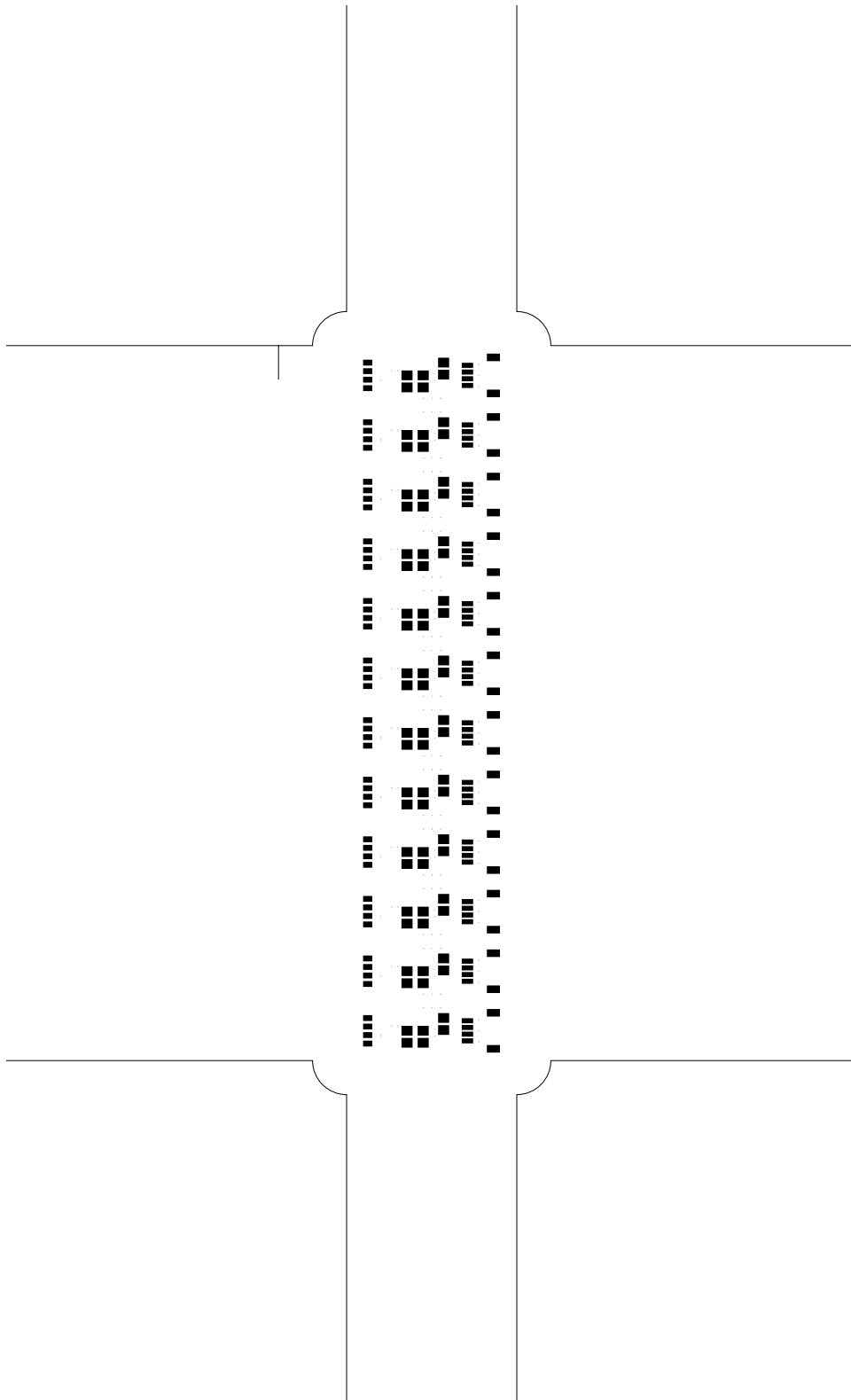


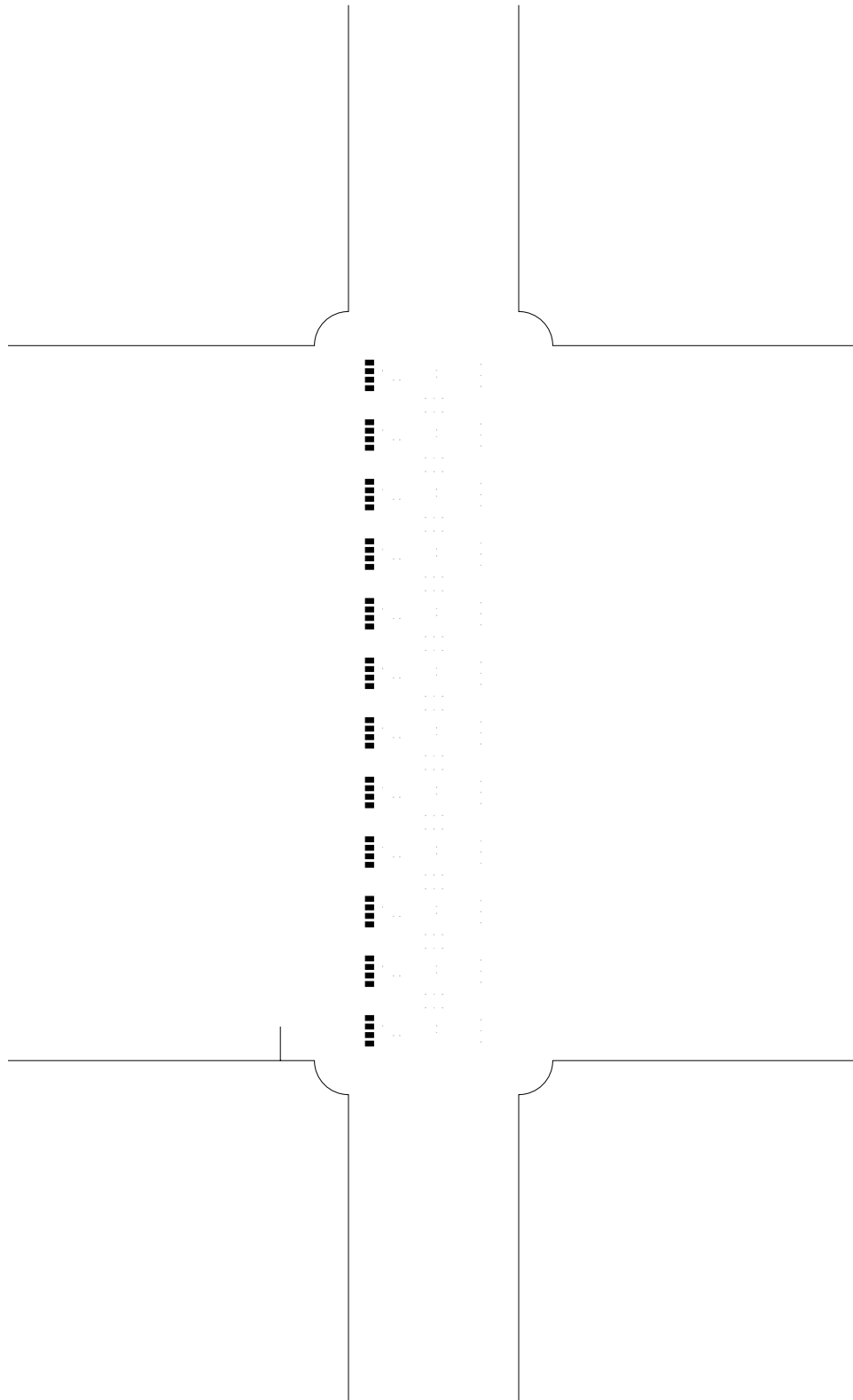




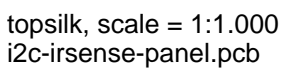


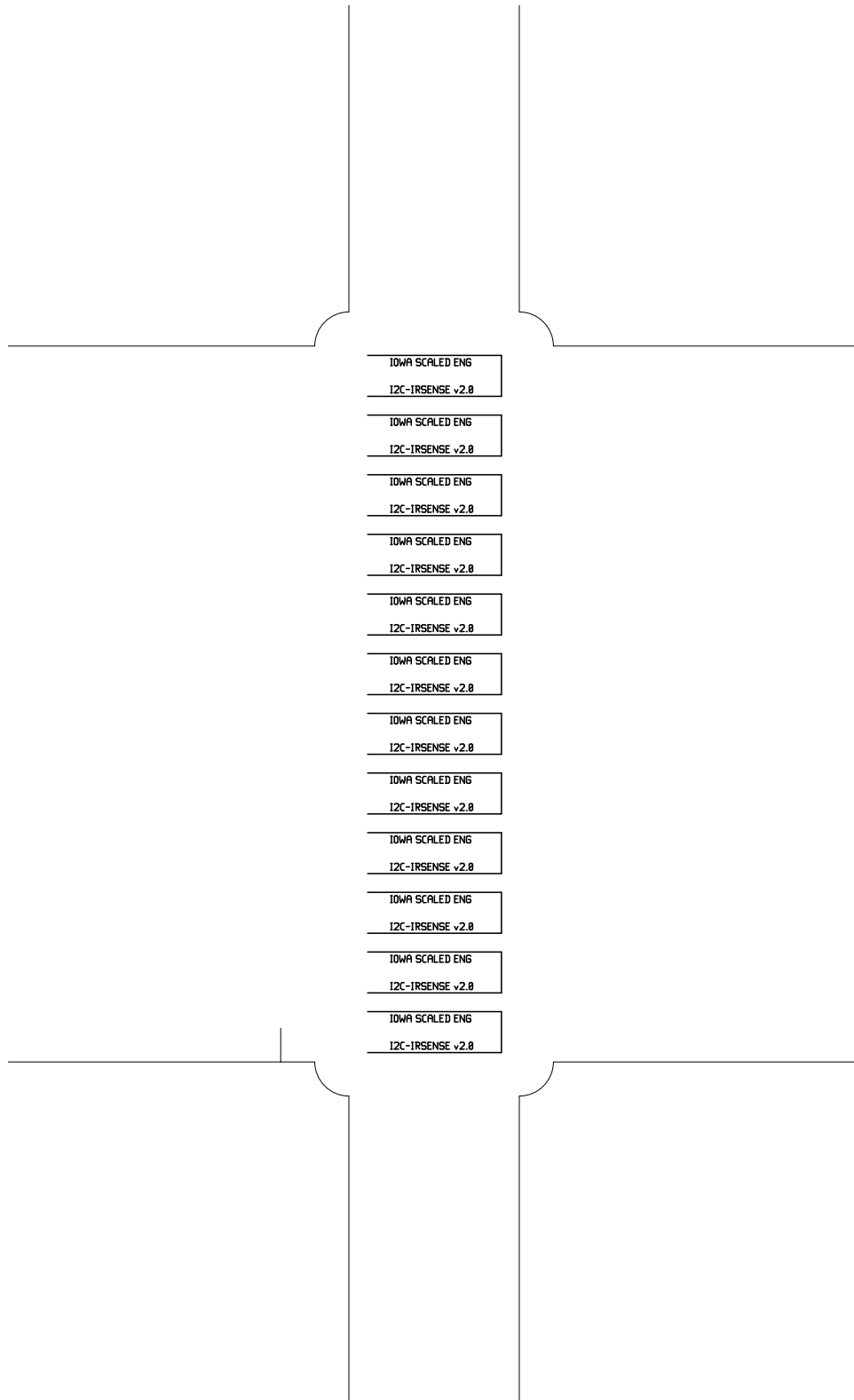


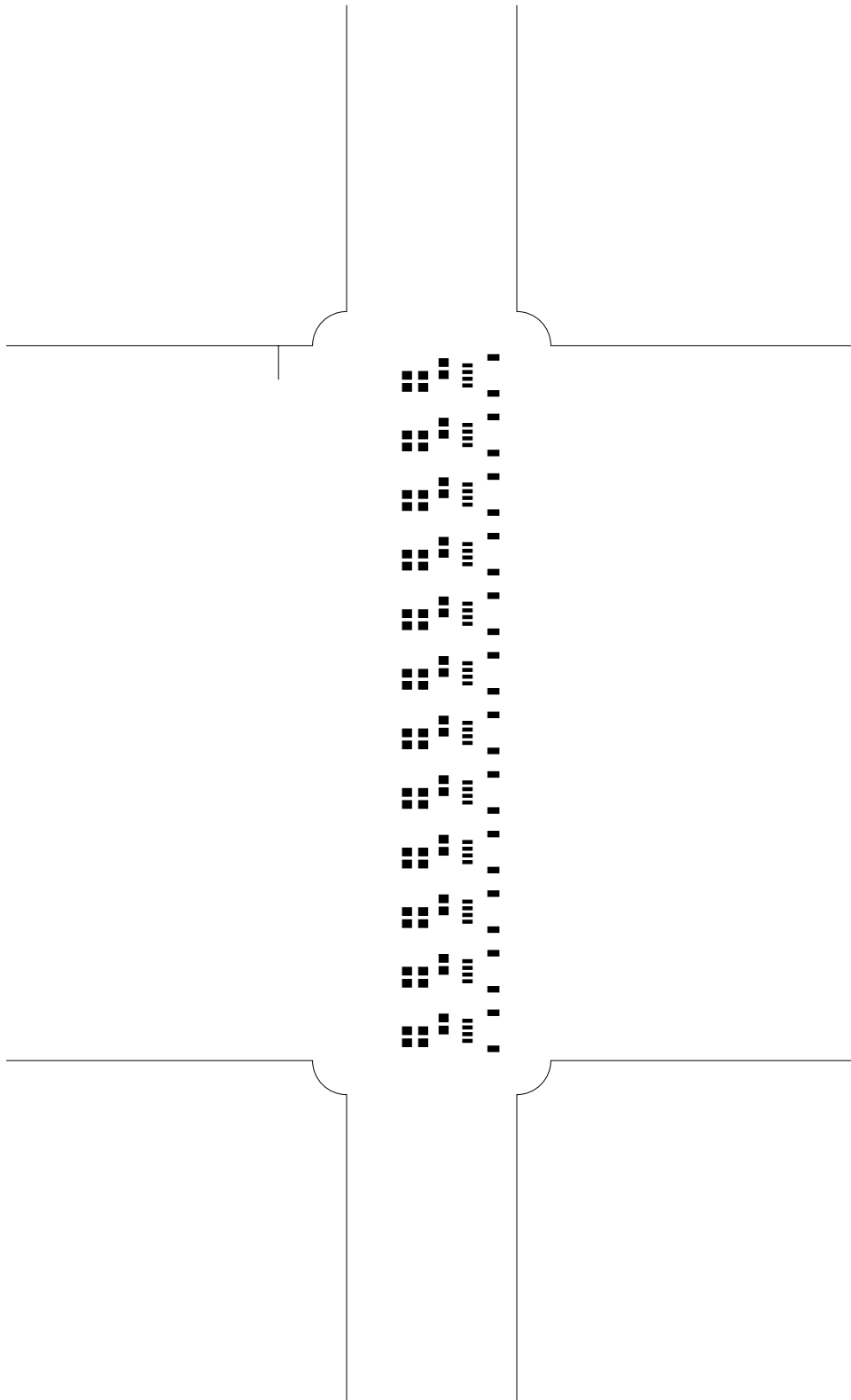


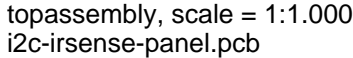


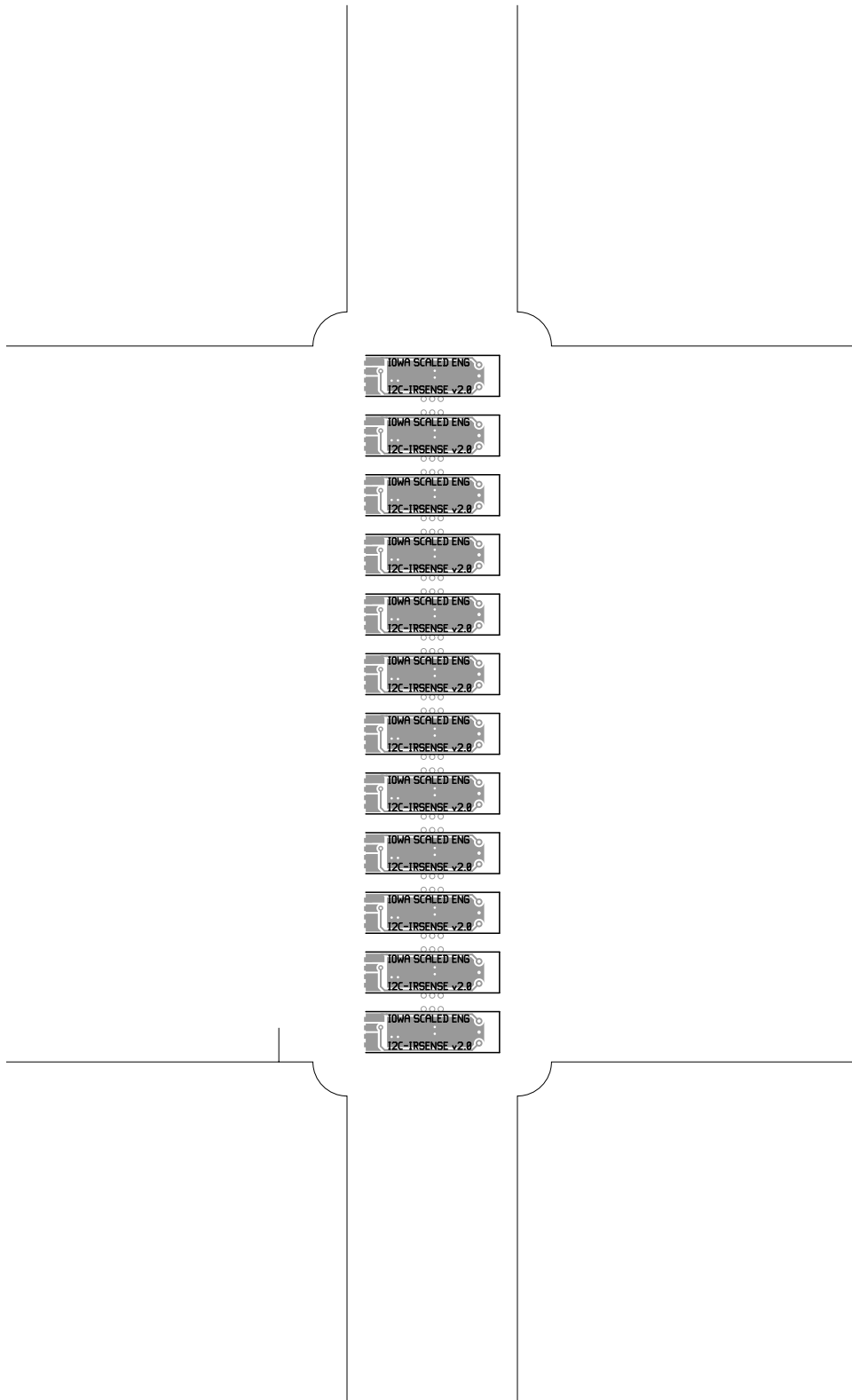












bottomassembly (mirrored), scale = 1:1.000  
i2c-irsense-panel.pcb

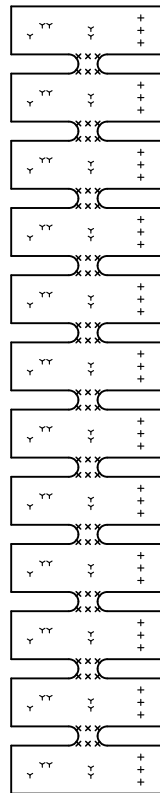
There are 3 different drill sizes used in this layout, 162 holes total

Symbol	Diam. (Inch)	Cpnt	Plated?
Y	0.014	60	YES
+	0.020	36	YES
x	0.031	66	NO

Title: (unknown) - Fabrication Drawing

Author: Michael Petersen

Date: Wed 03 Jan 2018 02:51:44 AM GMT UTC



Board outline is the centerline of this path