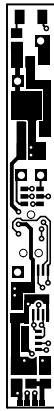


1. Table of Contents (This Page)

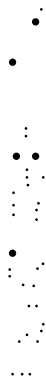
Created on Fri Jun 13 20:18:58 2014

- 2. top
- 3. bottom
- 4. outline
- 5. plated-drill
- 6. unplated-drill
- 7. componentmask
- 8. soldermask
- 9. topsilk
- 10. bottomsilk
- 11. toppaste
- 12. bottompaste
- 13. topassembly
- 14. bottomassembly
- 15. fab





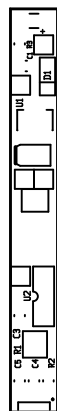


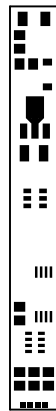


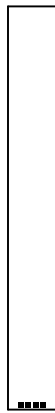
•
••

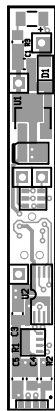














bottomassembly (mirrored), scale = 1:1.000
ckt-irsense.pcb

There are 3 different drill sizes used in this layout, 37 holes total

Symbol	Diam. (Inch)	Count	Plated?
γ	0.014	29	YES
+	0.038	5	YES
×	0.039	3	NO

Title: (unknown) - Fabrication Drawing
 Author: Michael Petersen
 Date: Sat 14 Jun 2014 02:18:58 AM GMT UTC

[illegible]

Board outline is the centerline of this path