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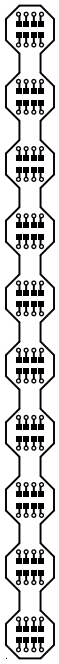
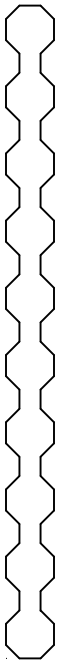
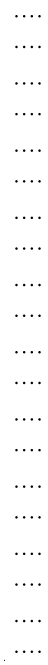


top, scale = 1:1.000
ckt-irsense-sns-panel.pcb

bottom (mirrored), scale = 1:1.000
ckt-irsense-sns-panel.pcb



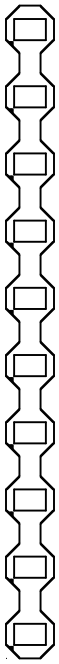
outline, scale = 1:1.000
ckt-irsense-sns-panel.pcb



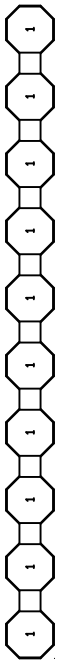
plated-drill, scale = 1:1.000
ckt-irsense-sns-panel.pcb

topmask, scale = 1:1.000
ckt-irsense-sns-panel.pcb

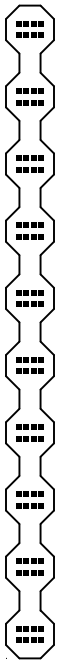
bottommask (mirrored), scale = 1:1.000
ckt-irsense-sns-panel.pcb



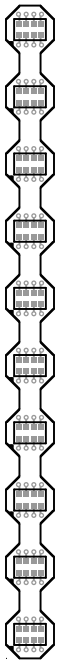
topsilk, scale = 1:1.000
ckt-irsense-sns-panel.pcb



bottomsilk (mirrored), scale = 1:1.000
ckt-irsense-sns-panel.pcb



toppaste, scale = 1:1.000
ckt-irsense-sns-panel.pcb



topassembly, scale = 1:1.000
ckt-irsense-sns-panel.pcb

bottomassembly (mirrored), scale = 1:1.000
ckt-irsense-sns-panel.pcb

fab, scale = 1:1.000
ckt-irsense-sns-panel.pcb

There are 1 different drill sizes used in this layout, 80 holes total

Symbol	Diam. (Inch)	Count	Plated?
+	0.013	80	YES

Title: (unknown) - Fabrication Drawing
Author: Michael Petersen
Date: Sun 22 Jun 2014 04:25:54 AM GMT UTC

Board outline is the centerline of this path