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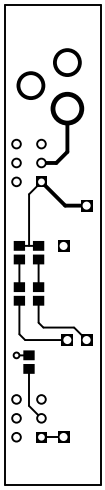
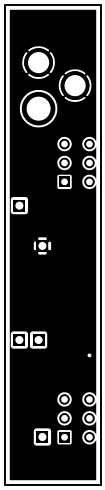
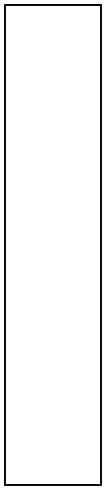


top, scale = 1:1.000
test-irsense.pcb

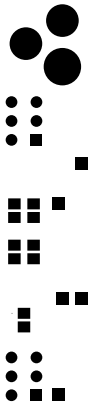


bottom (mirrored), scale = 1:1.000
test-irsense.pcb

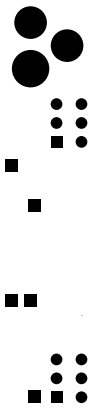


outline, scale = 1:1.000
test-irsense.pcb

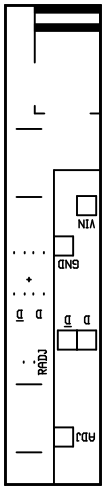
plated-drill, scale = 1:1.000
test-irsense.pcb



topmask, scale = 1:1.000
test-irsense.pcb

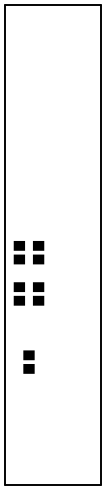


bottommask (mirrored), scale = 1:1.000
test-irsense.pcb

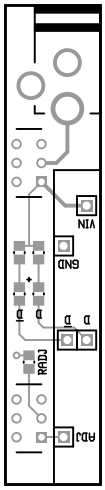


topsilk, scale = 1:1.000
test-irsense.pcb

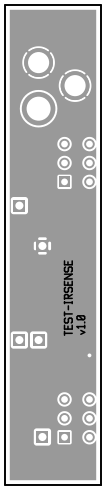
bottomsilk (mirrored), scale = 1:1.000
test-irsense.pcb



toppaste, scale = 1:1.000
test-irsense.pcb



topassembly, scale = 1:1.000
test-irsense.pcb



bottomassembly (mirrored), scale = 1:1.000
test-irsense.pcb

fab, scale = 1:1.000
test-irsense.pcb

There are 5 different drill sizes used in this layout, 21 holes total

Symbol	Diam. (Inch)	Count	Plated?
ᵞ	0.020	1	YES
+	0.035	12	YES
×	0.042	5	YES
°	0.110	2	YES
▫	0.125	1	YES

Title: (unknown) - Fabrication Drawing
Author: Michael Petersen
Date: Sat 30 Jan 2016 03:31:43 AM GMT UTC

Board outline is the centerline of this path