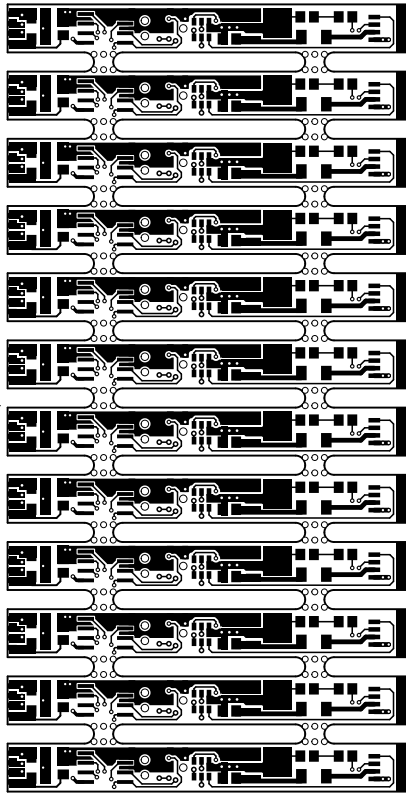


1. Table of Contents (This Page)

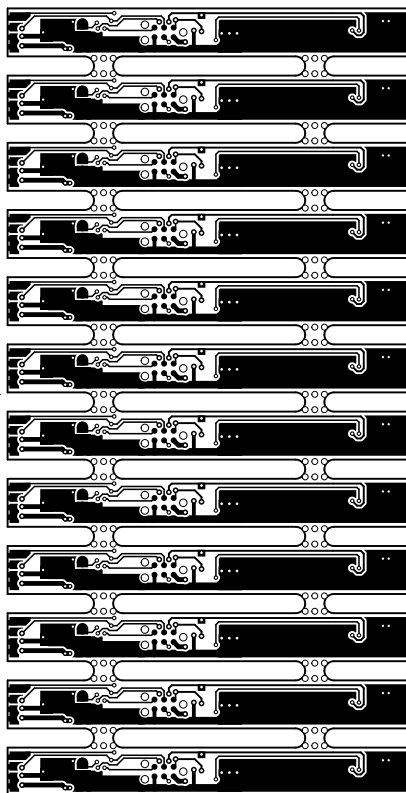
Created on Wed Aug 19 20:40:23 2020

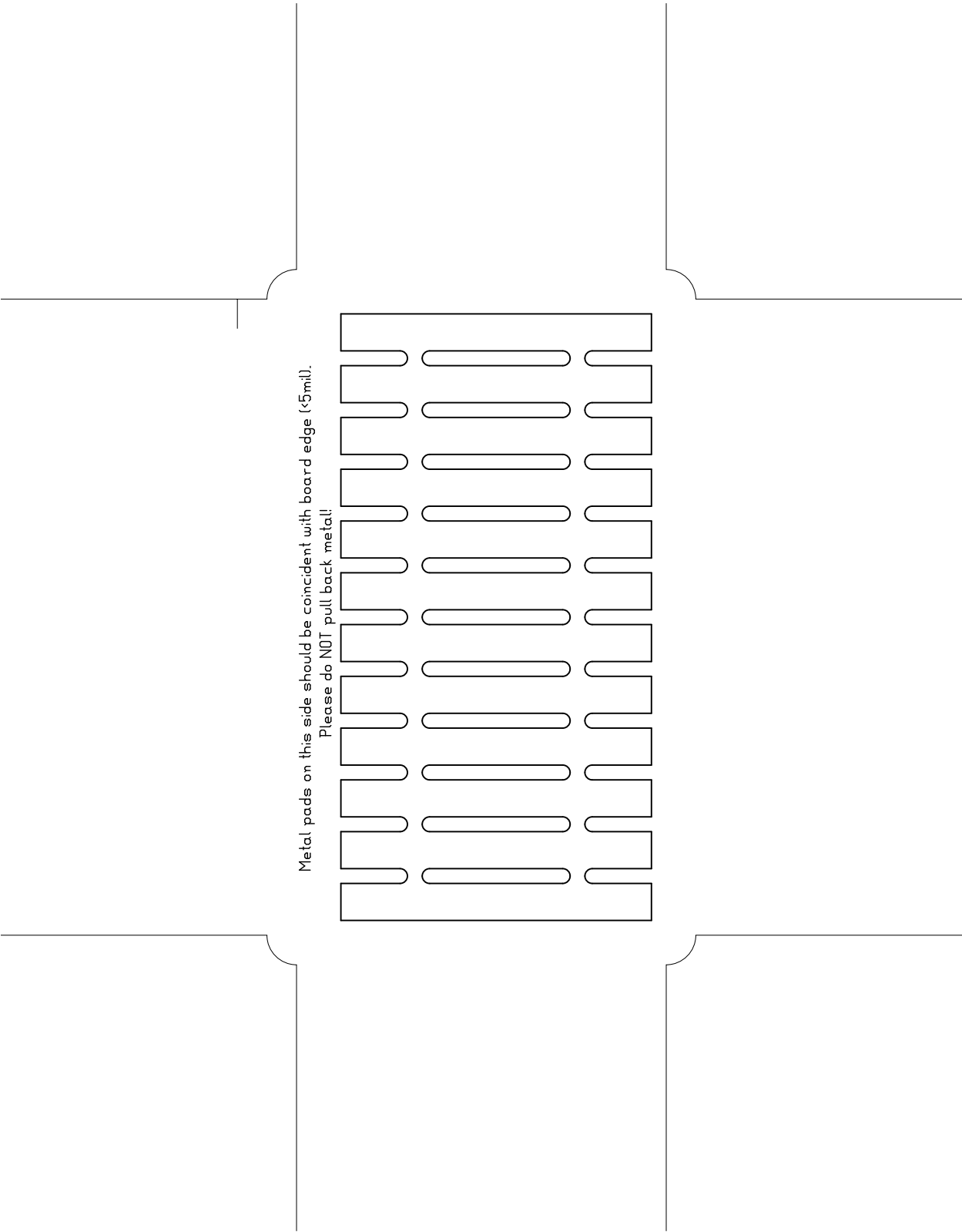
- 2. top
- 3. bottom
- 4. outline
- 5. plated-drill
- 6. unplated-drill
- 7. componentmask
- 8. soldermask
- 9. topsilk
- 10. bottomsilk
- 11. toppaste
- 12. bottompaste
- 13. topassembly
- 14. bottomassembly
- 15. fab

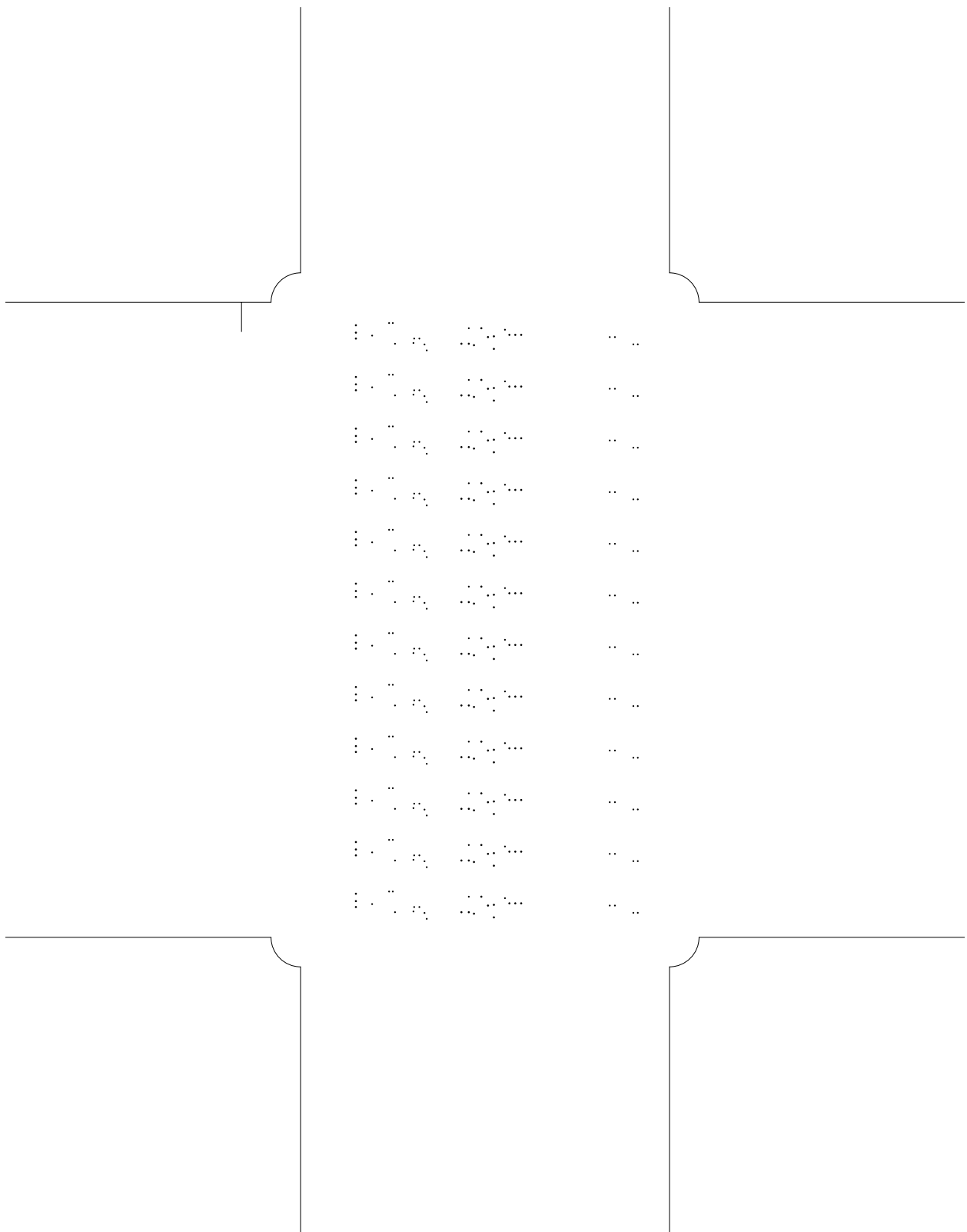
Metal pads on this side should be coincident with board edge (<5mil).
Please do NOT pull back metal!

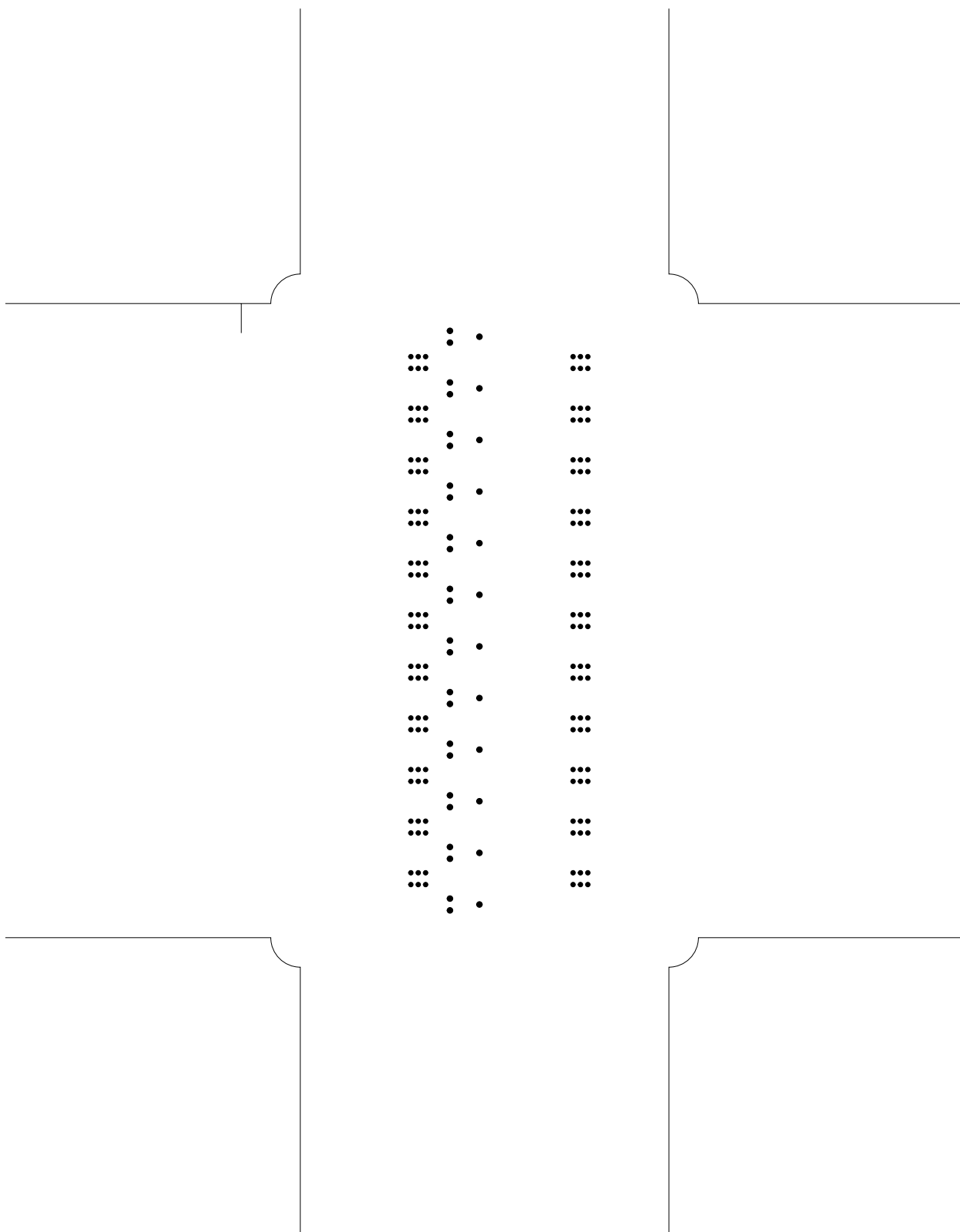


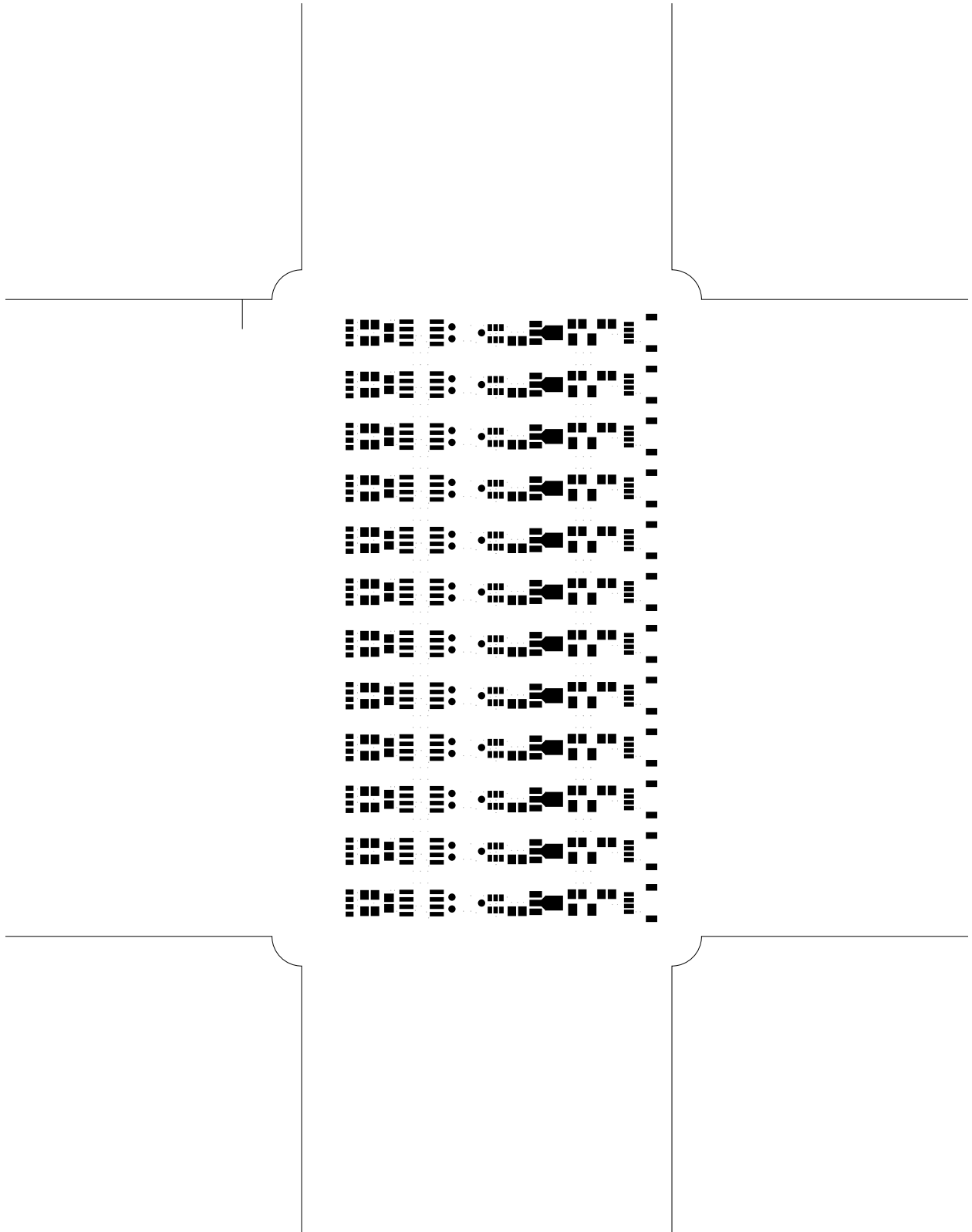
Metaj boaga on this side should be coincident with port q edge (2mil).
Please do NOT cut port metaj!

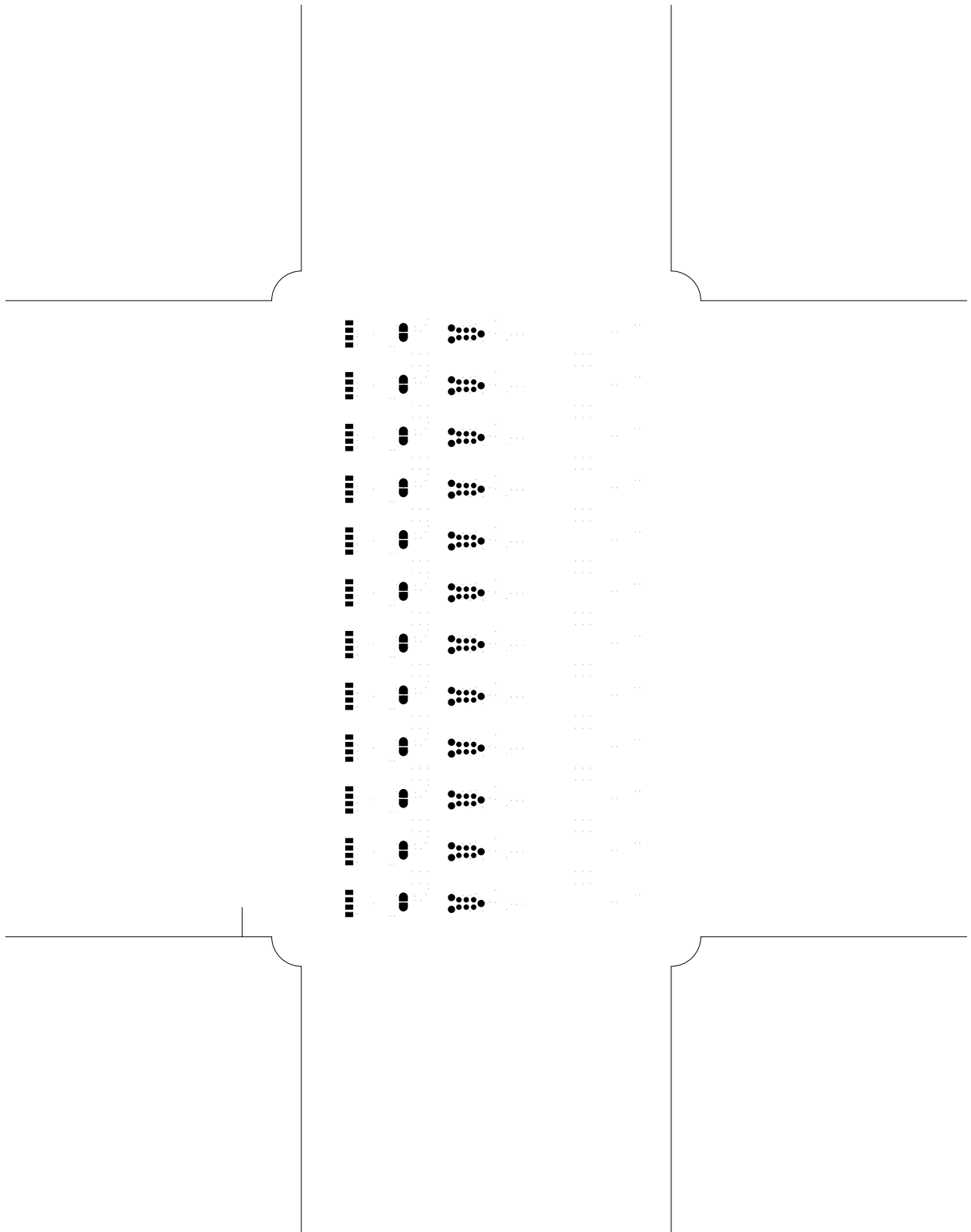




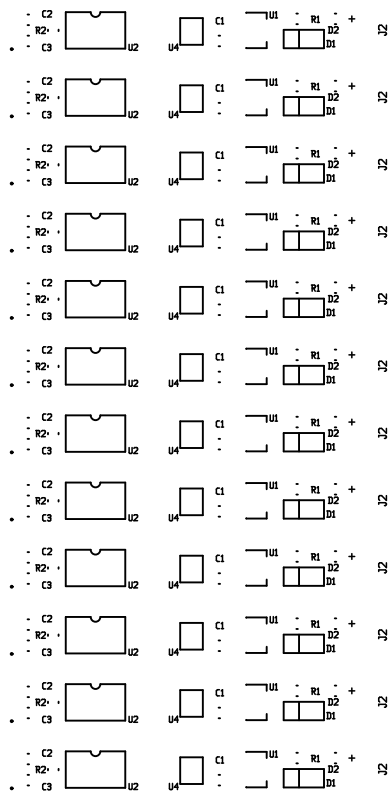


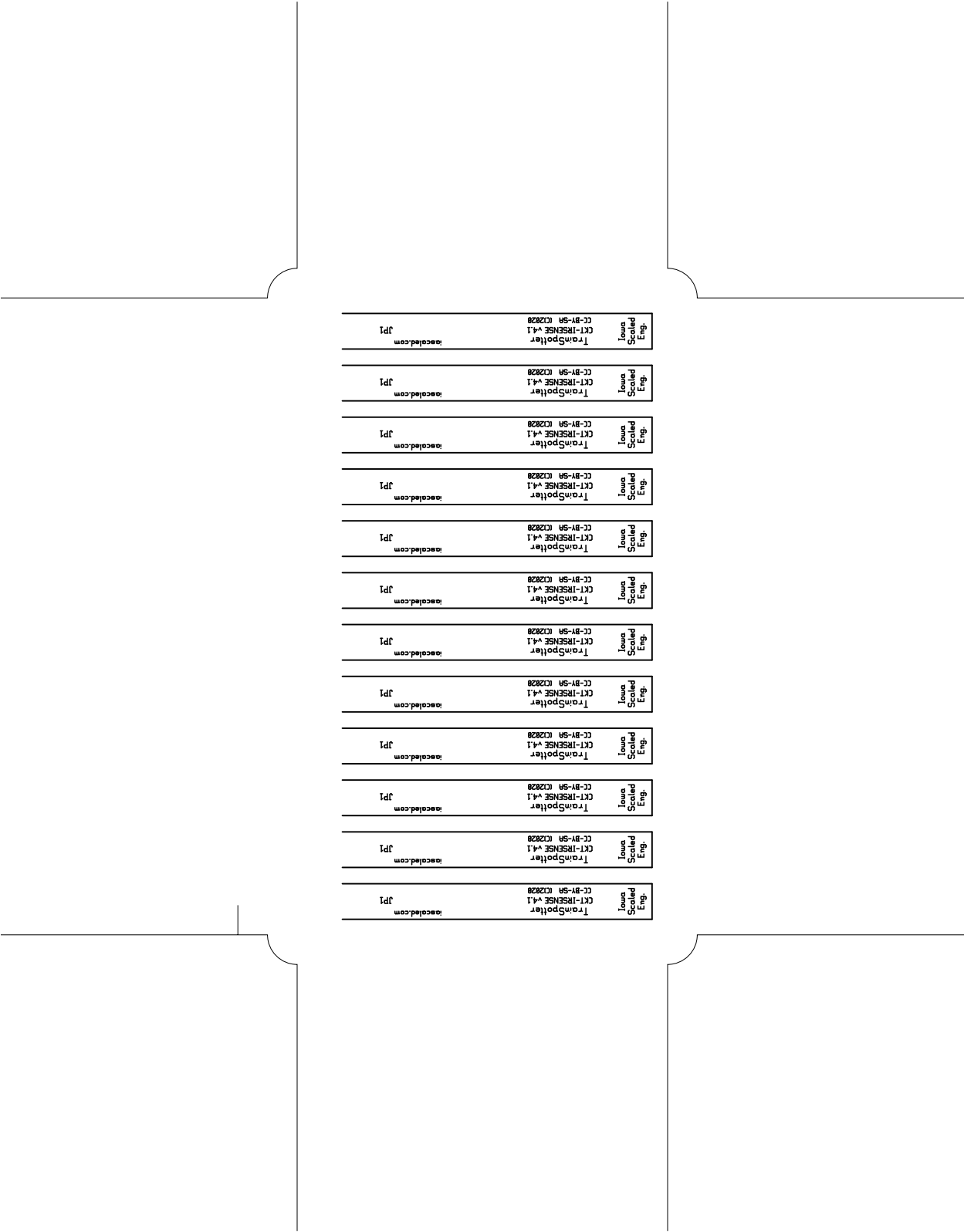




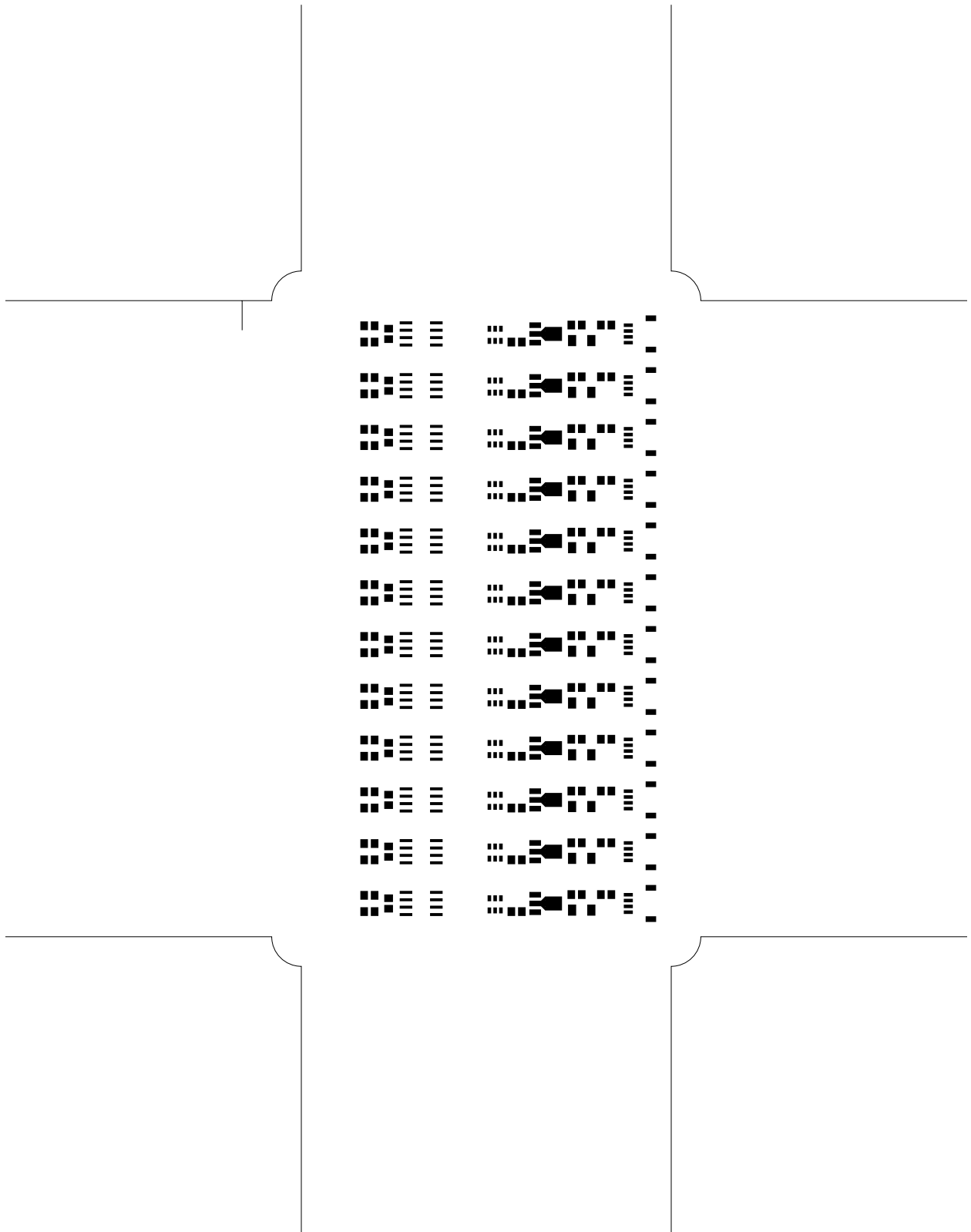


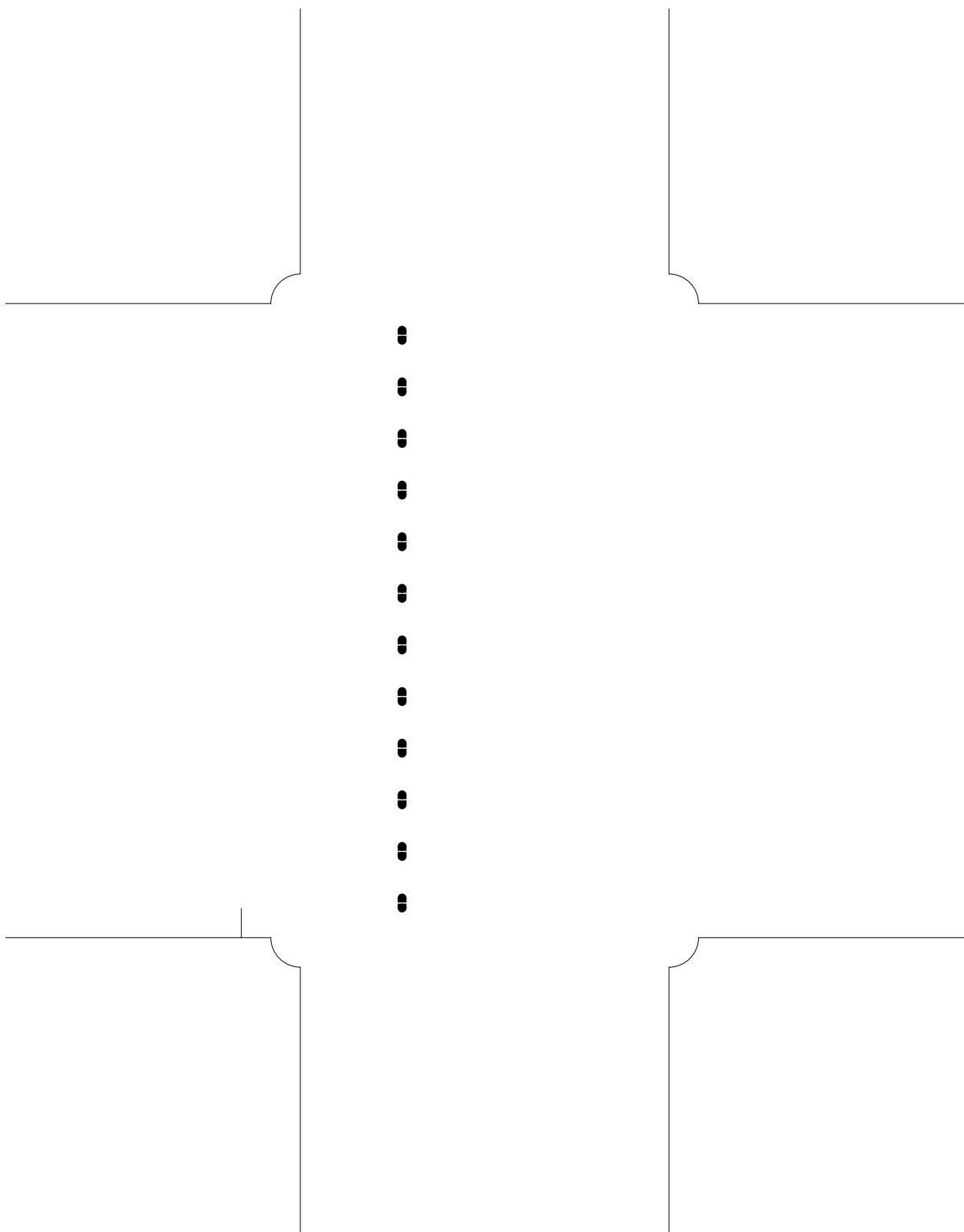
bottommask (mirrored), scale = 1:1.000
ckt-irsense-panel.pcb

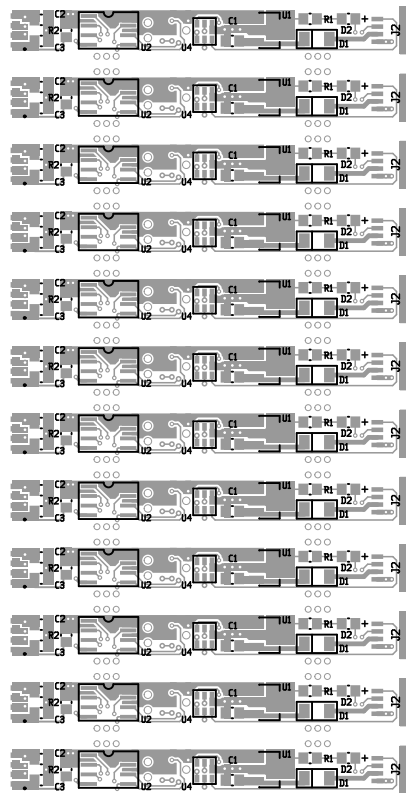


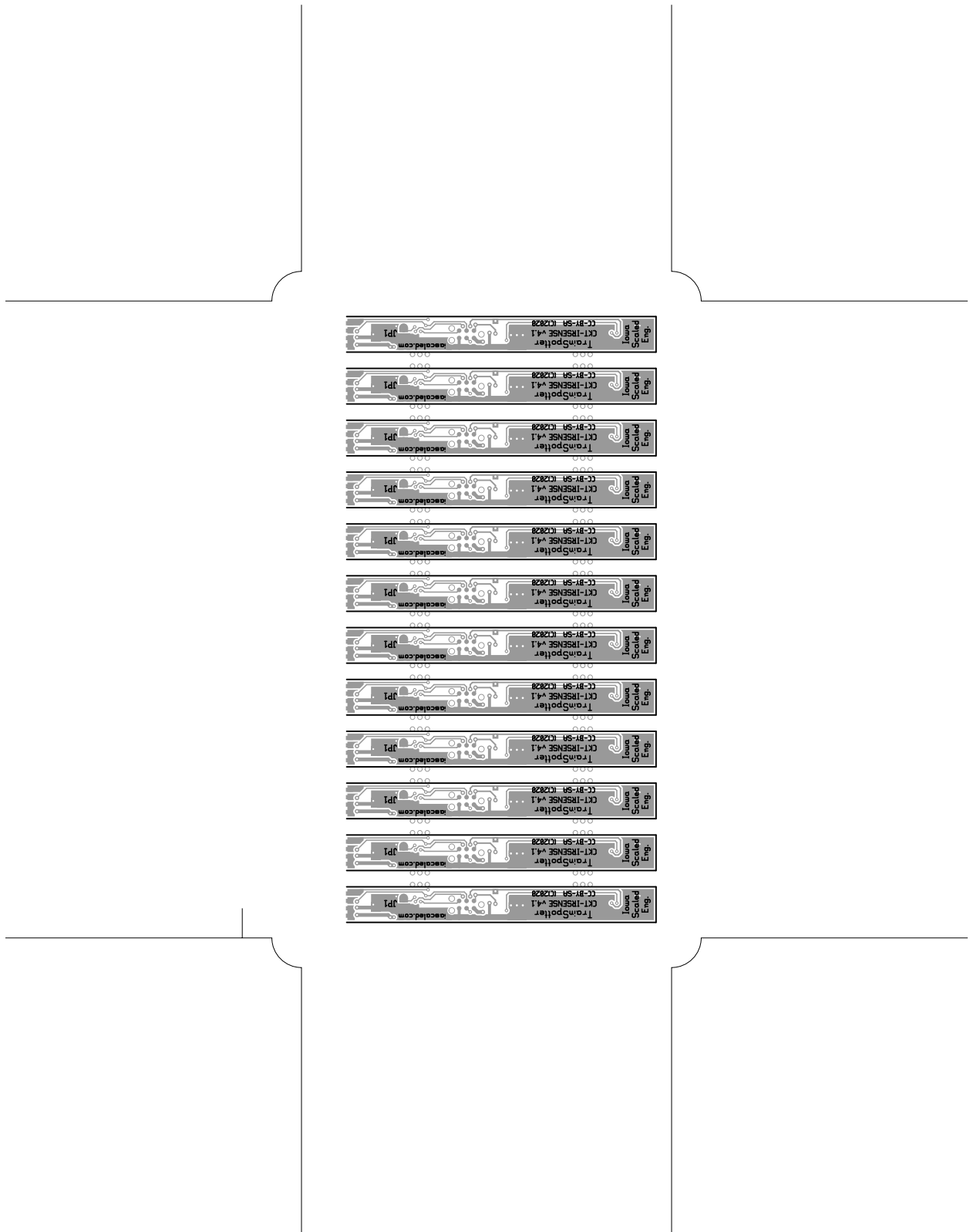


bottomsilk (mirrored), scale = 1:1.000
ckt-irsense-panel.pcb









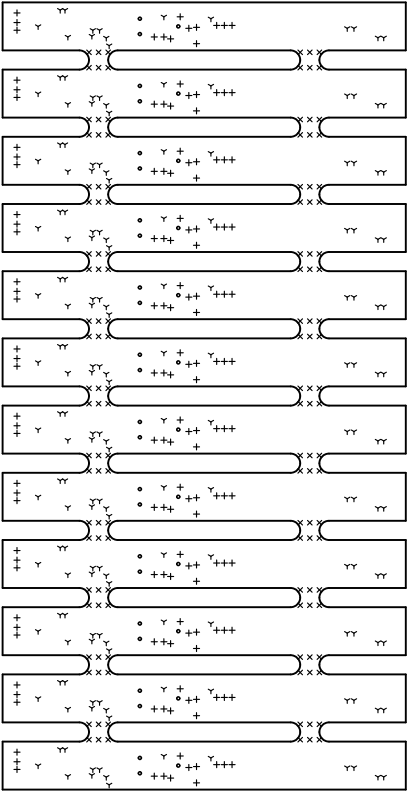
bottomassembly (mirrored), scale = 1:1.000
ckt-irsense-panel.pcb

There are 4 different drill sizes used in this layout, 504 holes total

Symbol	Diam. (Inch)	Count	Plated?
Y	0.012	180	YES
+	0.014	156	YES
x	0.031	132	NO
•	0.039	36	NO

Title: (unknown) - Fabrication Drawing
Author: Michael Petersen
Date: Thu 20 Aug 2020 02:40:23 AM GMT UTC

Metal pads on this side should be coincident with board edge (<5mil).
Please do NOT pull back metal!



Board outline is the centerline of this path