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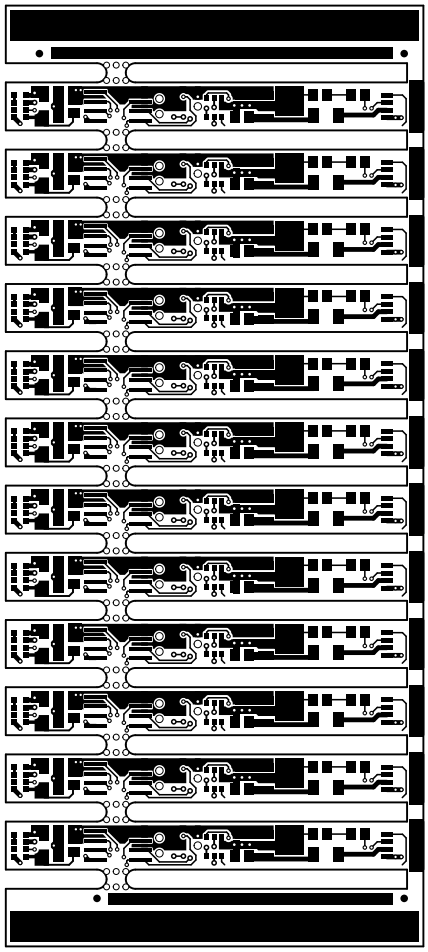
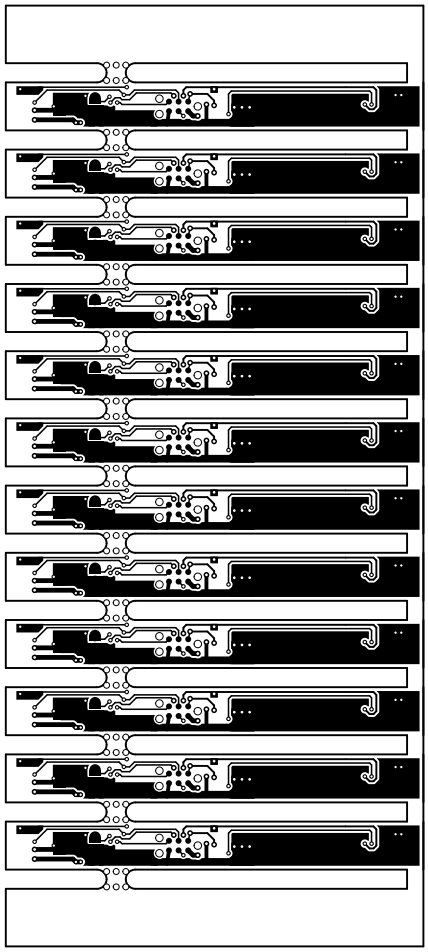


Tabs should be solid on this edge. No holes.

top, scale = 1:1.000
ckt-irsense-ra-array.pcb



bottom (mirrored), scale = 1:1.000
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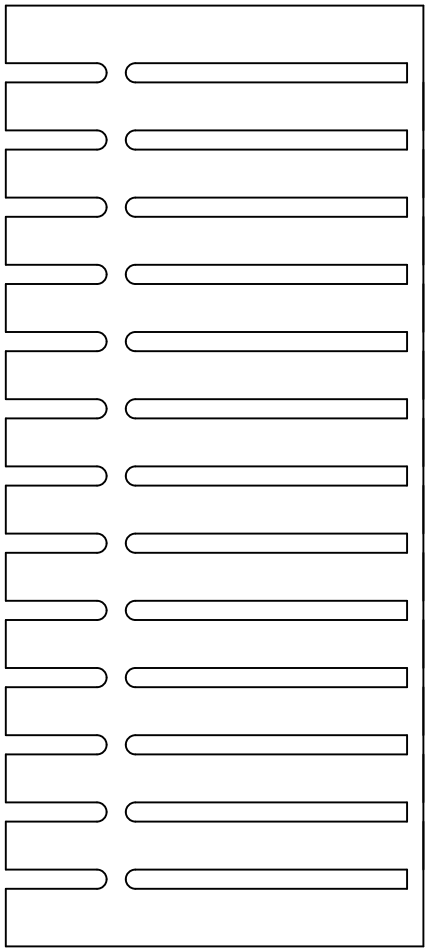

outline, scale = 1:1.000
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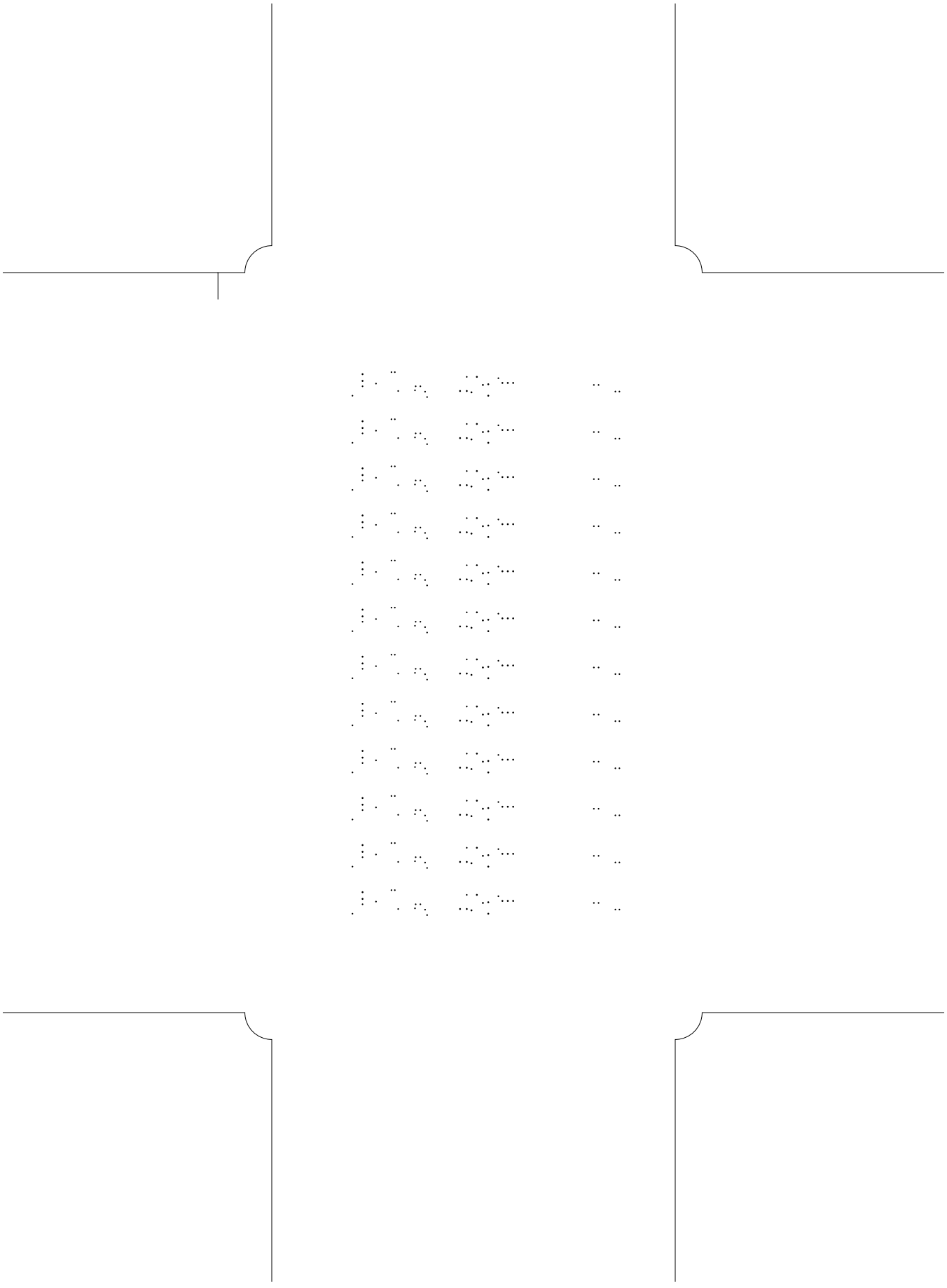

plated-drill, scale = 1:1.000
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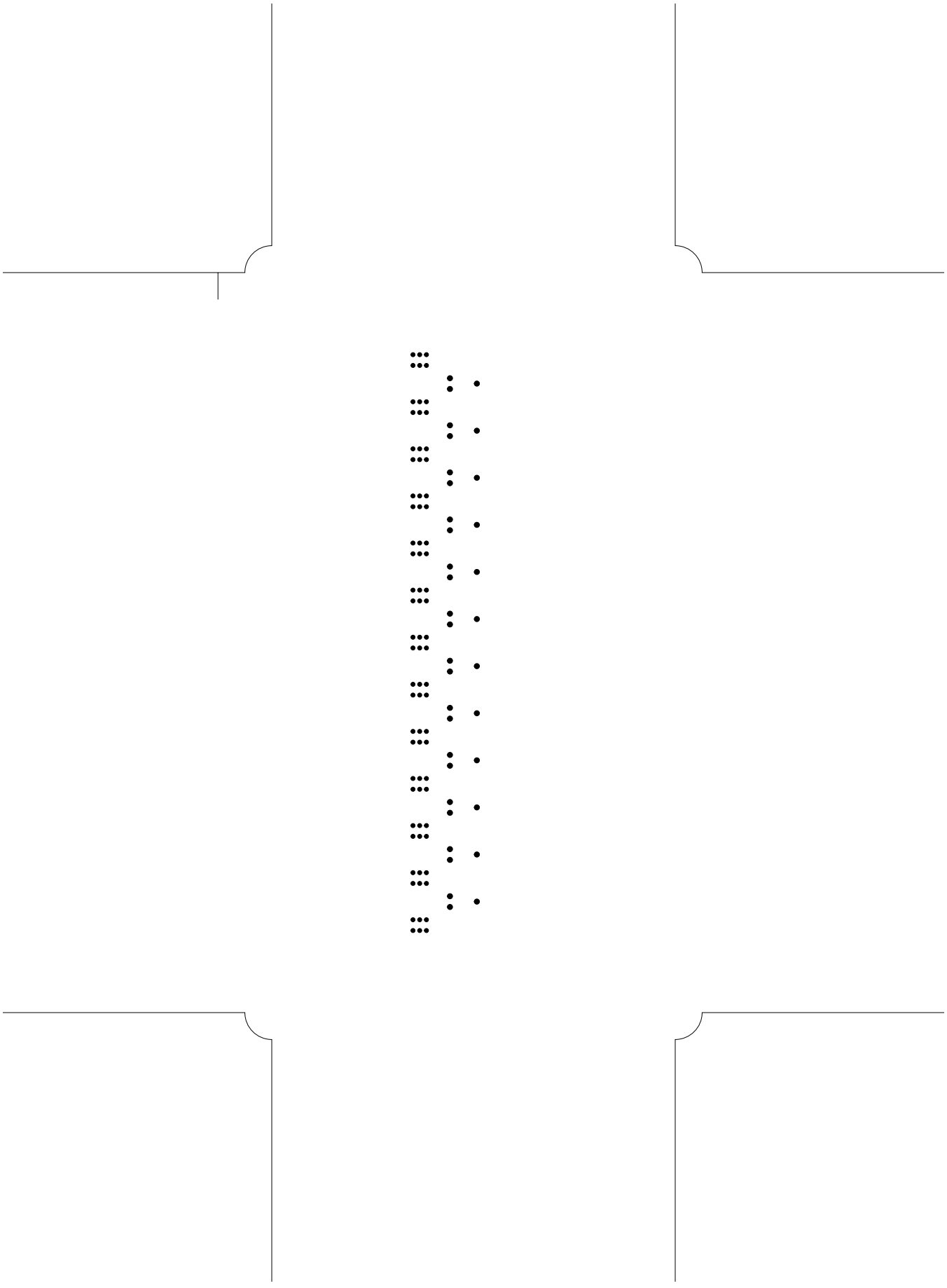

unplated-drill, scale = 1:1.000
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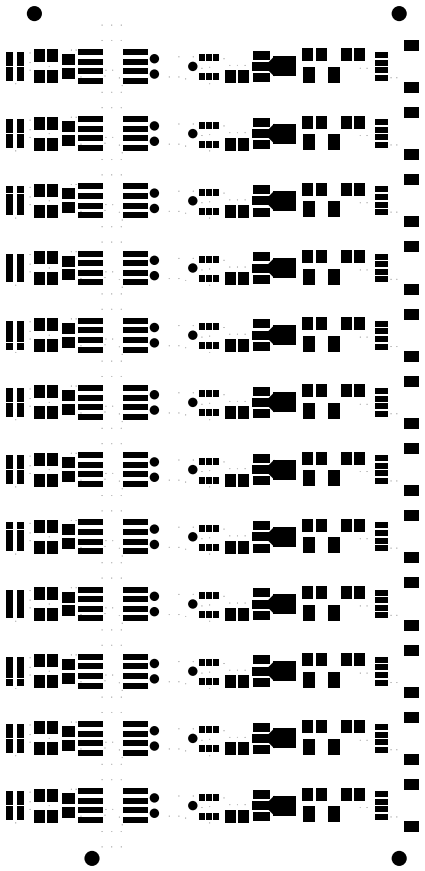
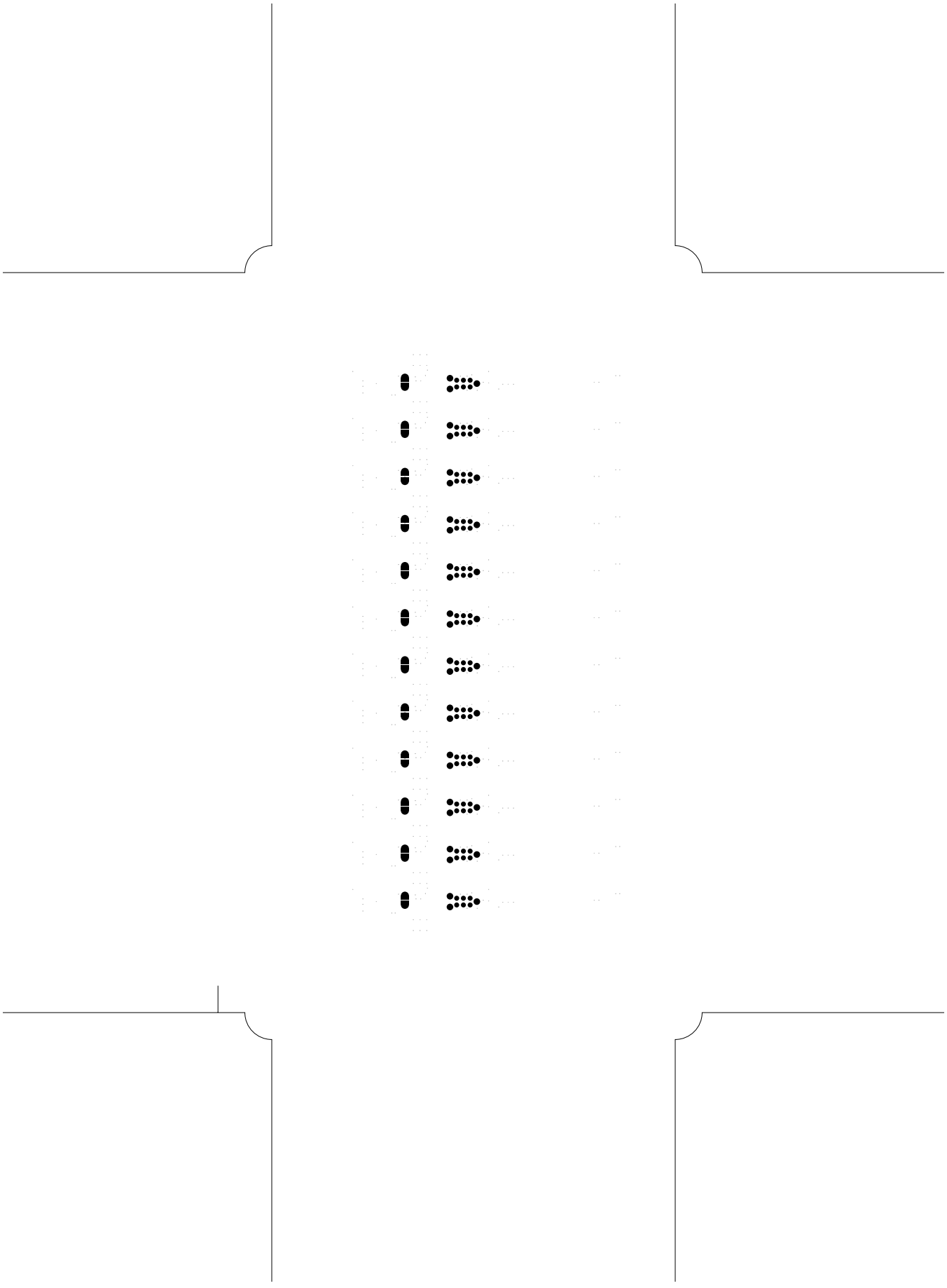


topmask, scale = 1:1.000
ckt-irsense-ra-array.pcb



bottommask (mirrored), scale = 1:1.000
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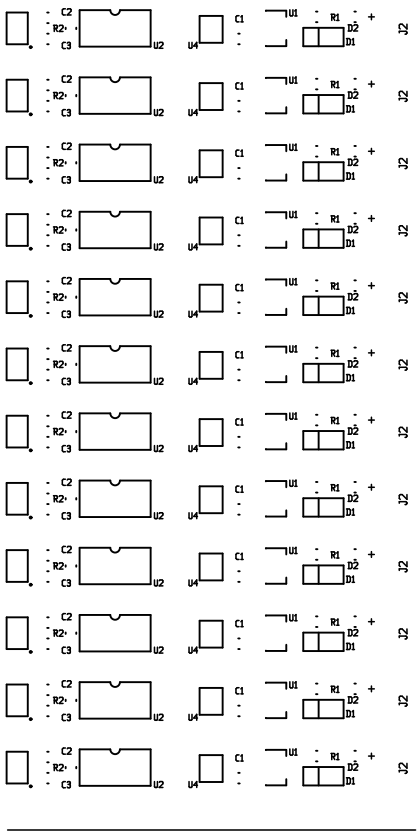
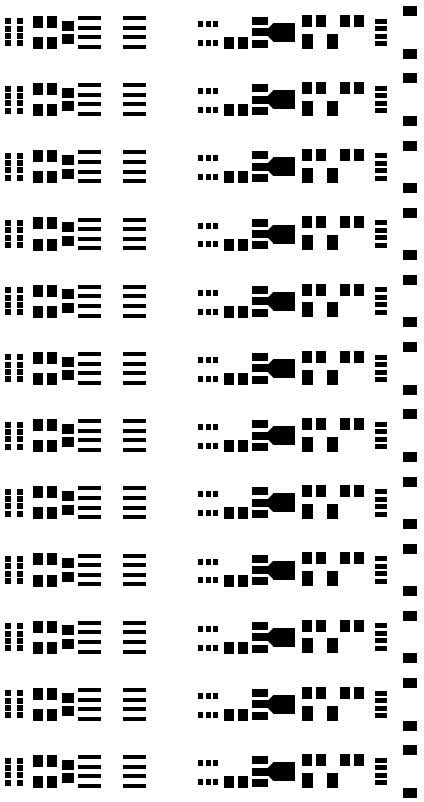
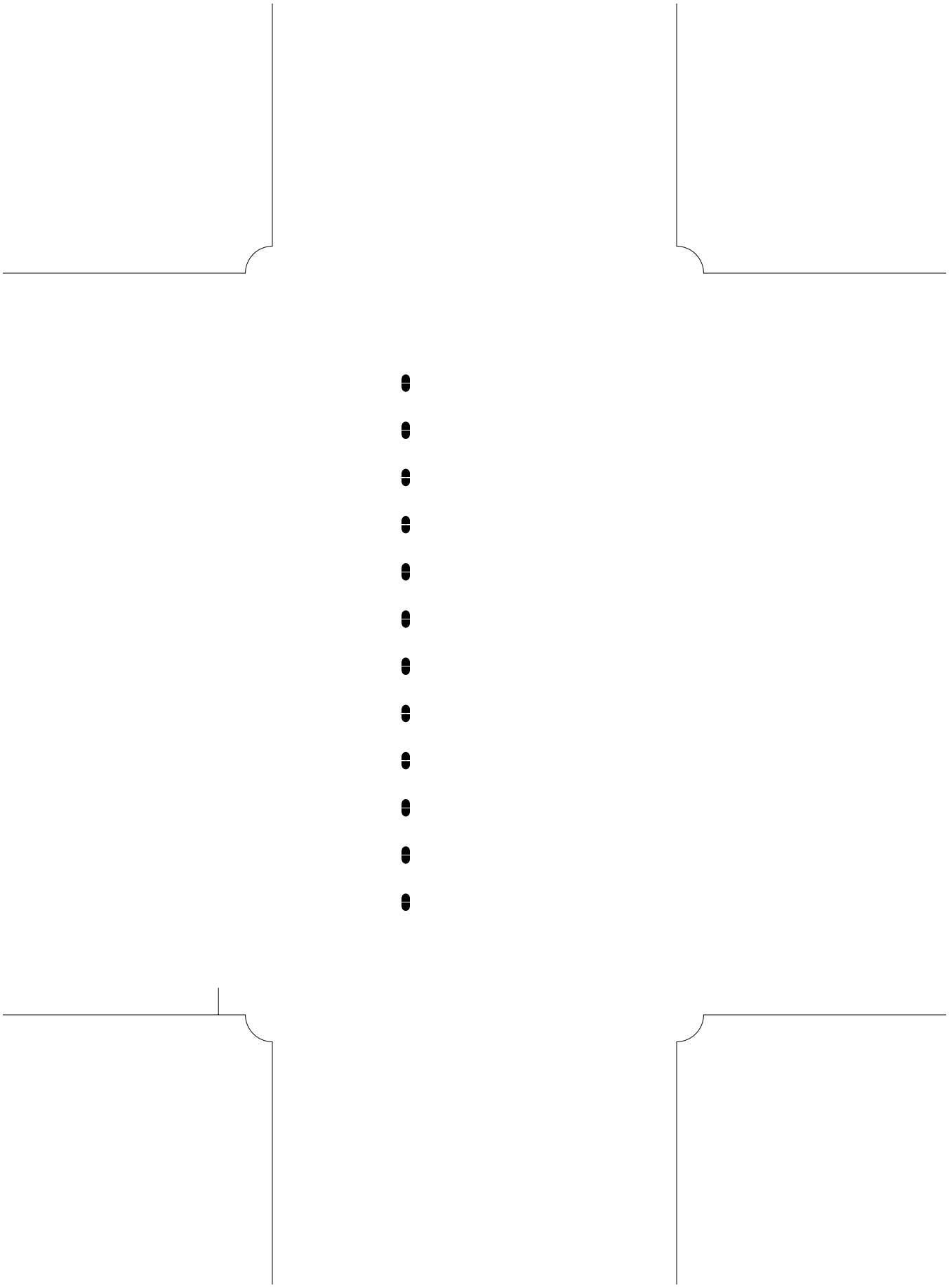


topsilk, scale = 1:1.000
ckt-irsense-ra-array.pcb

bottomsilk (mirrored), scale = 1:1.000
ckt-irsense-ra-array.pcb



toppaste, scale = 1:1.000
ckt-irsense-ra-array.pcb



bottompaste (mirrored), scale = 1:1.000
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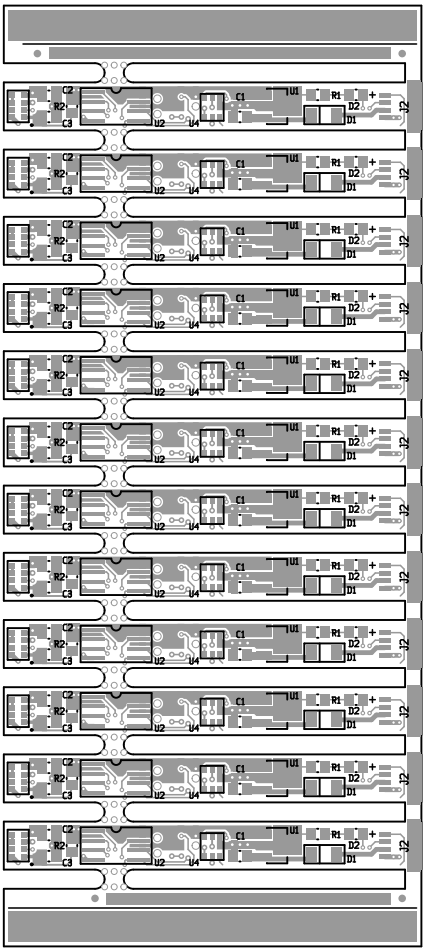


Tabs should be solid on this edge. No holes.

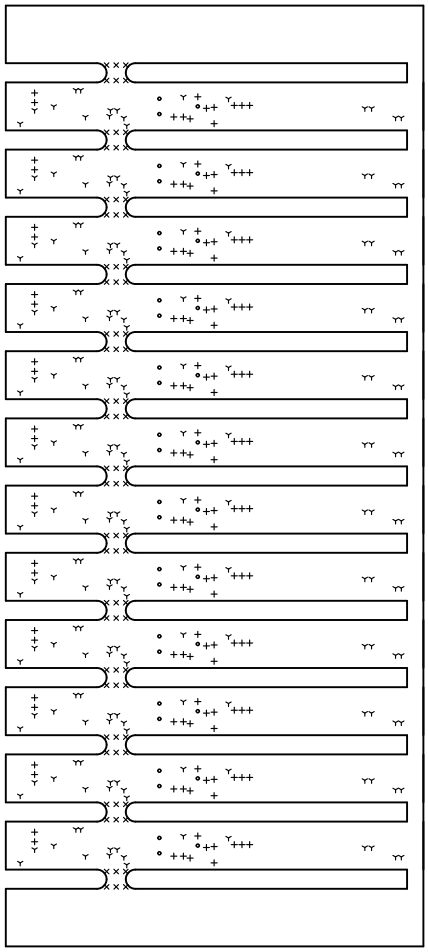
topassembly, scale = 1:1.000
ckt-irsense-ra-array.pcb

bottomassembly (mirrored), scale = 1:1.000
ckt-irsense-ra-array.pcb

There are 4 different drill sizes used in this layout, 462 holes total

Symbol	Diam. (Inch)	Count	Plated?
˅	0.012	204	YES
+	0.014	144	YES
×	0.031	78	NO
°	0.039	36	NO

Title: (unknown) – Fabrication Drawing
Author: Nathan D. Holmes
Date: Tue Dec 31 20:20:16 2024 UTC



Tabs should be solid on this edge. No holes.

Board outline is the centerline of this path

fab, scale = 1:1.000
ckt-irsense-ra-array.pcb