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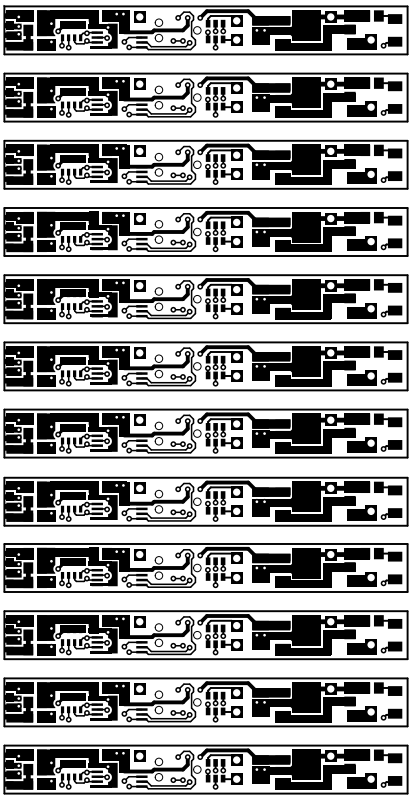
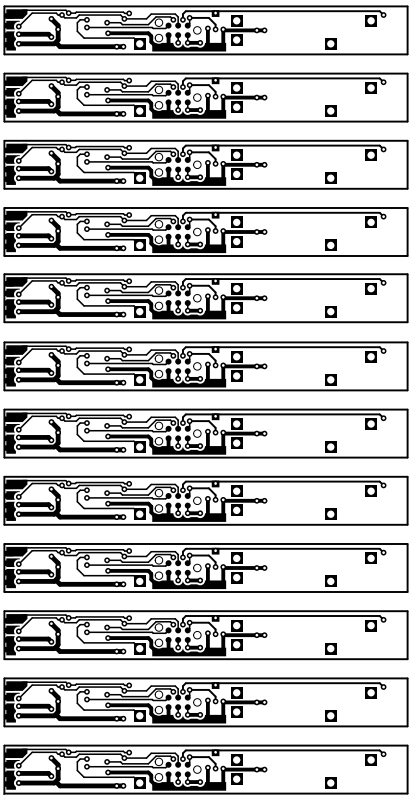


top, scale = 1:1.000
ckt-irsense-panel.pcb



bottom (mirrored), scale = 1:1.000
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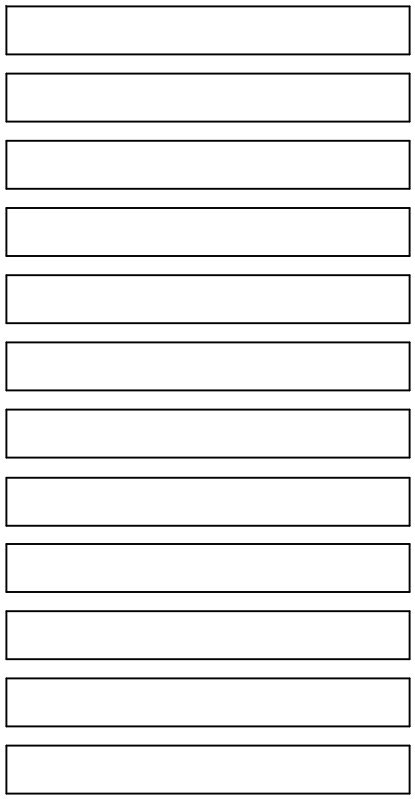

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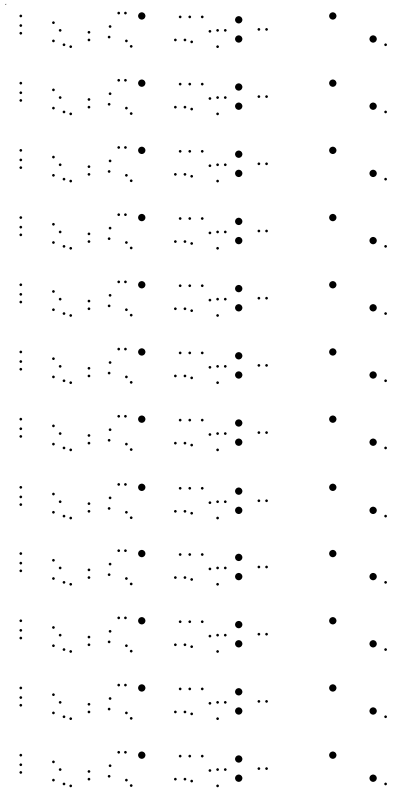
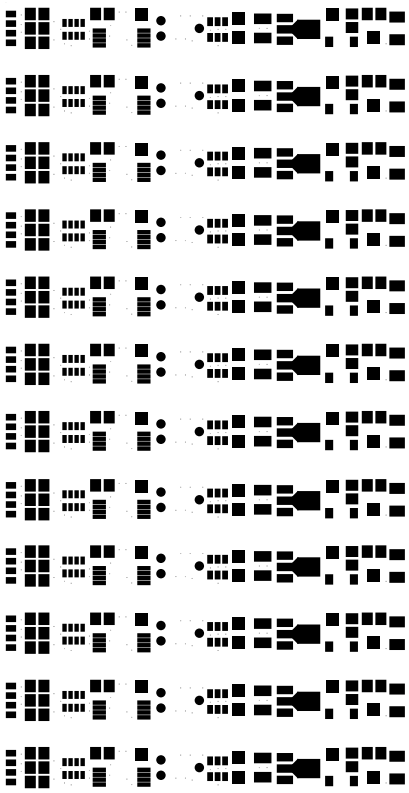
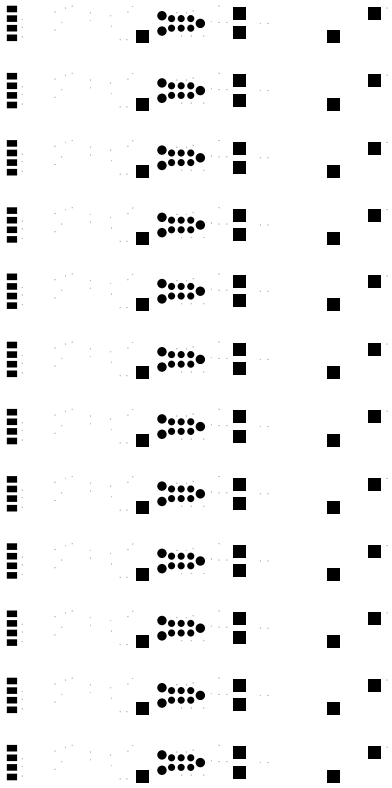


plated-drill, scale = 1:1.000
ckt-irsense-panel.pcb

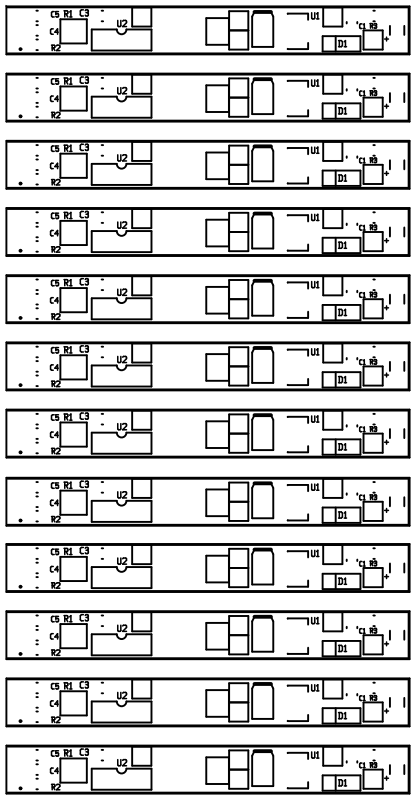
unplated-drill, scale = 1:1.000
ckt-irsense-panel.pcb



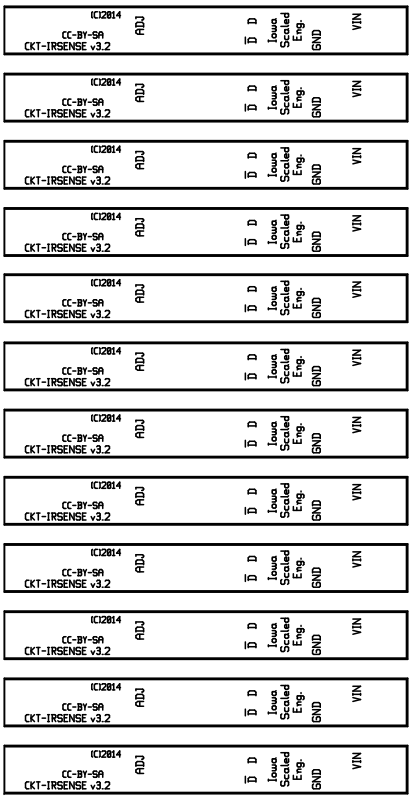
topmask, scale = 1:1.000
ckt-irsense-panel.pcb



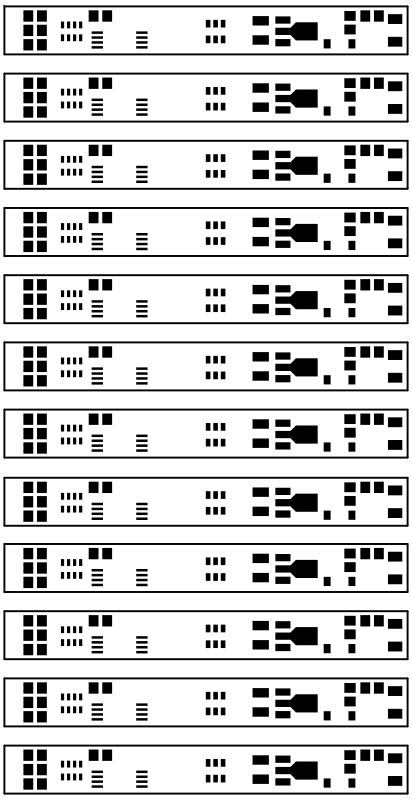
bottommask (mirrored), scale = 1:1.000
ckt-irsense-panel.pcb



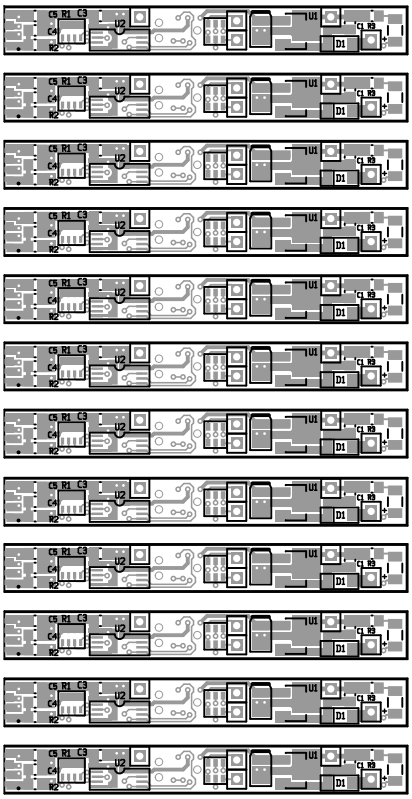
topsilk, scale = 1:1.000
ckt-irsense-panel.pcb



bottomsilk (mirrored), scale = 1:1.000
ckt-irsense-panel.pcb



toppaste, scale = 1:1.000
ckt-irsense-panel.pcb



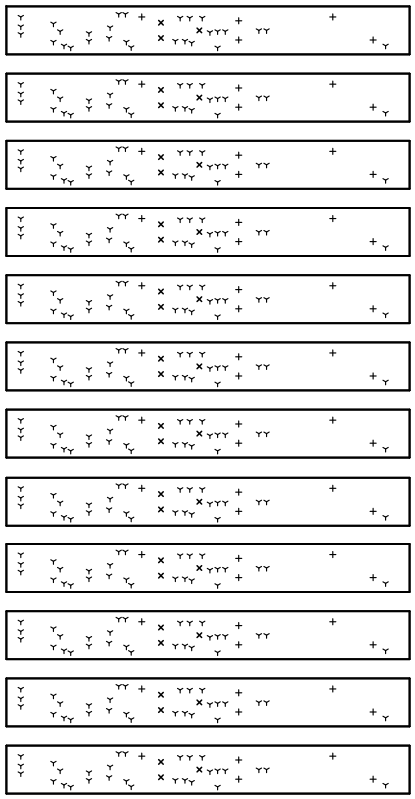
topassembly, scale = 1:1.000
ckt-irsense-panel.pcb

bottomassembly (mirrored), scale = 1:1.000
ckt-irsense-panel.pcb

There are 3 different drill sizes used in this layout, 444 holes total

Symbol	Diam. (Inch)	Count	Plated?
▿	0.014	348	YES
+	0.038	60	YES
×	0.039	36	NO

Title: (unknown) - Fabrication Drawing
Author: Michael Petersen
Date: Mon 02 Feb 2015 04:29:52 AM GMT UTC



Board outline is the centerline of this path

fab, scale = 1:1.000
ckt-irsense-panel.pcb