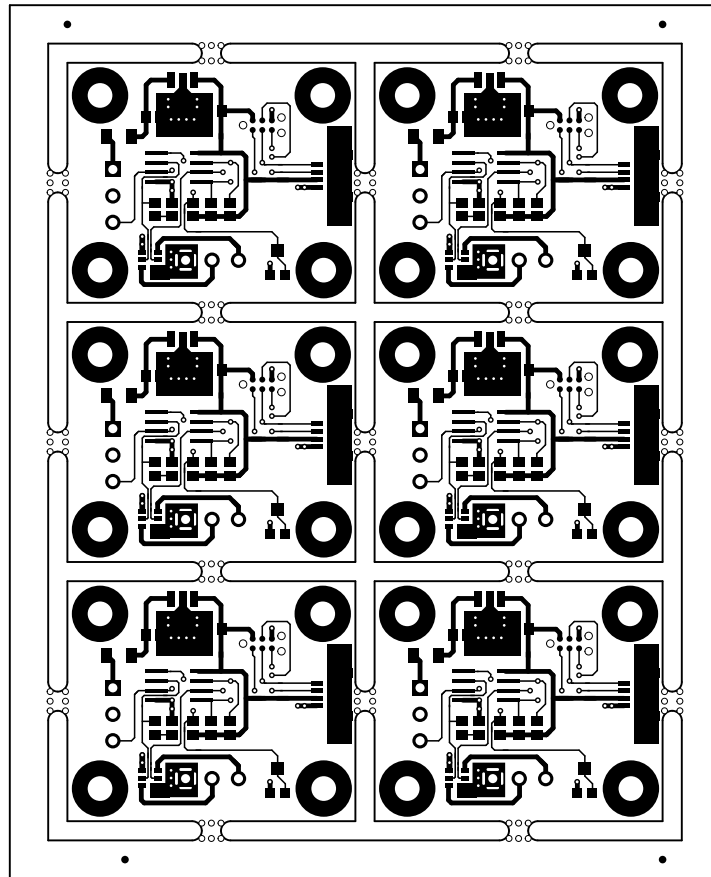
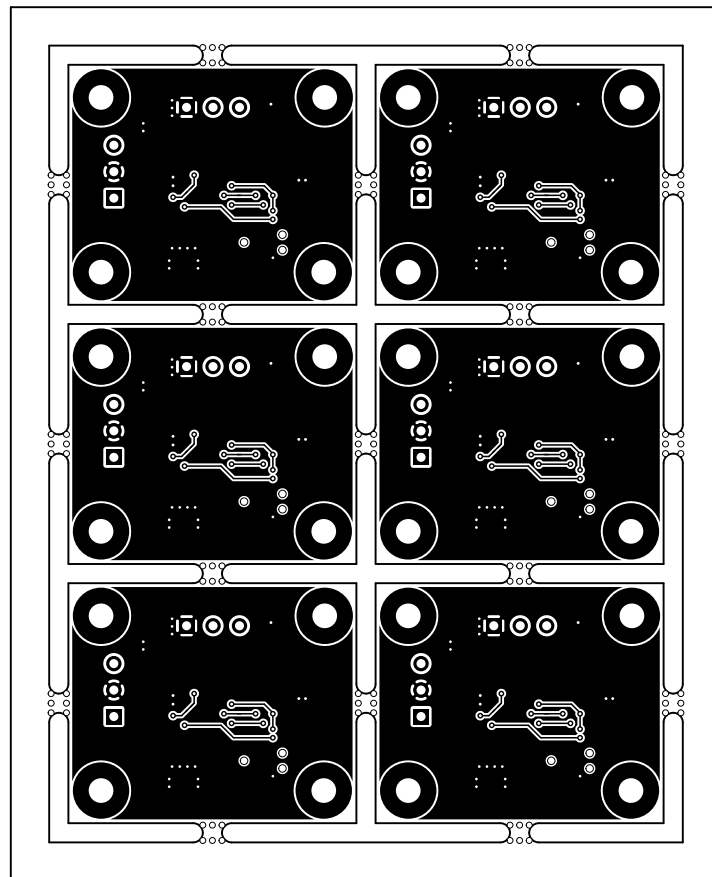


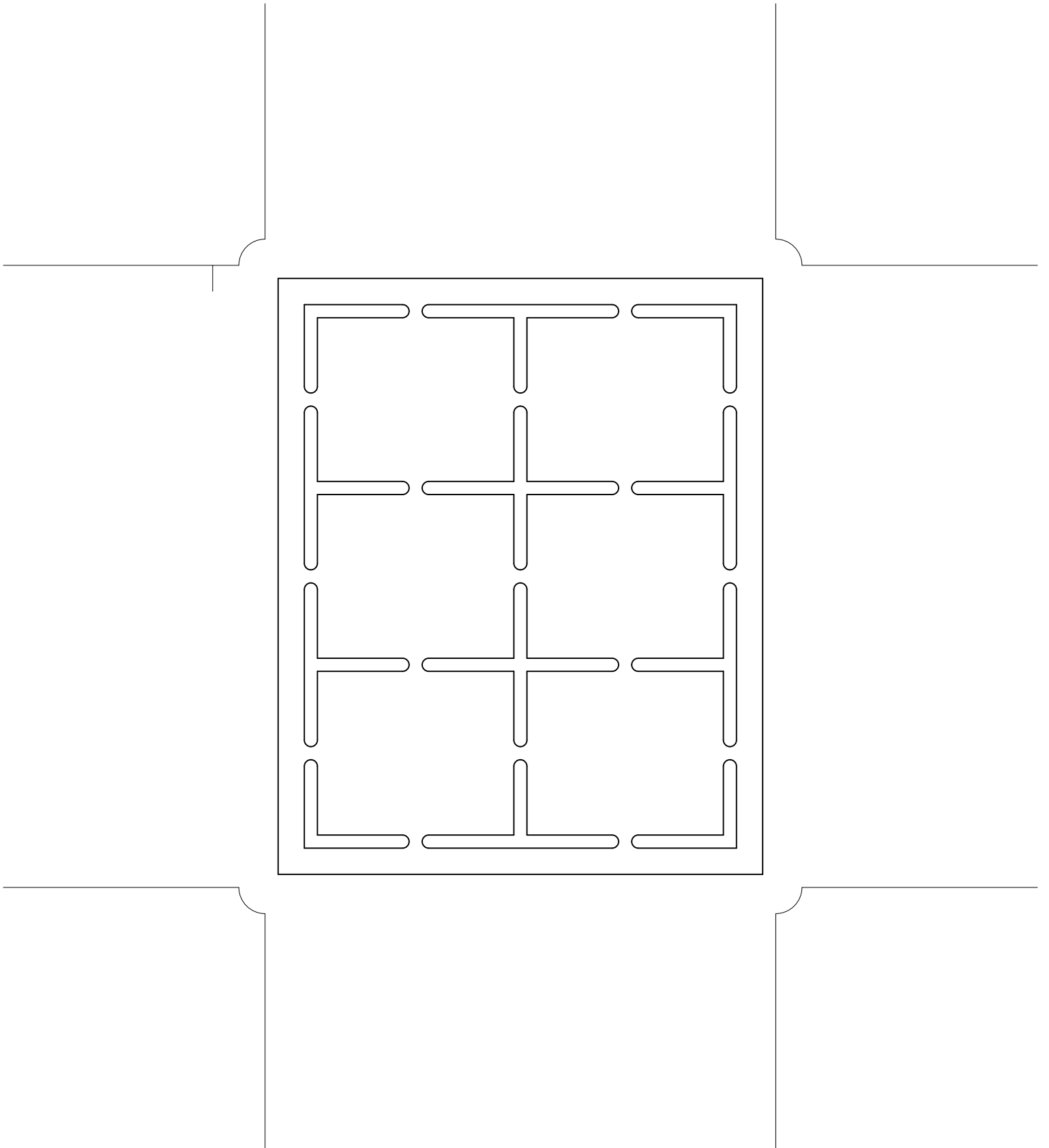
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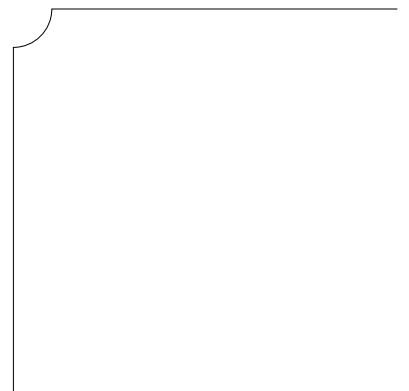
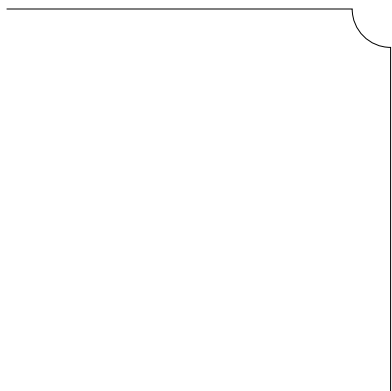
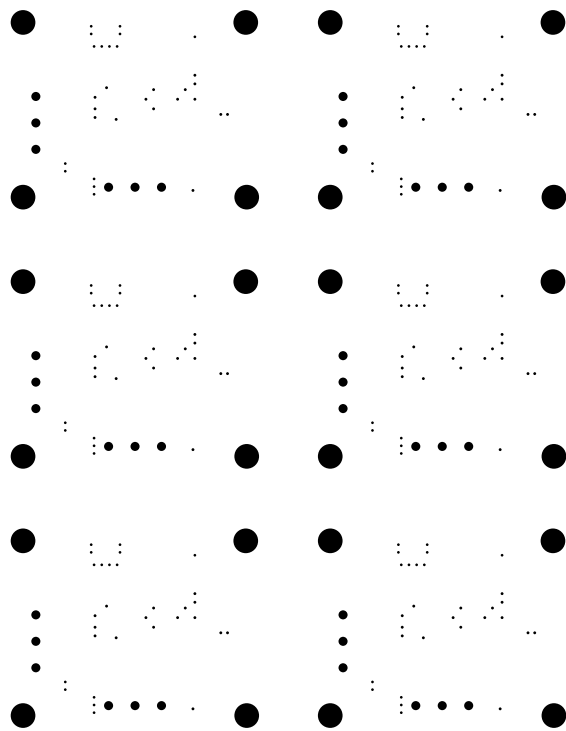
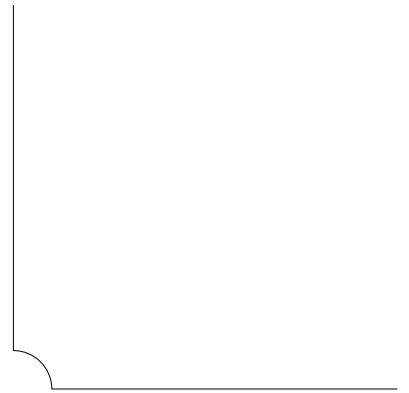
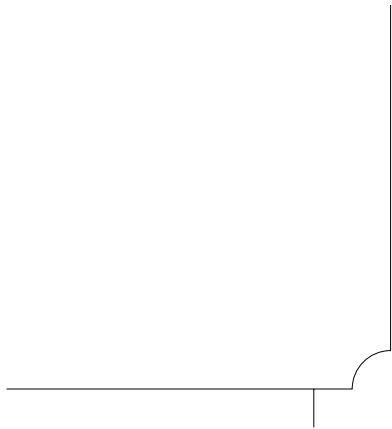
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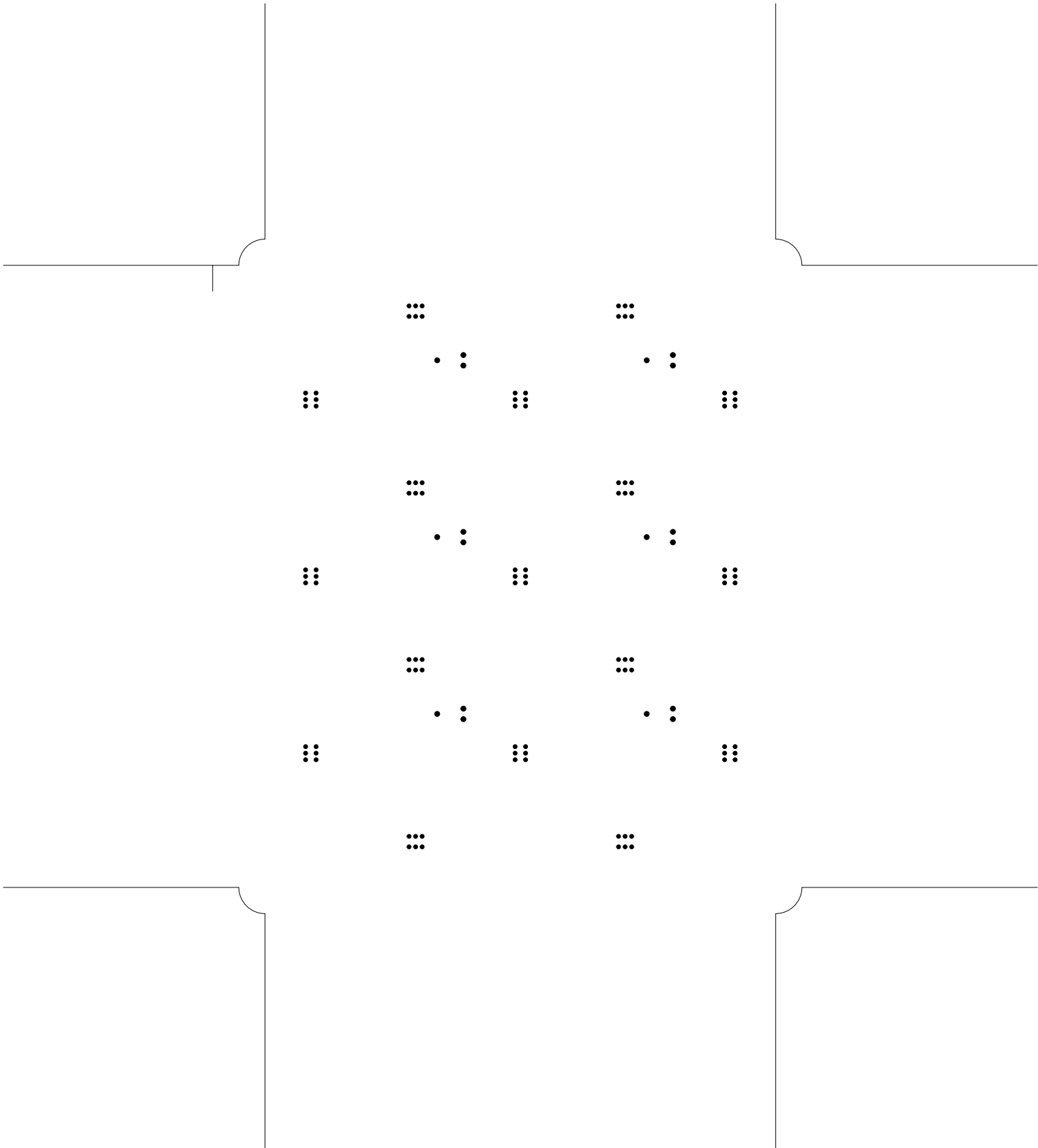
- 2. top
- 3. bottom
- 4. outline
- 5. plated-drill
- 6. unplated-drill
- 7. componentmask
- 8. soldermask
- 9. topsilk
- 10. bottomsilk
- 11. toppaste
- 12. topassembly
- 13. bottomassembly
- 14. fab

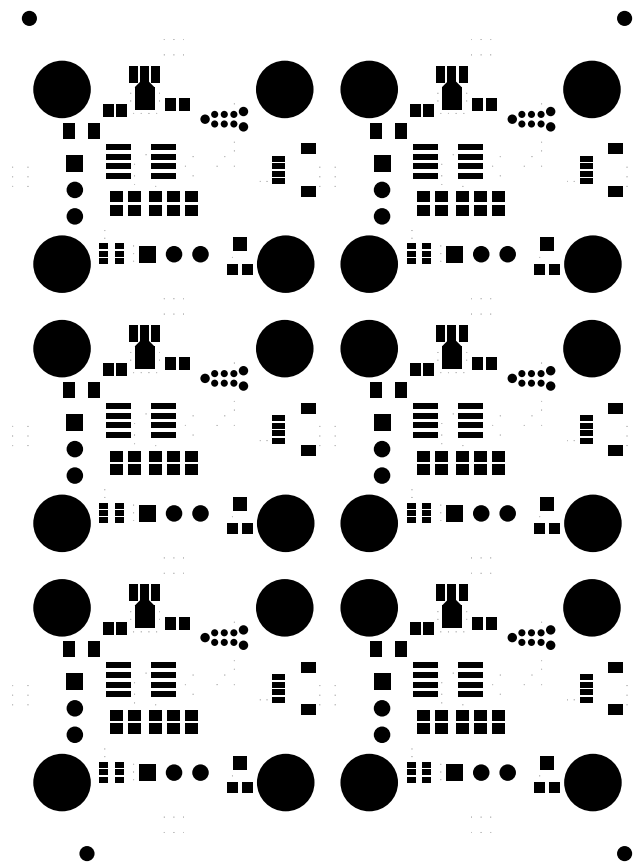


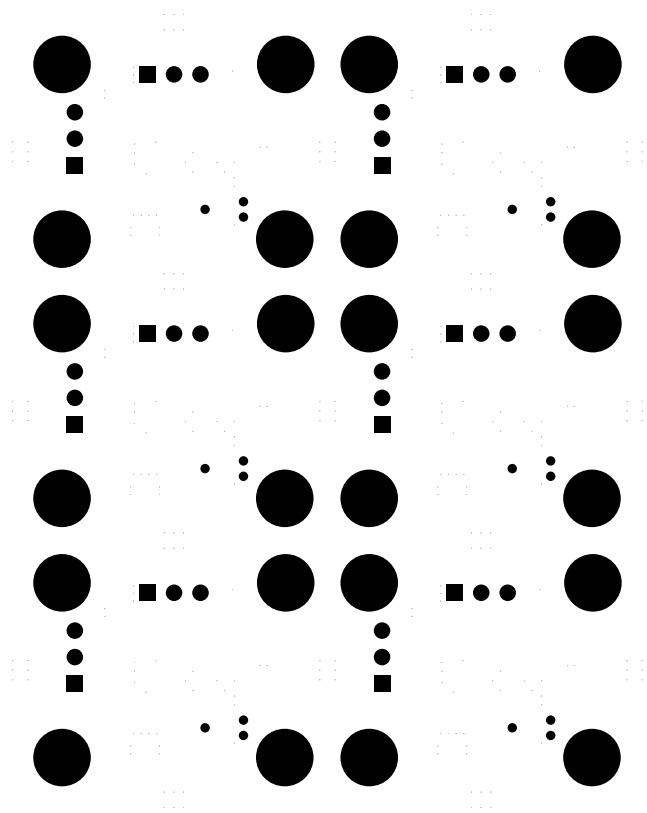




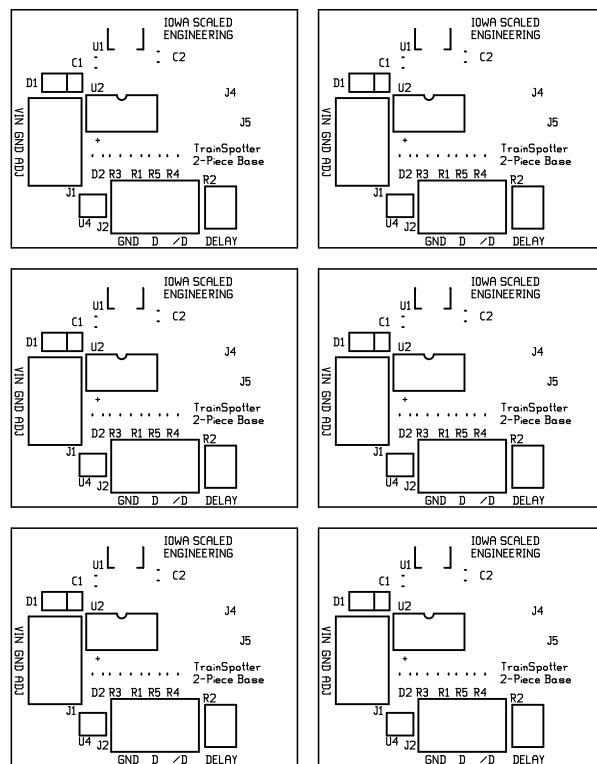


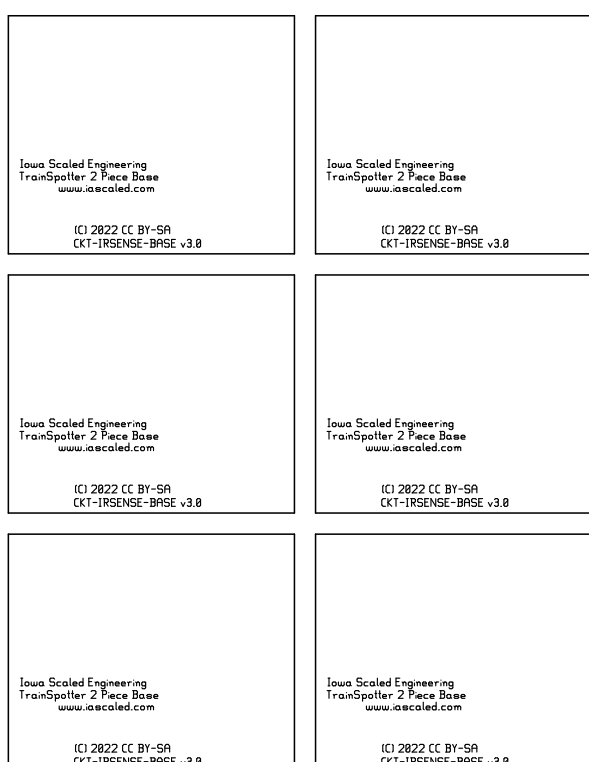


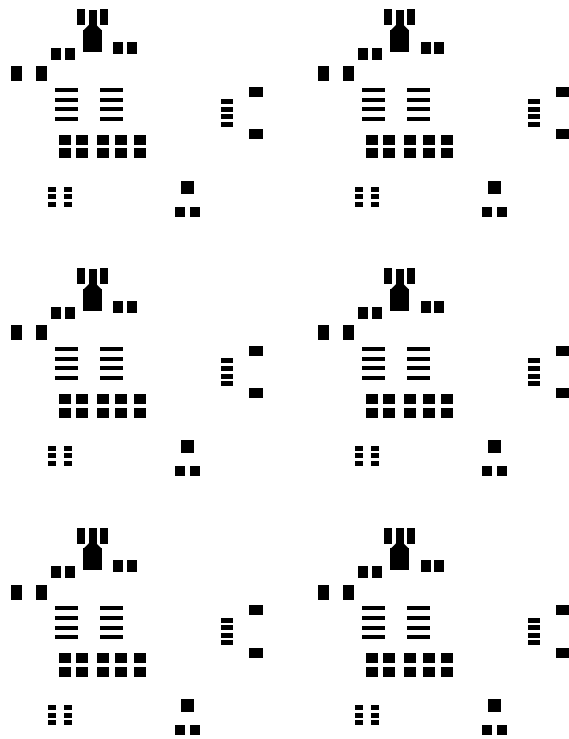


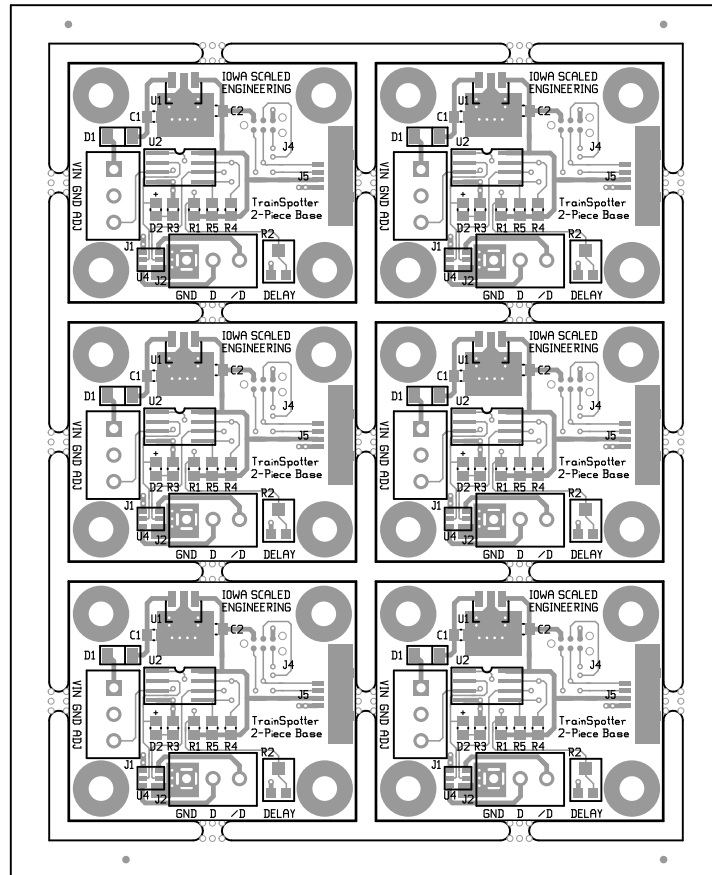


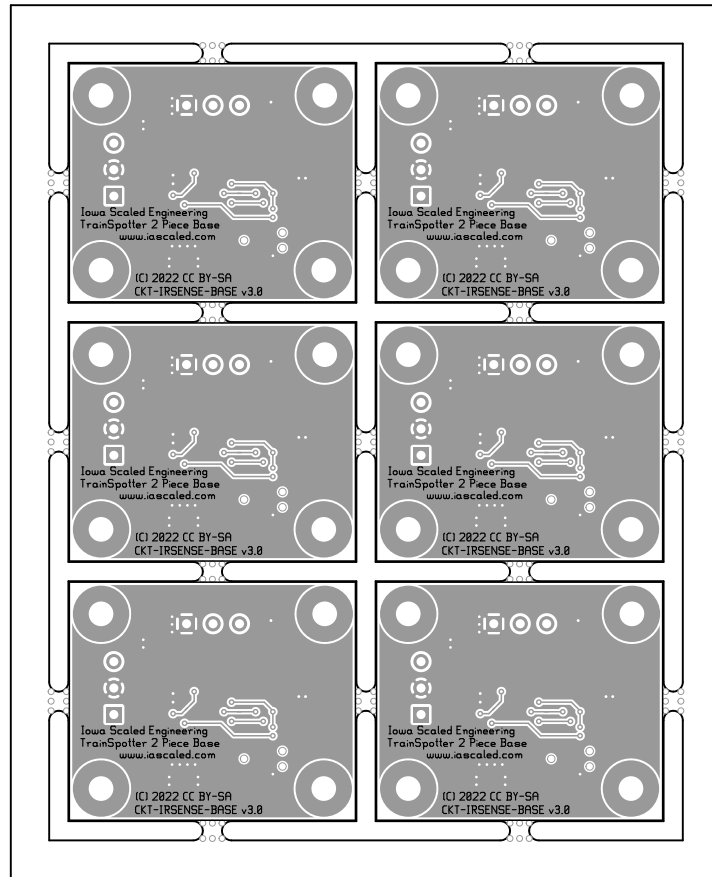










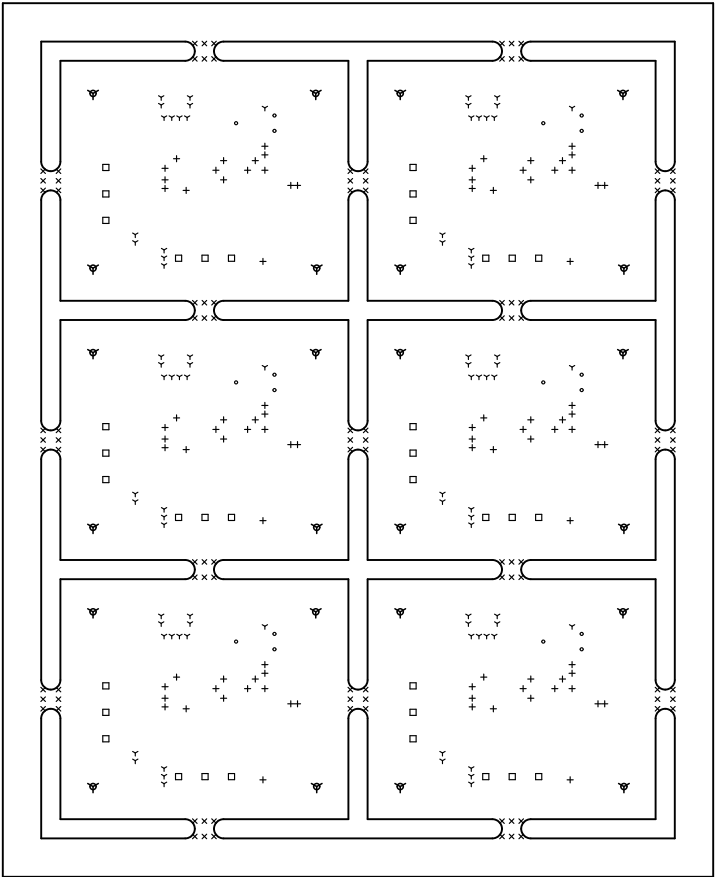


bottomassembly (mirrored), scale = 1:1.000  
ckt-irsense-base-array.pcb

There are 6 different drill sizes used in this layout, 360 holes total

Symbol	Diam. (Inch)	Count	Plated?
▽	0.014	84	YES
+	0.015	96	YES
×	0.031	102	NO
•	0.039	18	NO
□	0.047	36	YES
▽	0.129	24	YES

Title: (unknown) - Fabrication Drawing  
Author: Nathan D. Holmes  
Date: Sun Oct 16 03:50:19 2022 UTC



Board outline is the centerline of this path