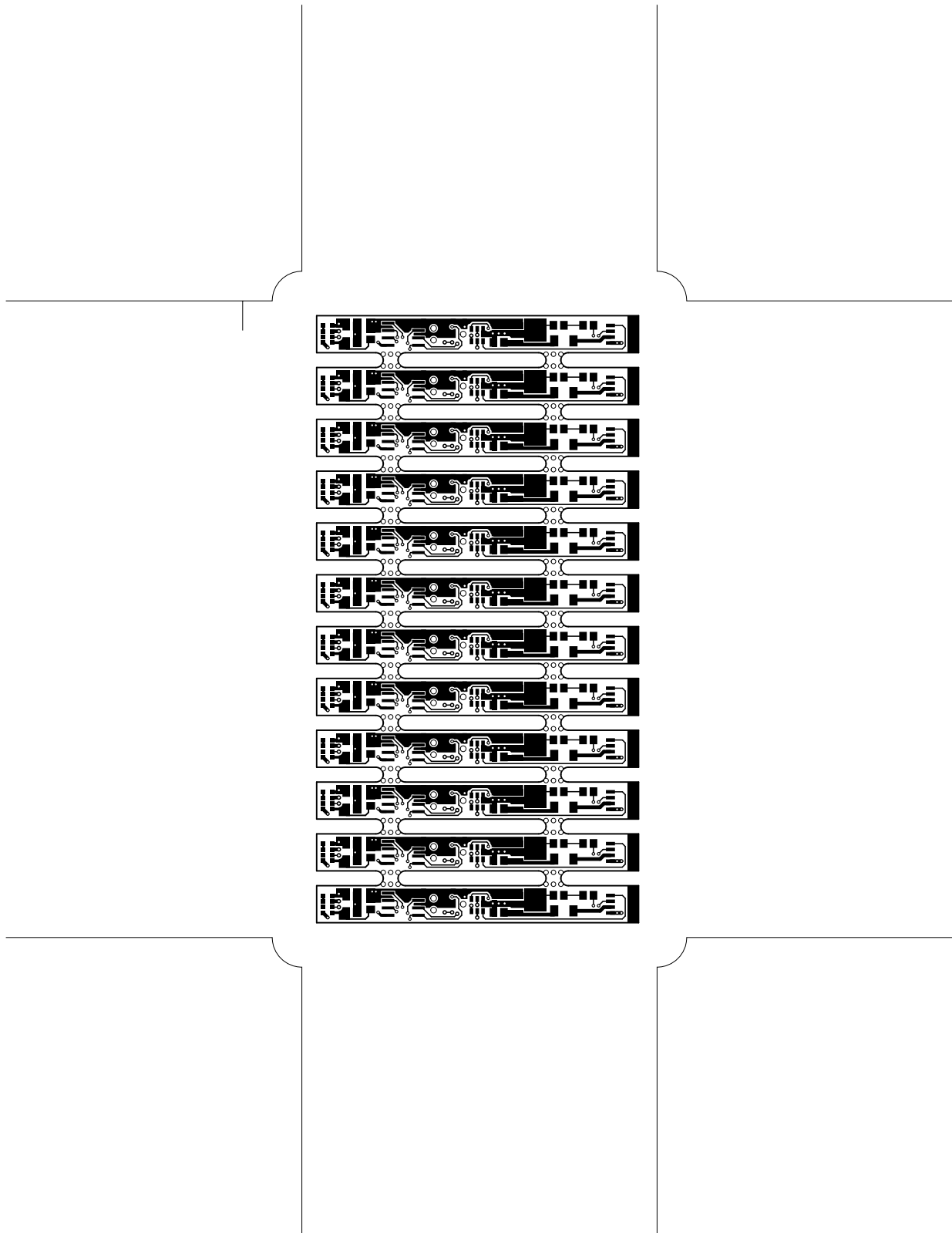
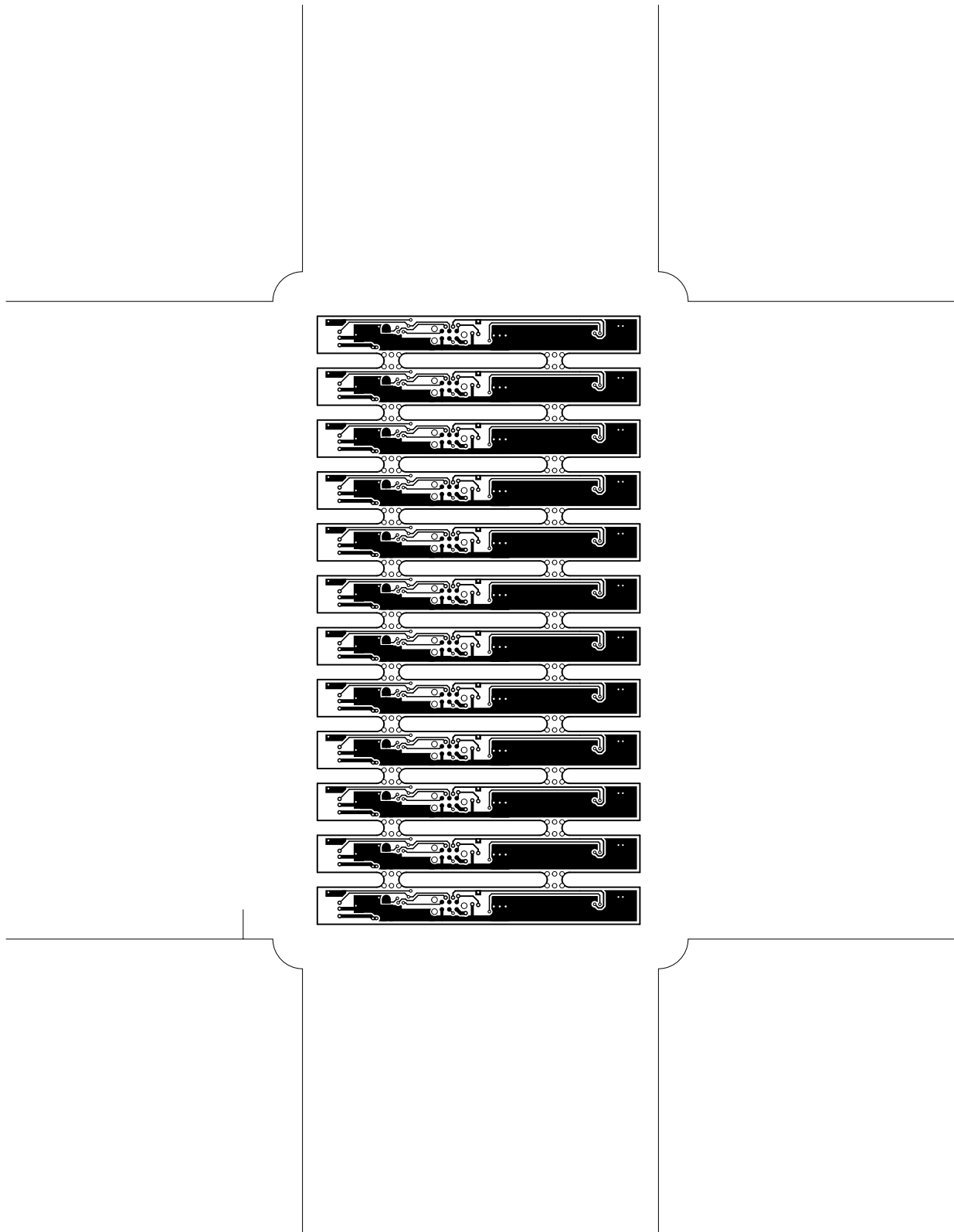


1. Table of Contents (This Page)

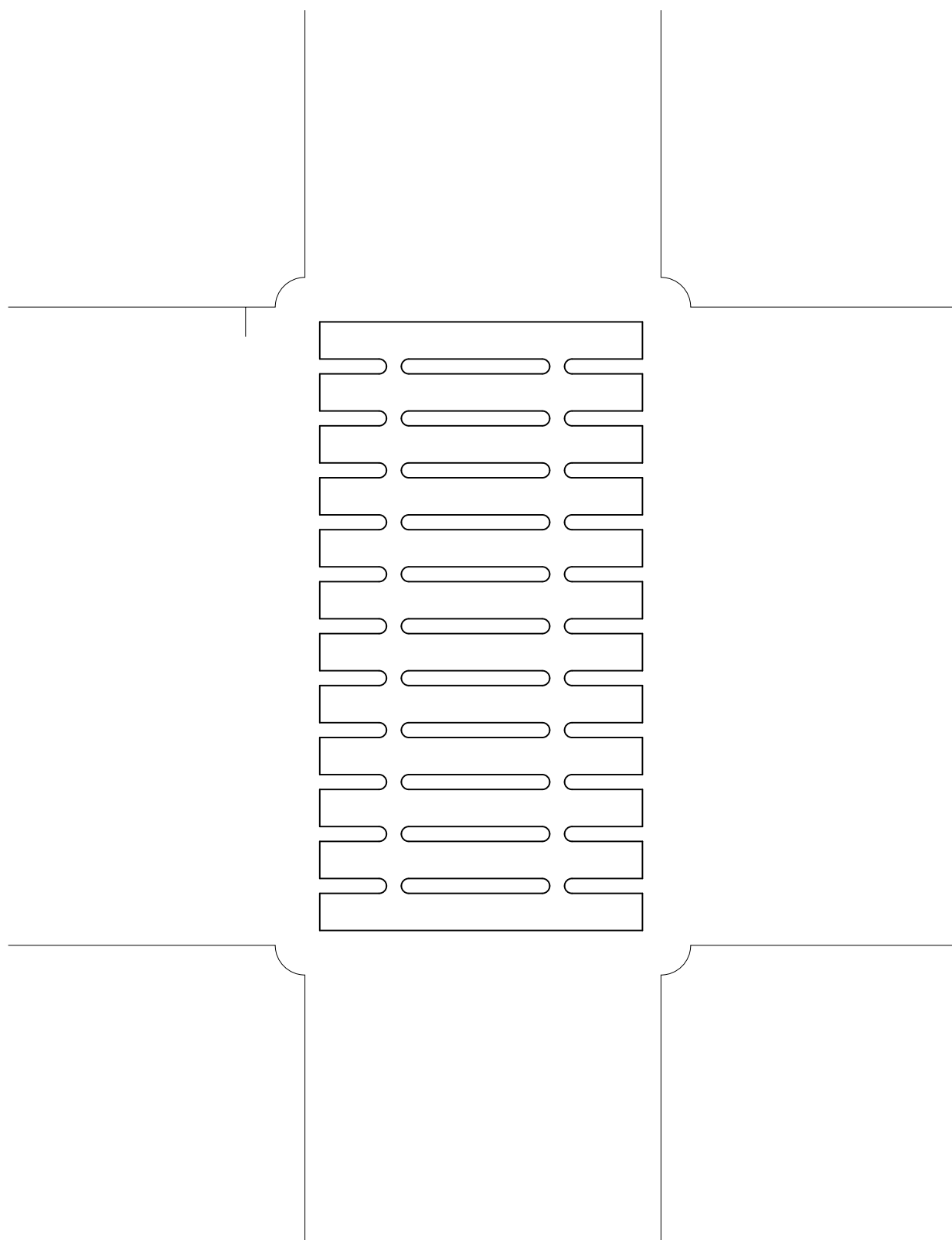
Created on Wed Aug 19 20:40:31 2020

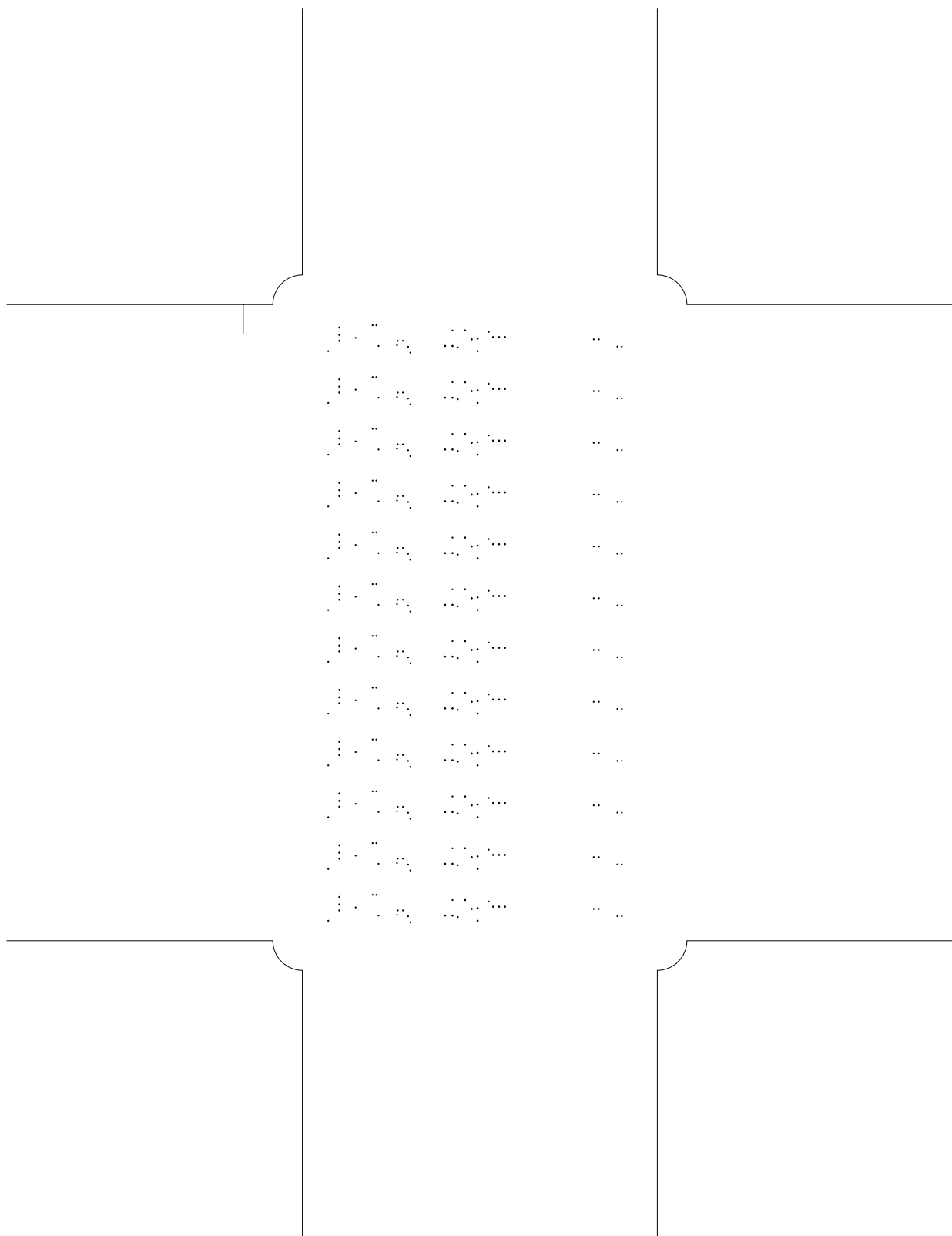
- 2. top
- 3. bottom
- 4. outline
- 5. plated-drill
- 6. unplated-drill
- 7. componentmask
- 8. soldermask
- 9. topsilk
- 10. bottomsilk
- 11. toppaste
- 12. bottompaste
- 13. topassembly
- 14. bottomassembly
- 15. fab

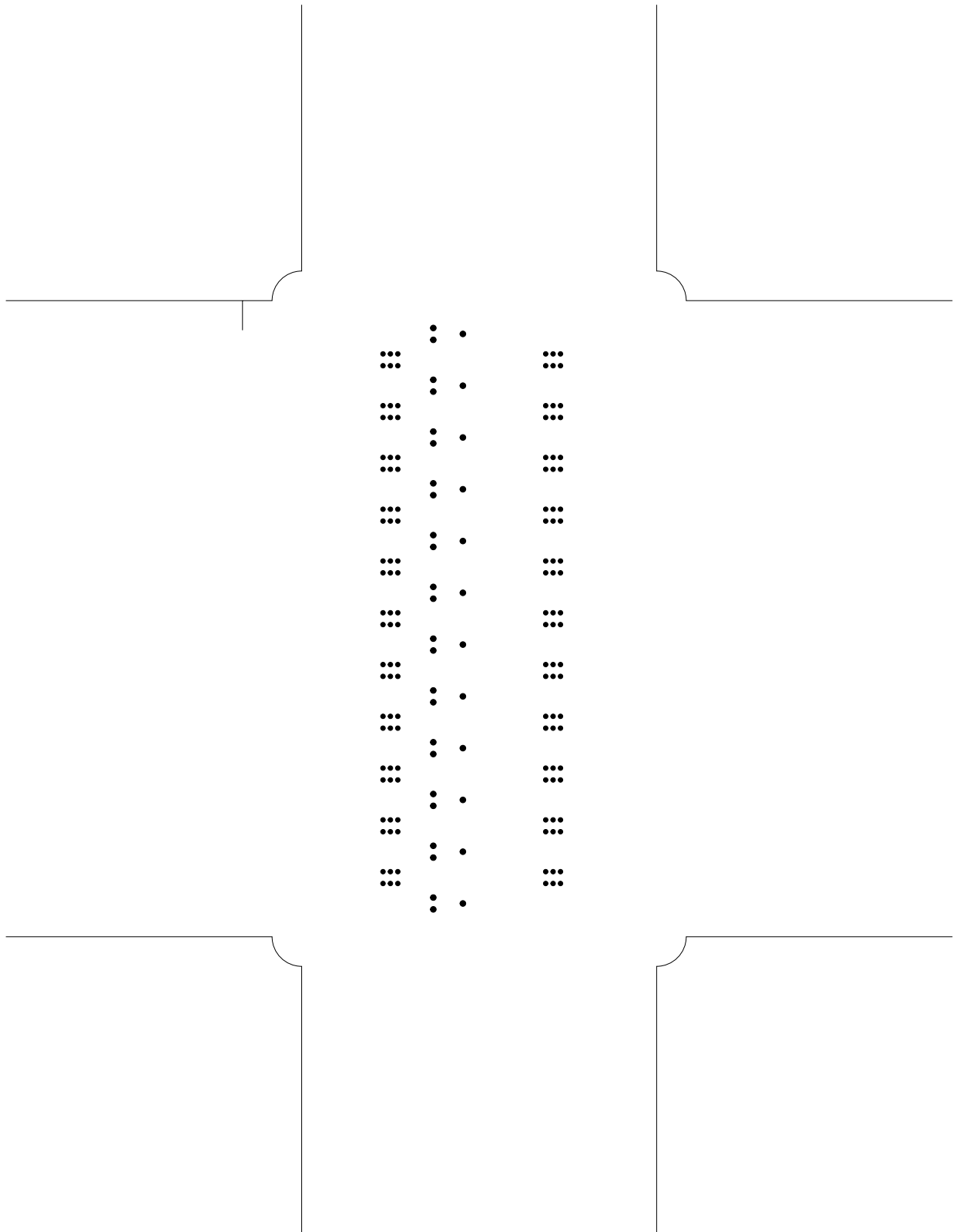


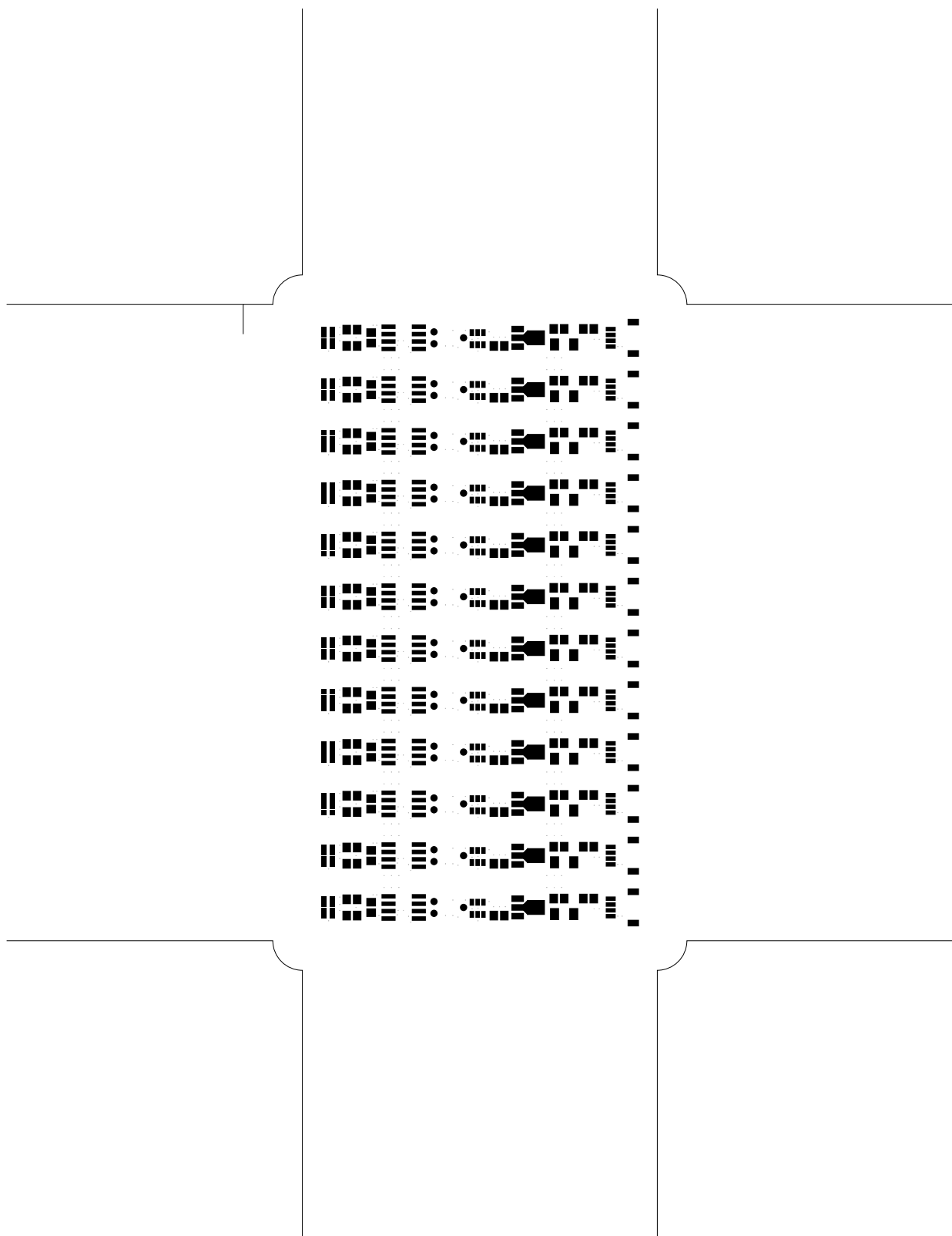


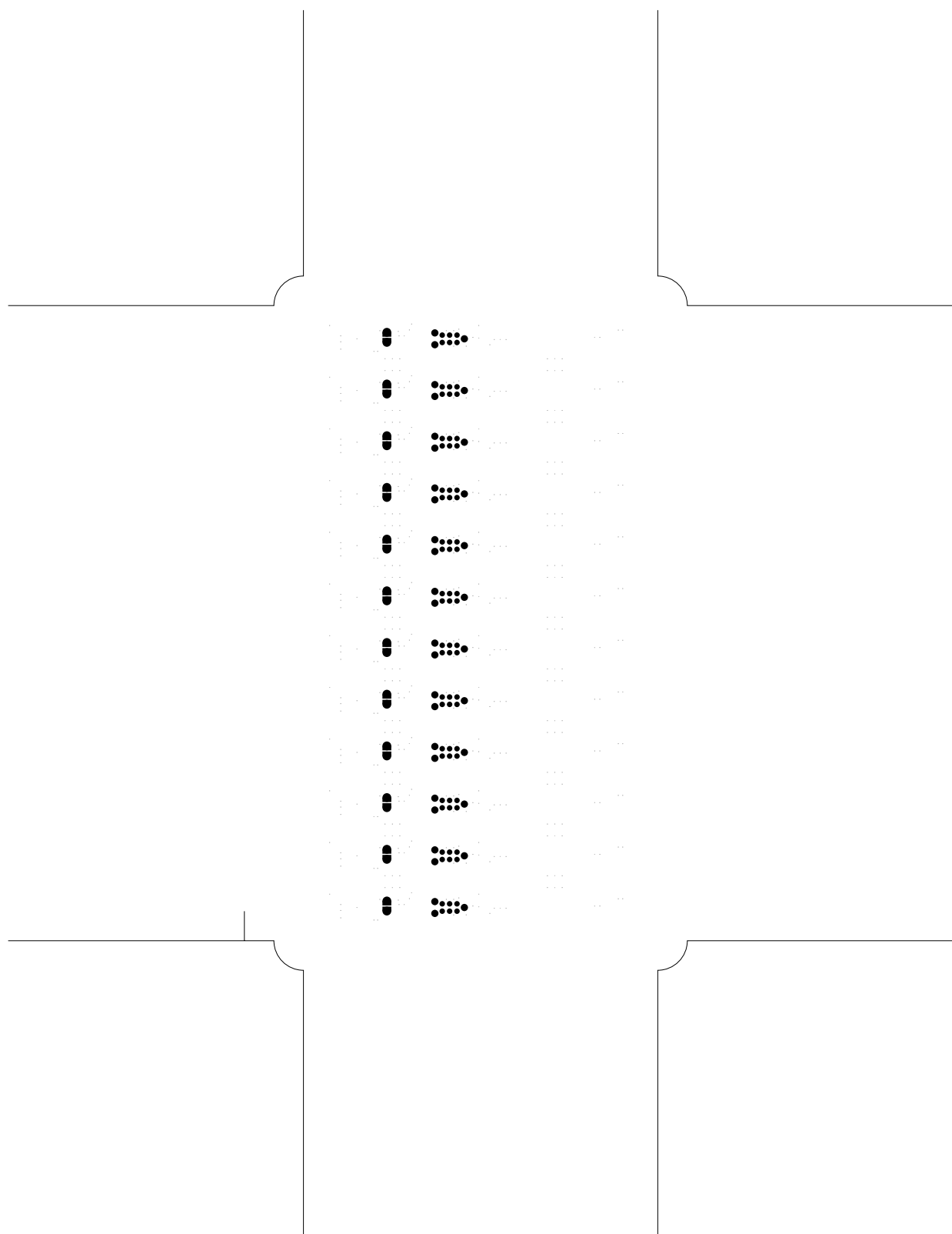
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ckt-irsense-ra-panel.pcb

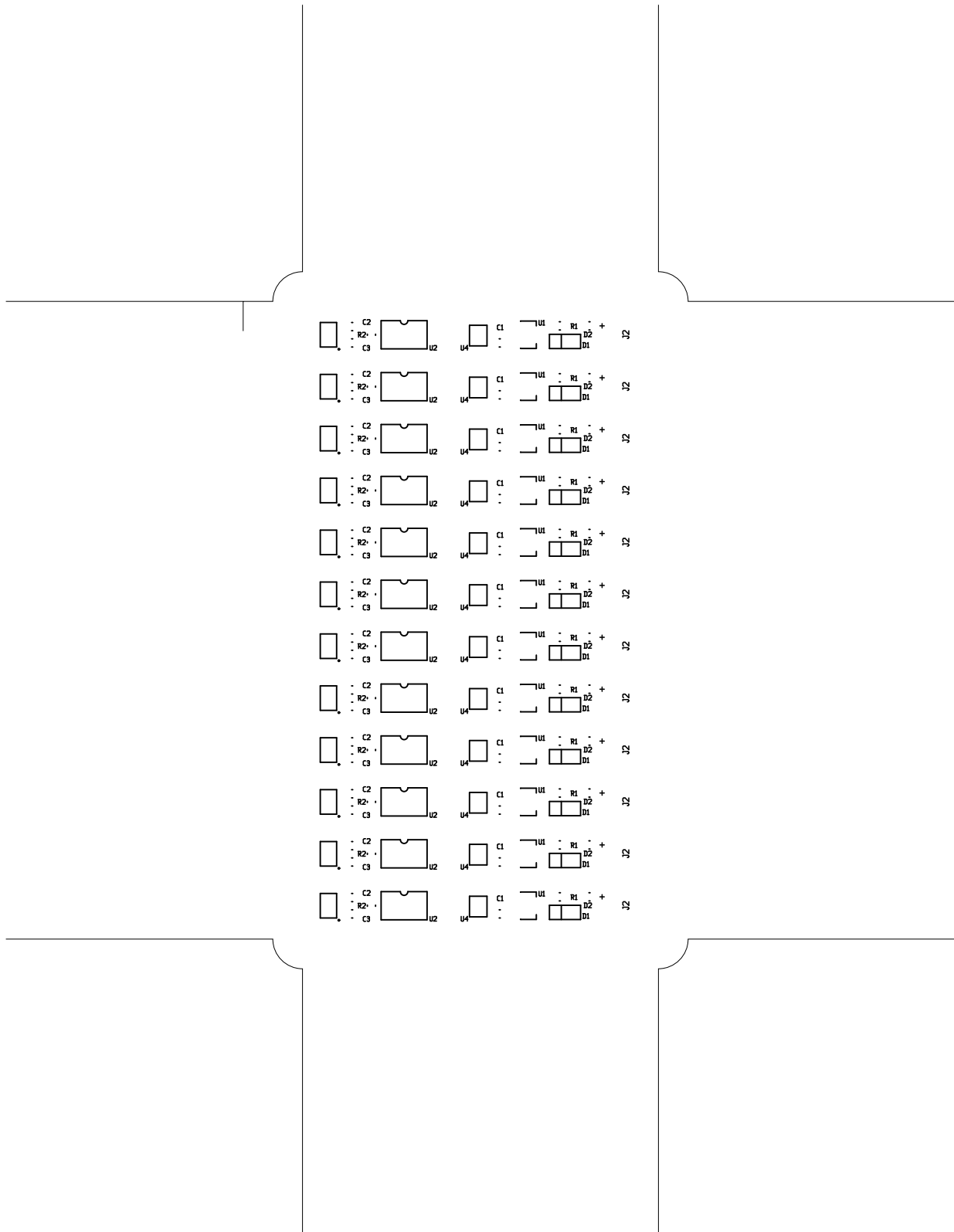


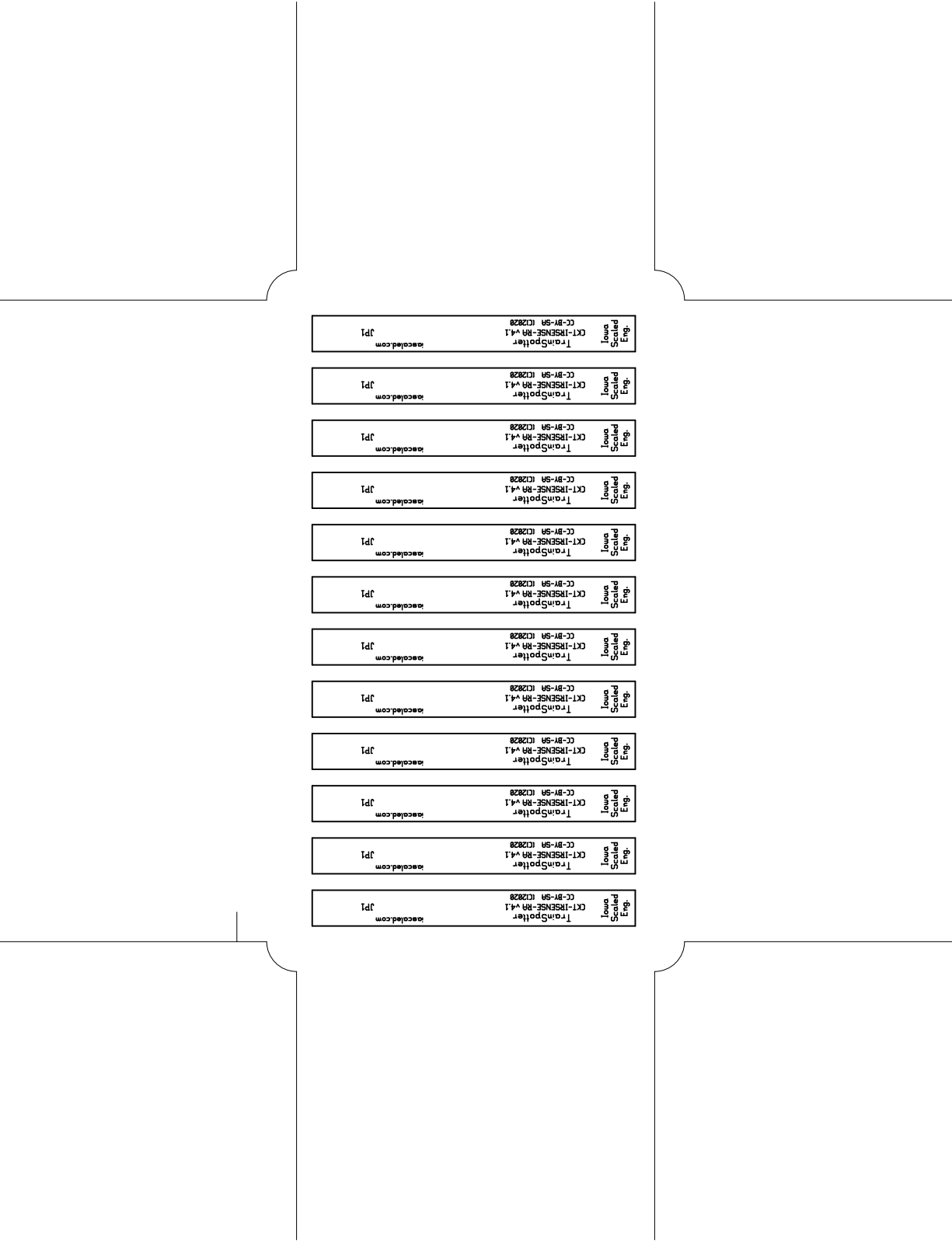




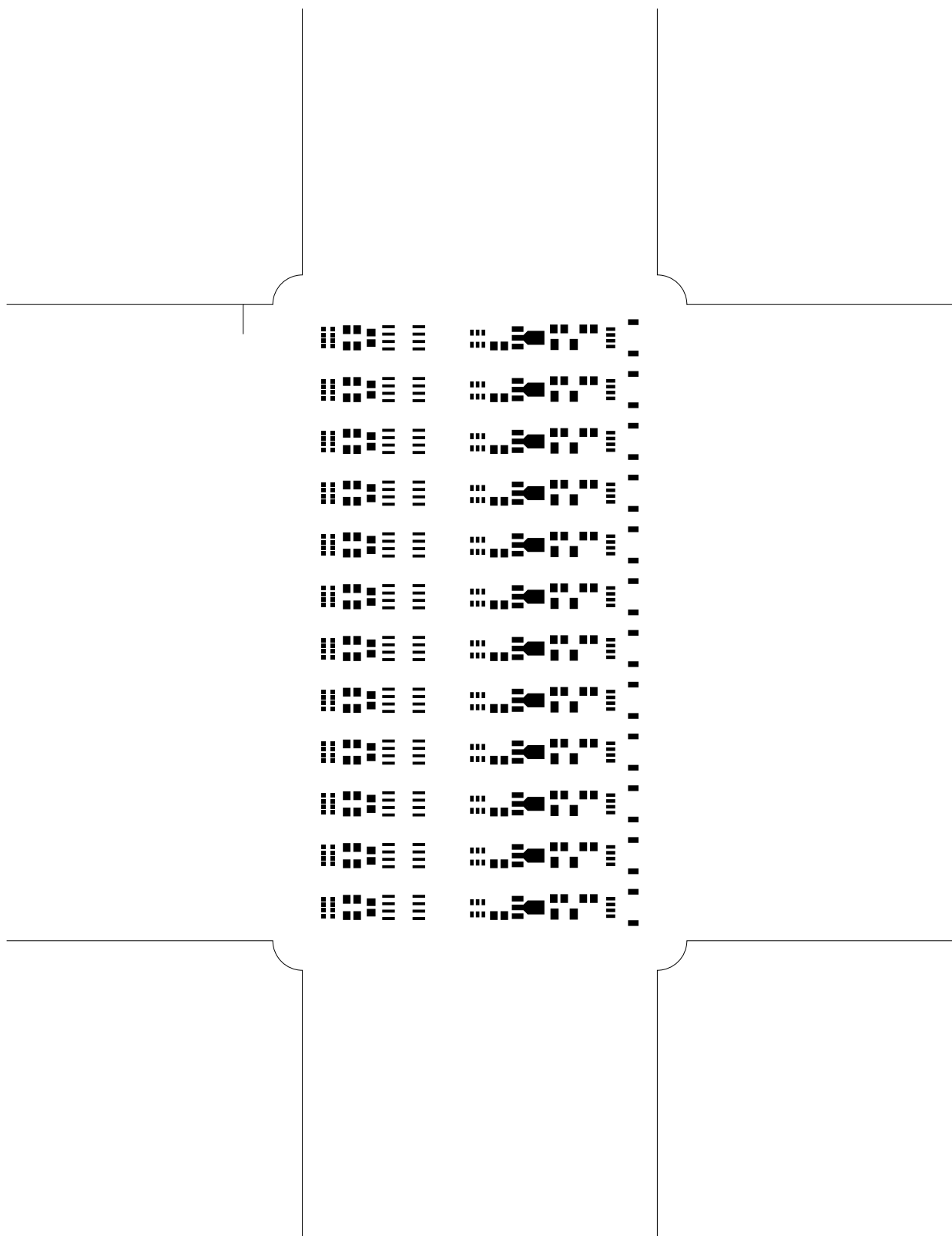


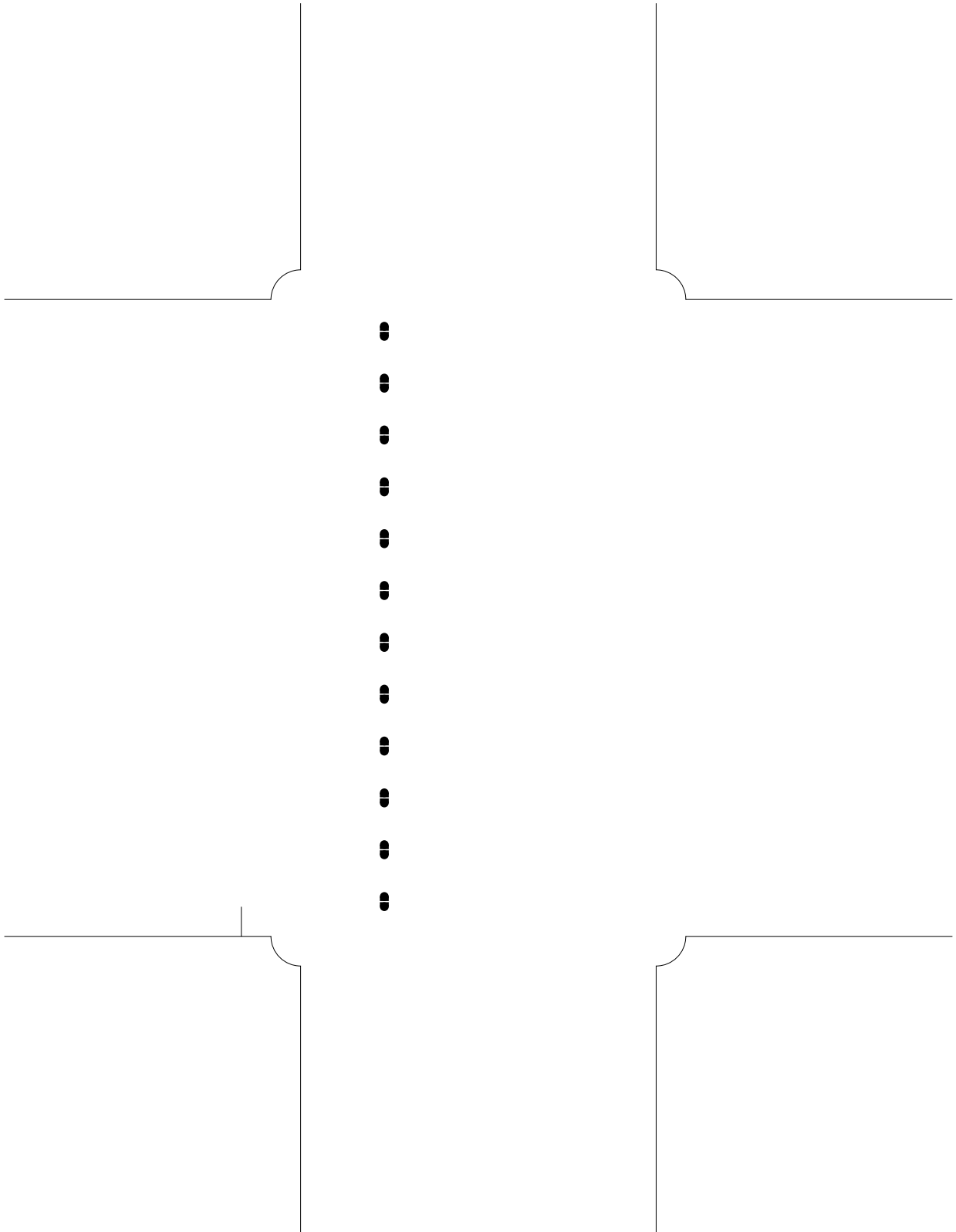


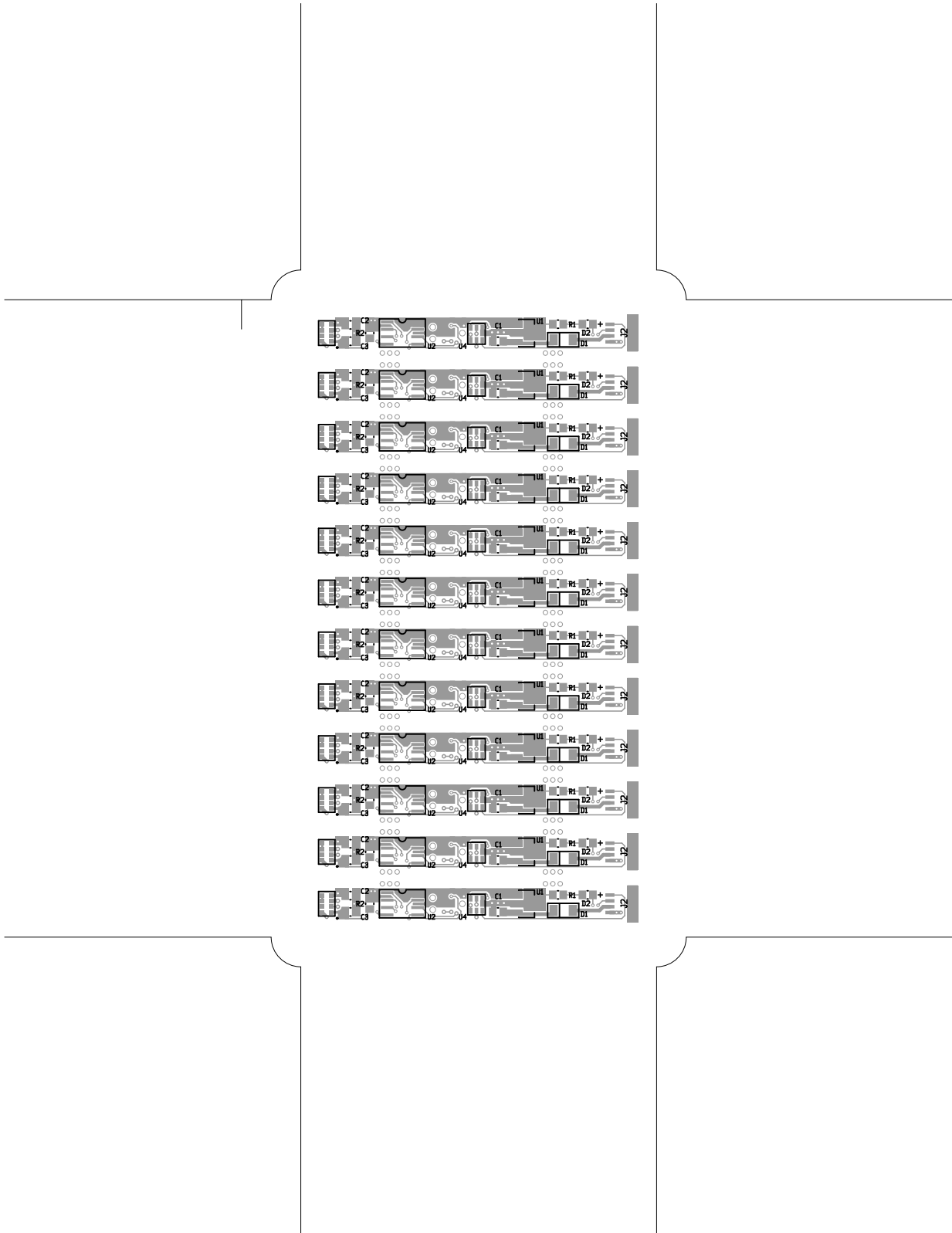


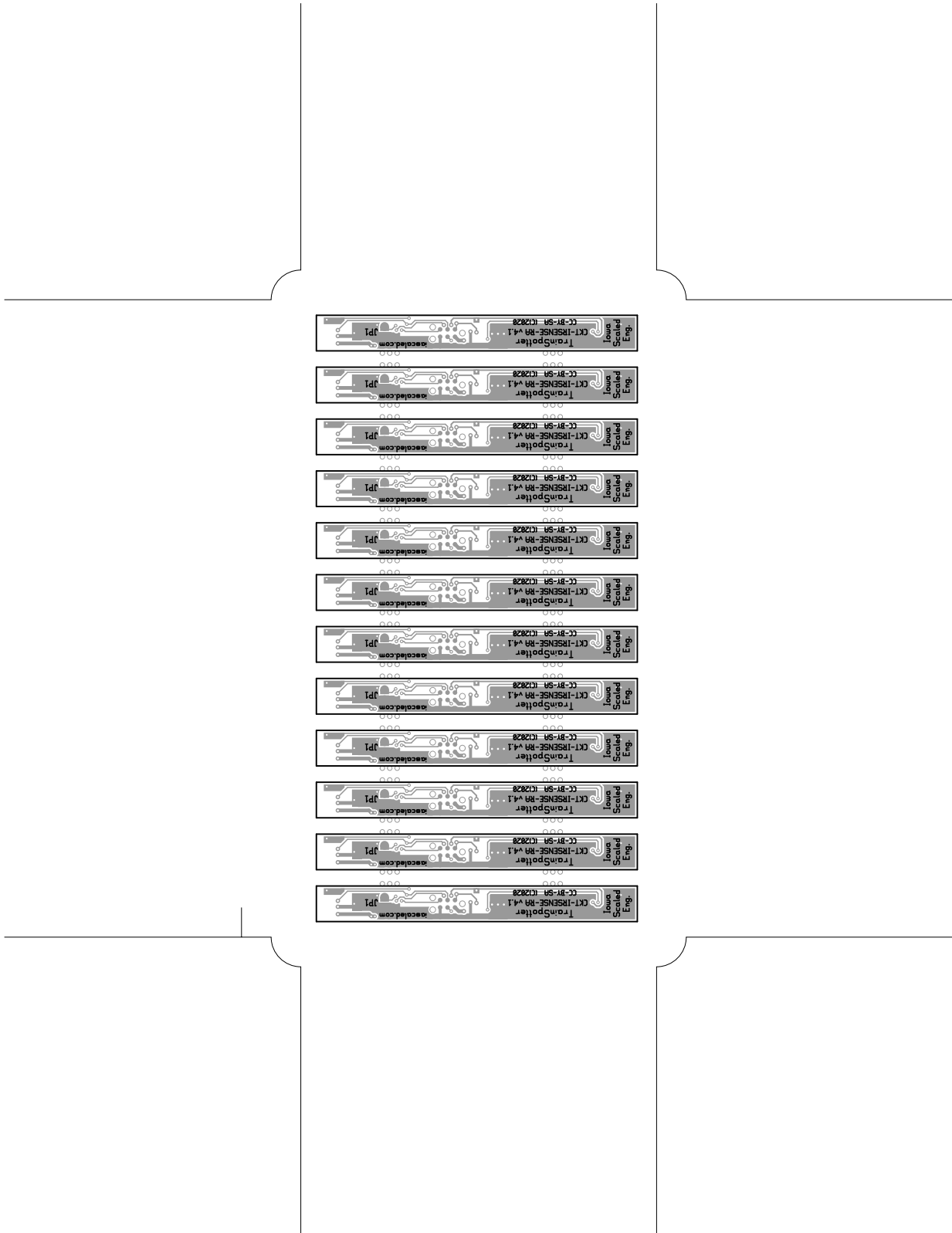


bottomsilk (mirrored), scale = 1:1.000
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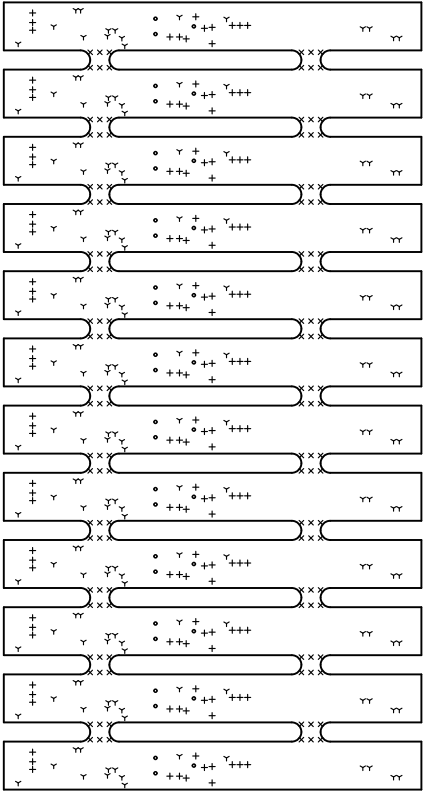




bottomassembly (mirrored), scale = 1:1.000
ckt-irsense-ra-panel.pcb

There are 4 different drill sizes used in this layout, 516 holes total

Symbol	Diam. (Inch)	Count	Plated?	
Y	0.012	192	YES	Title: (unknown) - Fabrication Drawing
+	0.014	156	YES	Author: Michael Petersen
x	0.031	132	NO	Date: Thu 20 Aug 2020 02:40:31 AM GMT UTC
•	0.039	36	NO	



Board outline is the centerline of this path