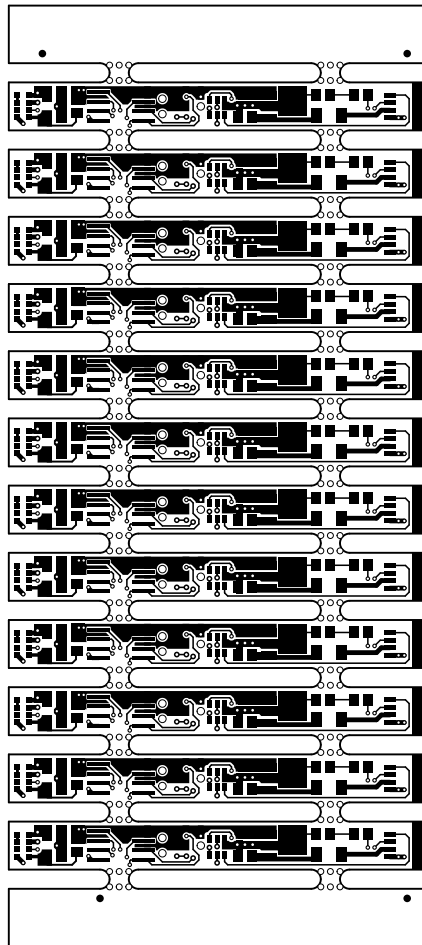
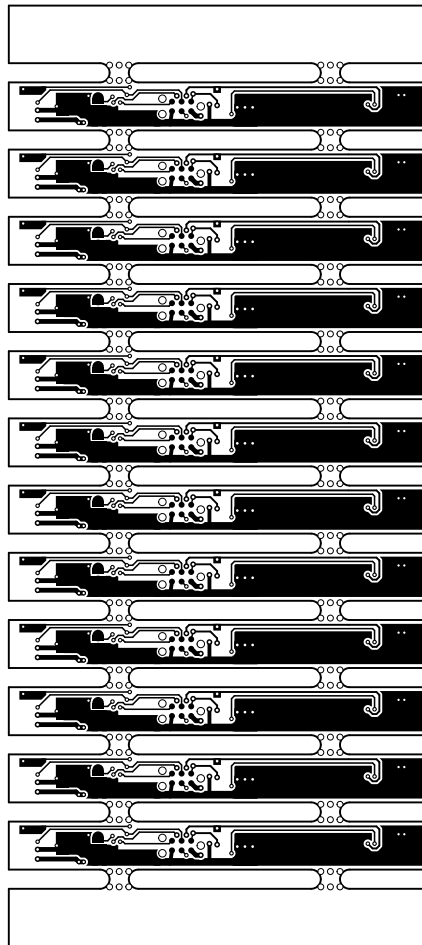


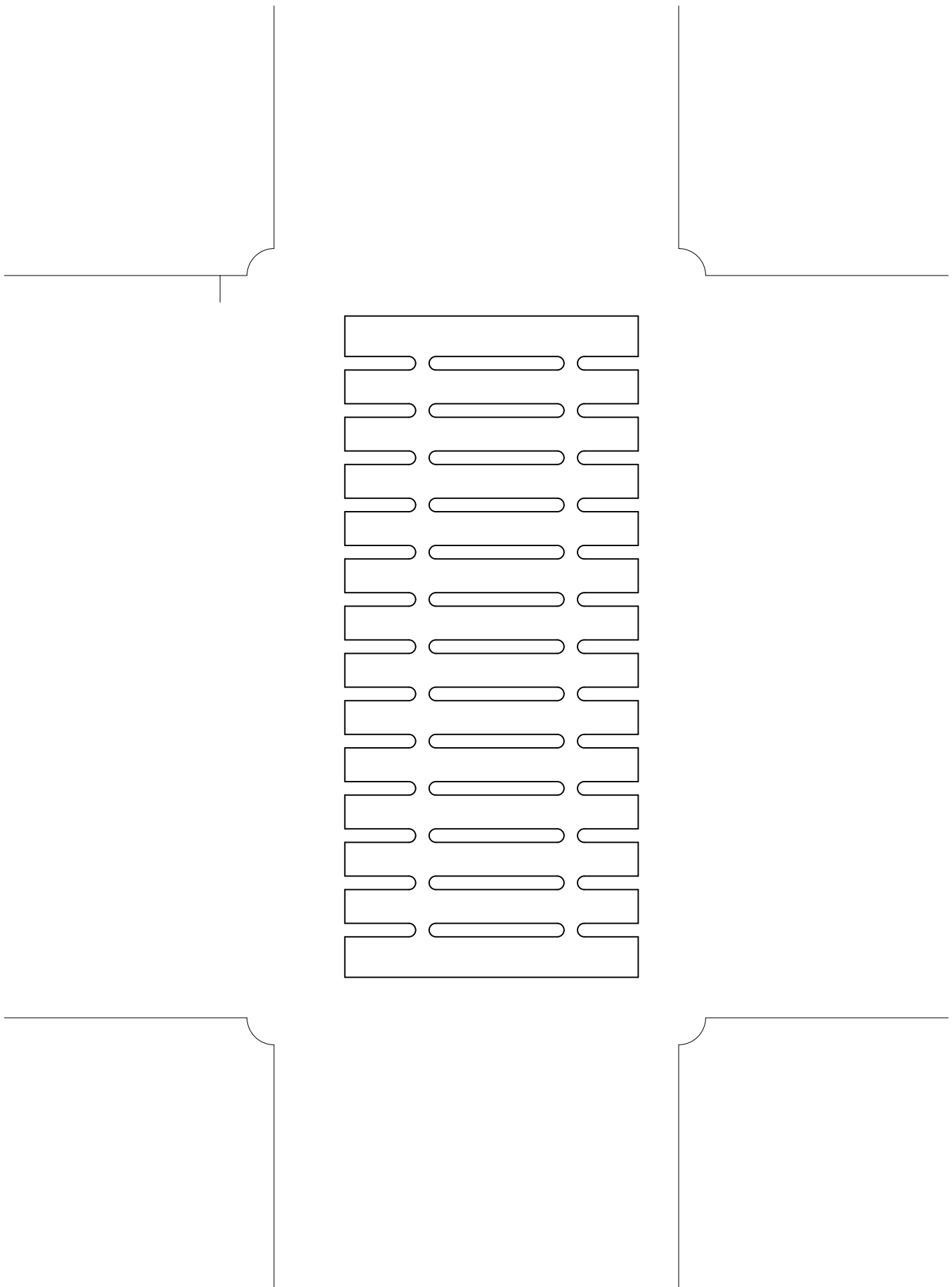
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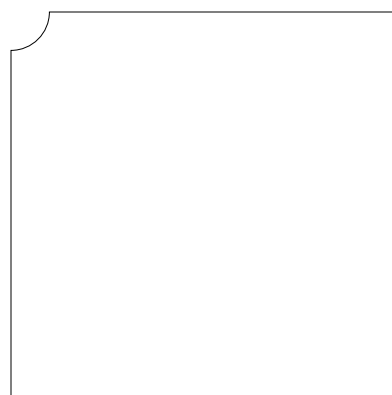
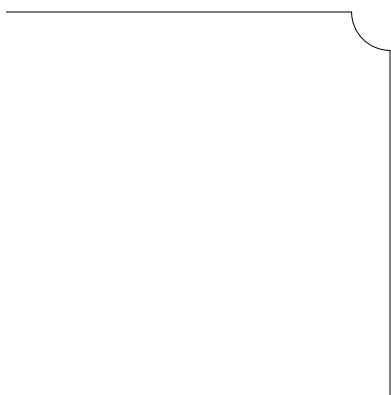
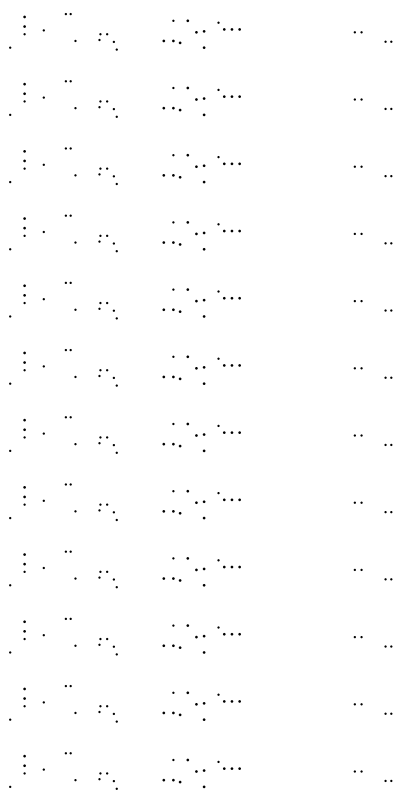
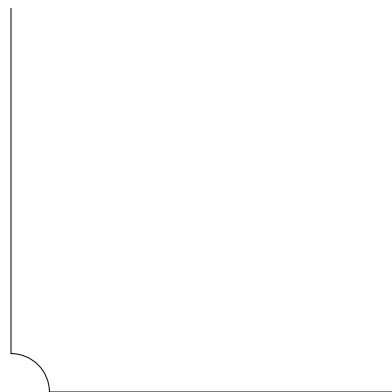
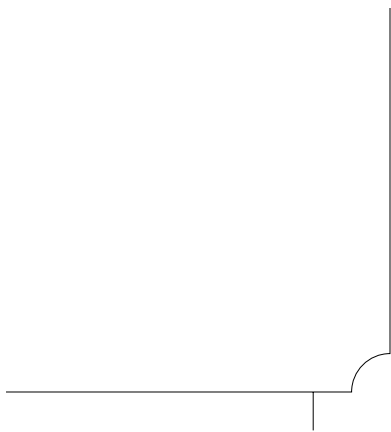
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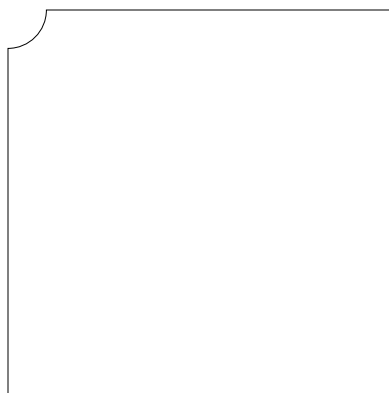
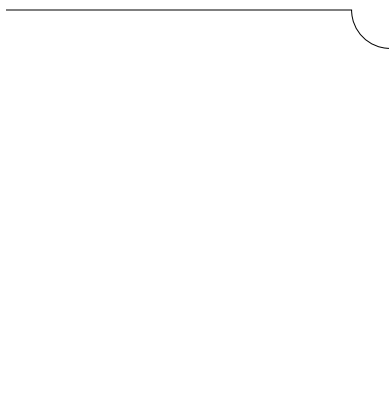
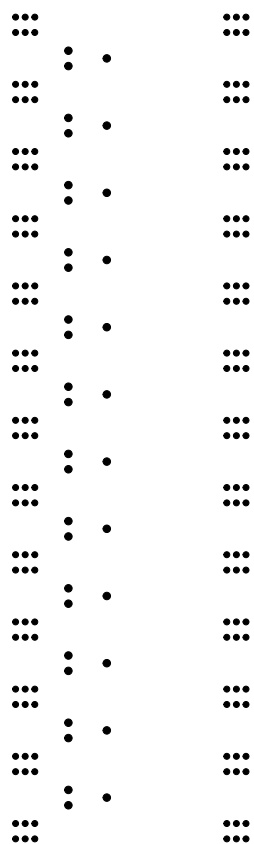
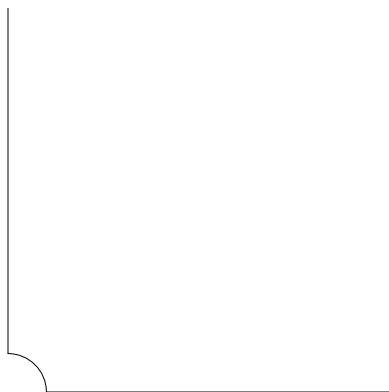
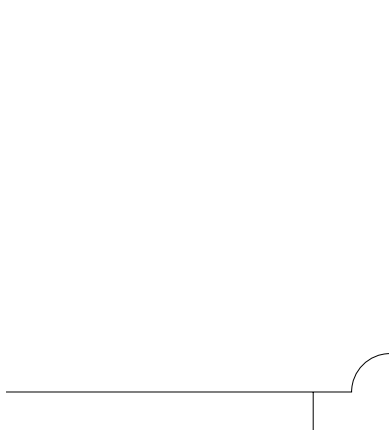
- 2. top
- 3. bottom
- 4. outline
- 5. plated-drill
- 6. unplated-drill
- 7. componentmask
- 8. soldermask
- 9. topsilk
- 10. bottomsilk
- 11. toppaste
- 12. bottompaste
- 13. topassembly
- 14. bottomassembly
- 15. fab

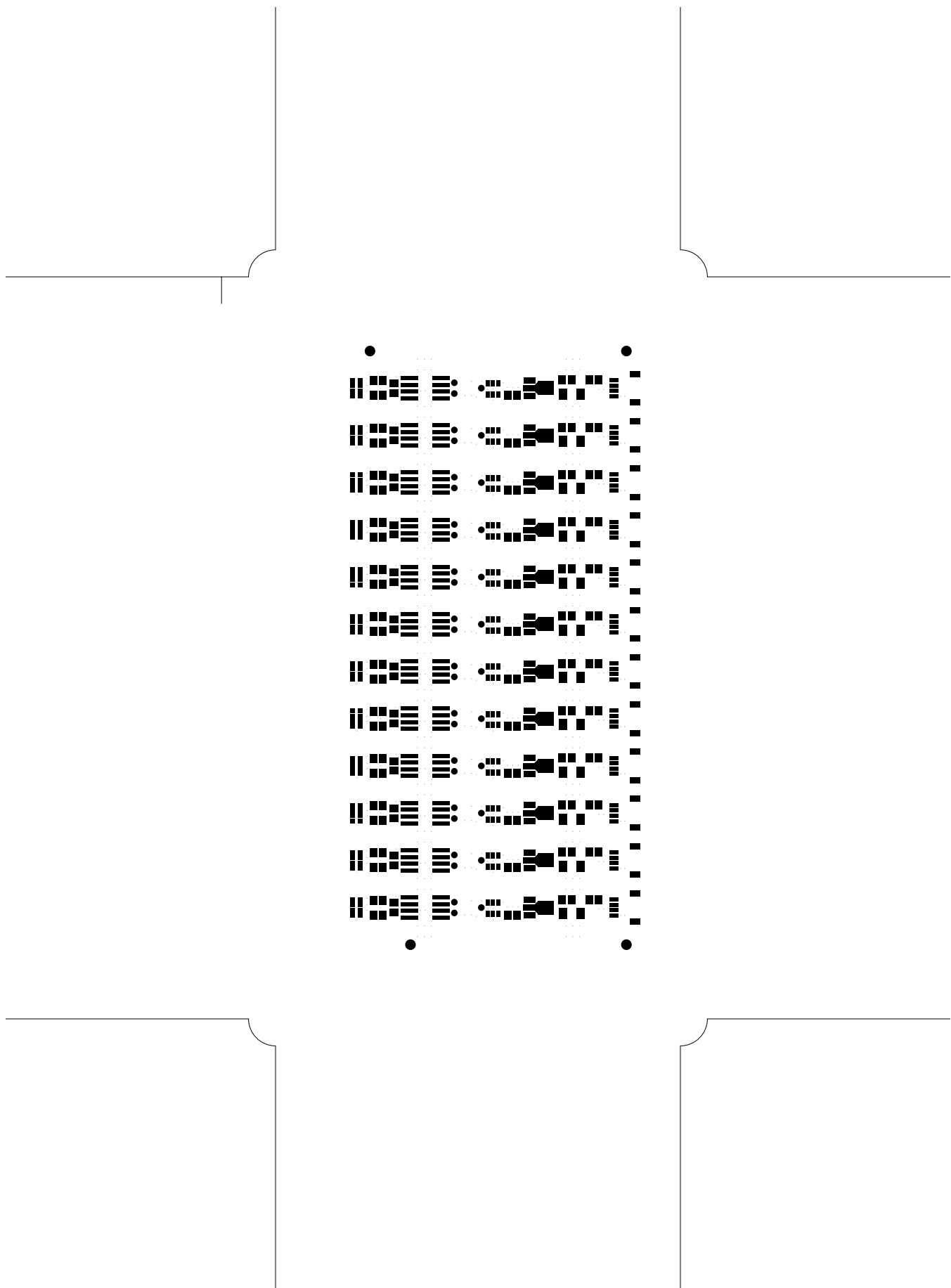


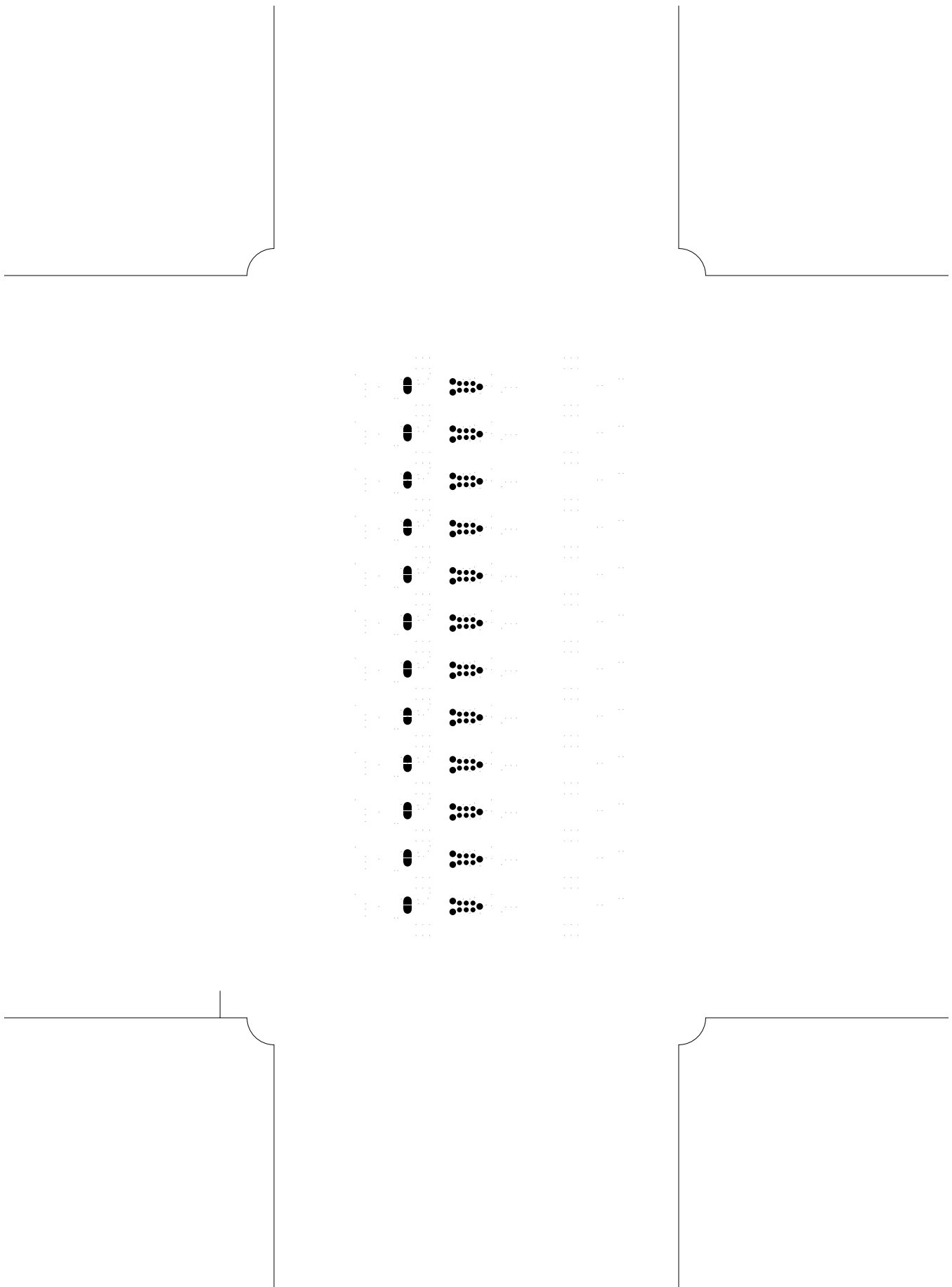




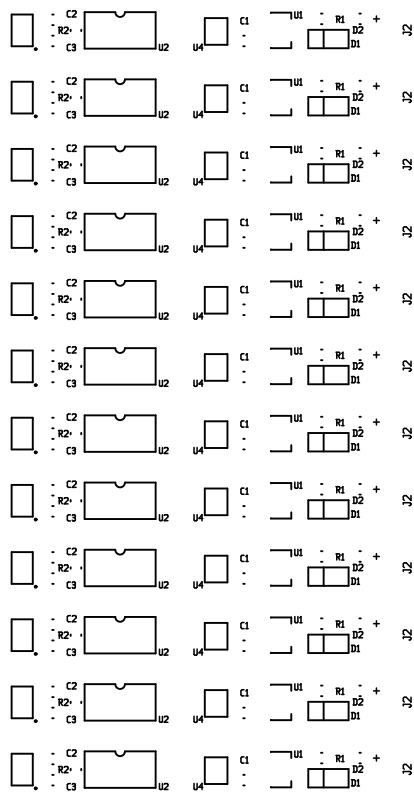




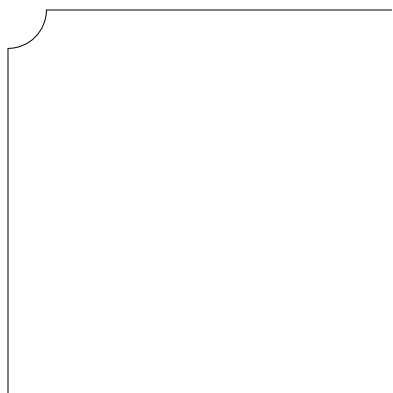
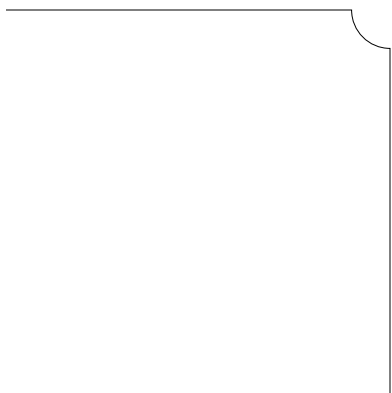
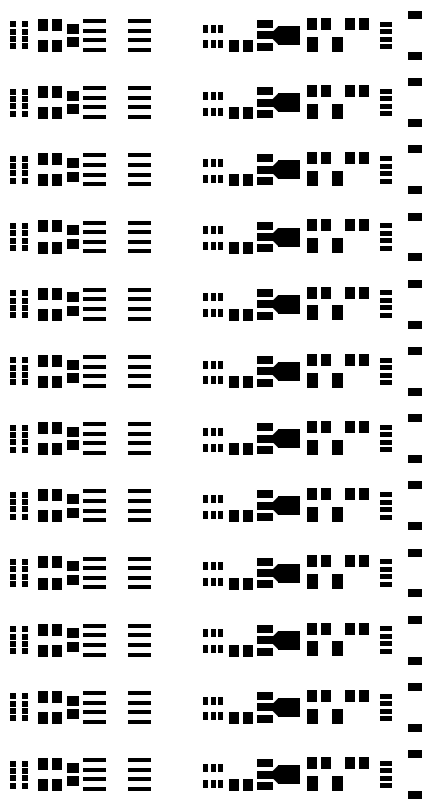
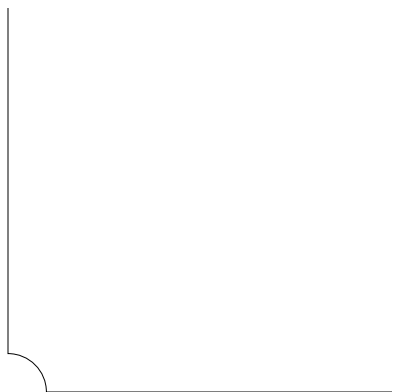
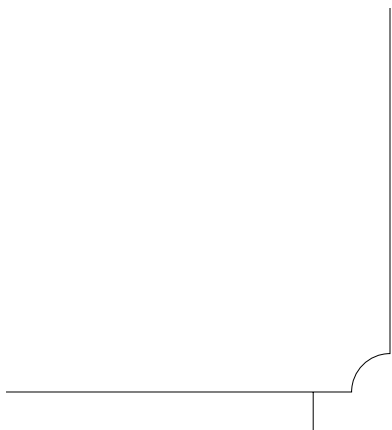


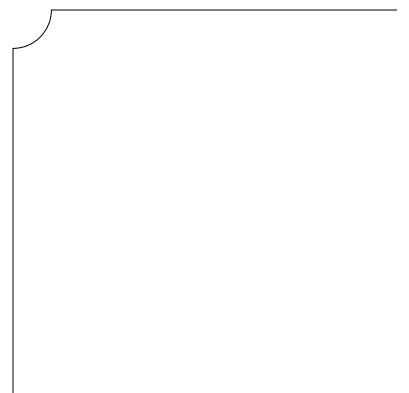
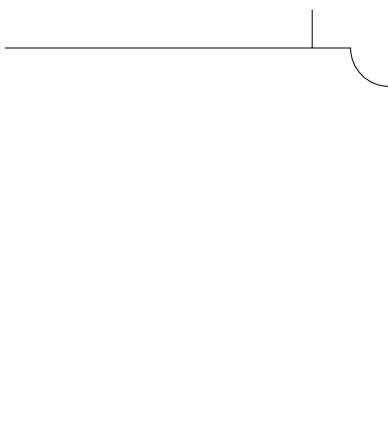
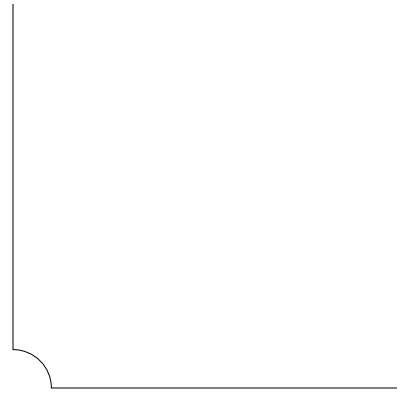
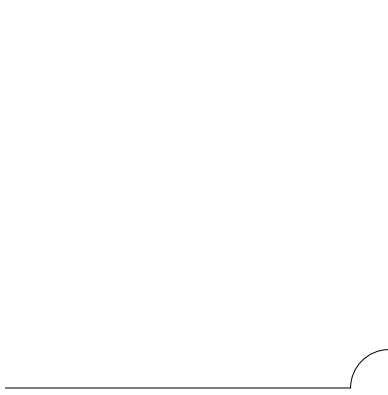


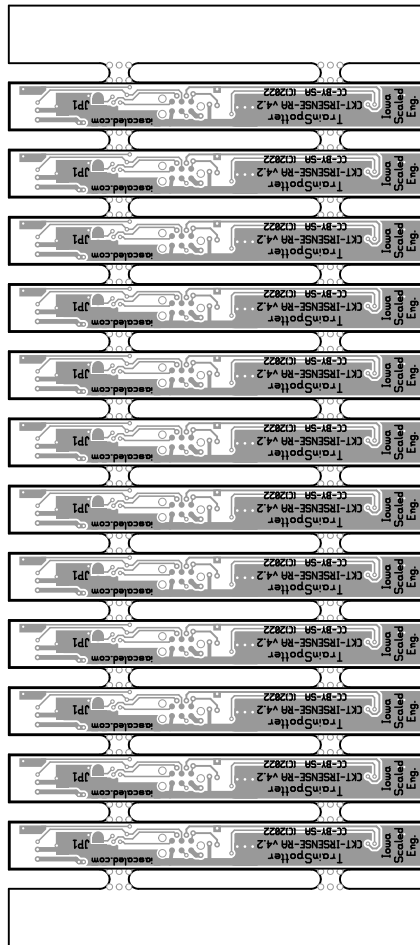
bottommask (mirrored), scale = 1:1.000
ckt-irsense-ra-array.pcb



img. 3 panel Scale 1	TrainSpotter CKT-IRSENSE-RA v4.2 CC-BV-SN IC2822	kascloud.com jpg
img. 3 panel Scale 1	TrainSpotter CKT-IRSENSE-RA v4.2 CC-BV-SN IC2822	kascloud.com jpg
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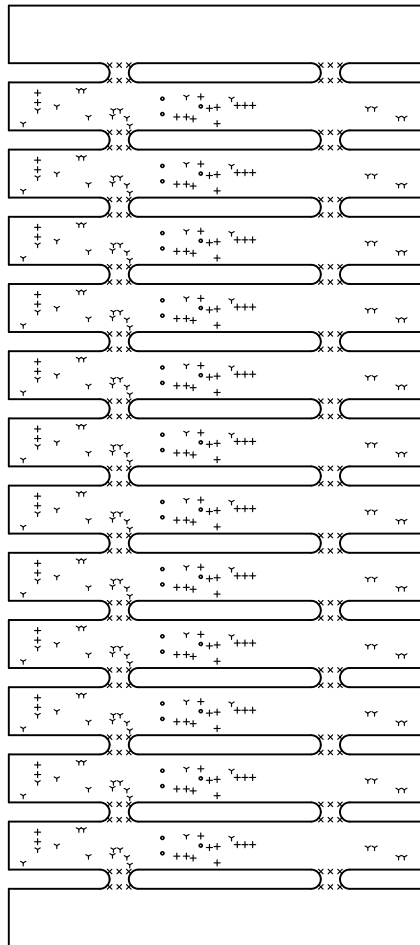




bottomassembly (mirrored), scale = 1:1.000
ckt-irsense-ra-array.pcb

There are 4 different drill sizes used in this layout, 540 holes total

Symbol	Diam. (Inch)	Count	Plated?	
Y	0.012	204	YES	Title: (unknown) - Fabrication Drawing
+	0.014	144	YES	Author: Michael Petersen
x	0.031	156	NO	Date: Sun May 15 03:05:20 2022 UTC
•	0.039	36	NO	



Board outline is the centerline of this path