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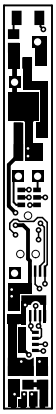
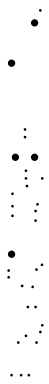


top, scale = 1:1.000
ckt-irsense.pcb

bottom (mirrored), scale = 1:1.000
ckt-irsense.pcb

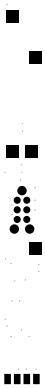
outline, scale = 1:1.000
ckt-irsense.pcb



plated-drill, scale = 1:1.000
ckt-irsense.pcb

unplated-drill, scale = 1:1.000
ckt-irsense.pcb

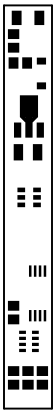
topmask, scale = 1:1.000
ckt-irsense.pcb



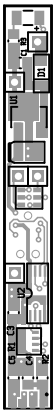
bottommask (mirrored), scale = 1:1.000
ckt-irsense.pcb

topsilk, scale = 1:1.000
ckt-irsense.pcb

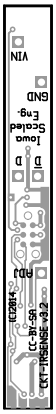
bottomsilk (mirrored), scale = 1:1.000
ckt-irsense.pcb



toppaste, scale = 1:1.000
ckt-irsense.pcb



topassembly, scale = 1:1.000
ckt-irsense.pcb



bottomassembly (mirrored), scale = 1:1.000
ckt-irsense.pcb

fab, scale = 1:1.000
ckt-irsense.pcb

There are 3 different drill sizes used in this layout, 37 holes total

Symbol	Diam. (Inch)	Count	Plated?
˅	0.014	29	YES
+	0.038	5	YES
×	0.039	3	NO

Title: (unknown) - Fabrication Drawing
Author: Michael Petersen
Date: Thu 16 Oct 2014 06:59:54 PM GMT UTC

Board outline is the centerline of this path