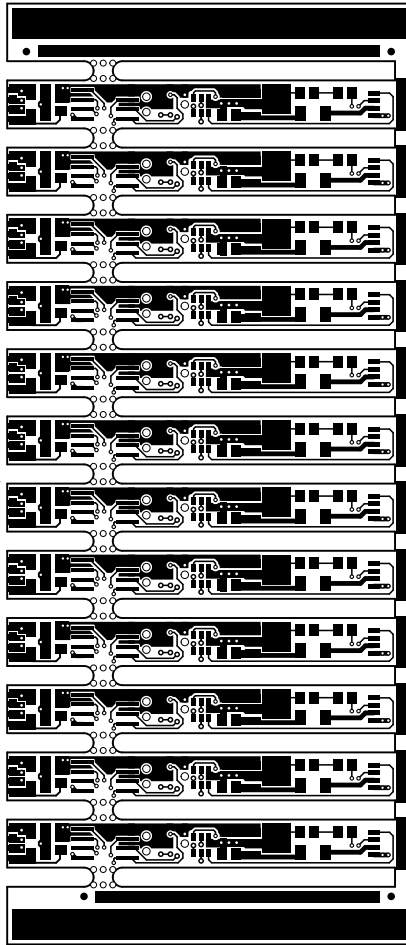


1. Table of Contents (This Page)

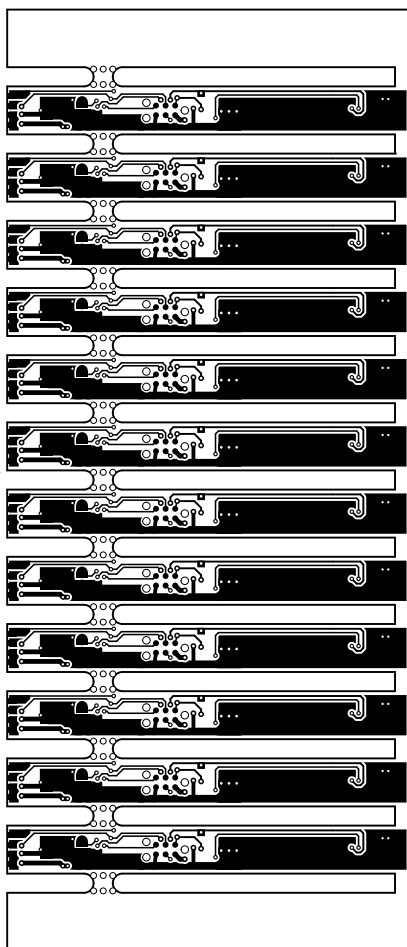
Created on Wed Jun 21 21:03:34 2023

2. top
3. bottom
4. outline
5. plated-drill
6. unplated-drill
7. componentmask
8. soldermask
9. topsilk
10. bottomsilk
11. toppaste
12. bottompaste
13. topassembly
14. bottomassembly
15. fab

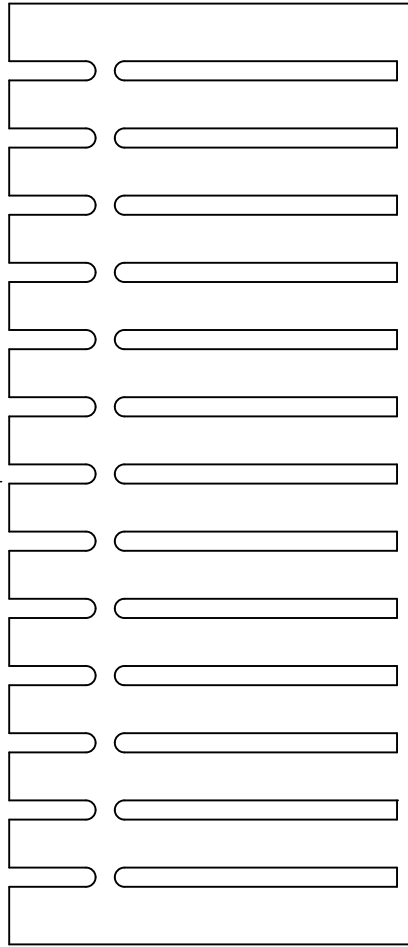
NO tabs, scoring, or breakaway rails on this edge.
Metal pads on this side should be coincident with board edge (<5mil).
Please do NOT pull back metal!

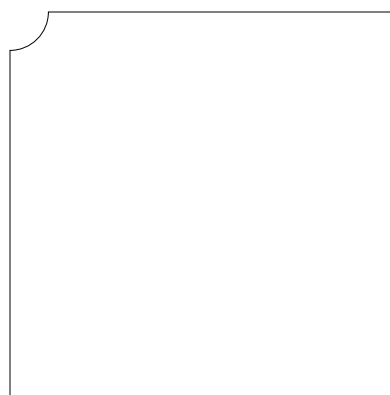
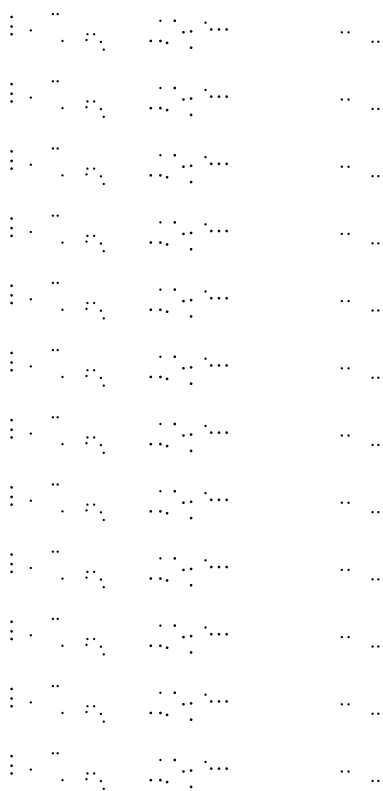


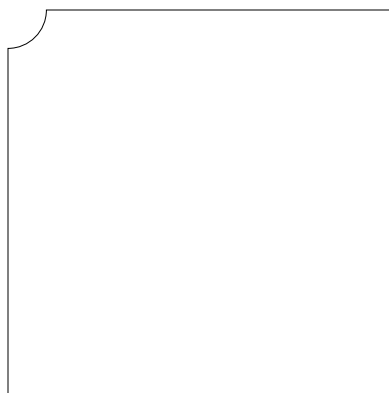
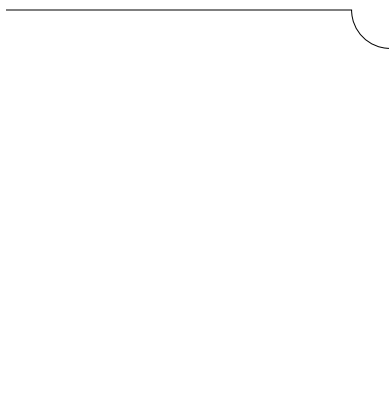
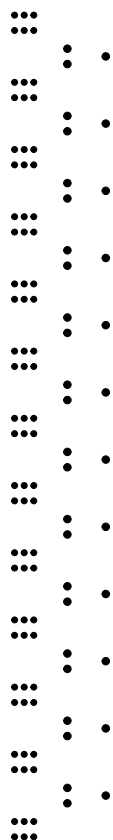
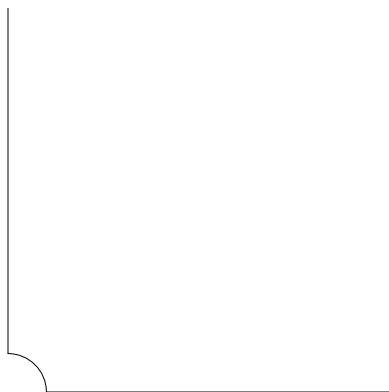
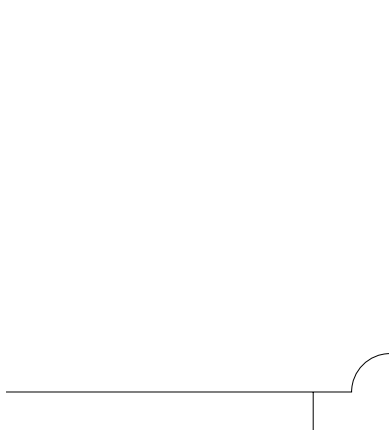
Please do NOT put back metal!
 Metal pads on the side should be coincident with pour edge (2mil).
 NO tape scoring or preformal route on this edge.

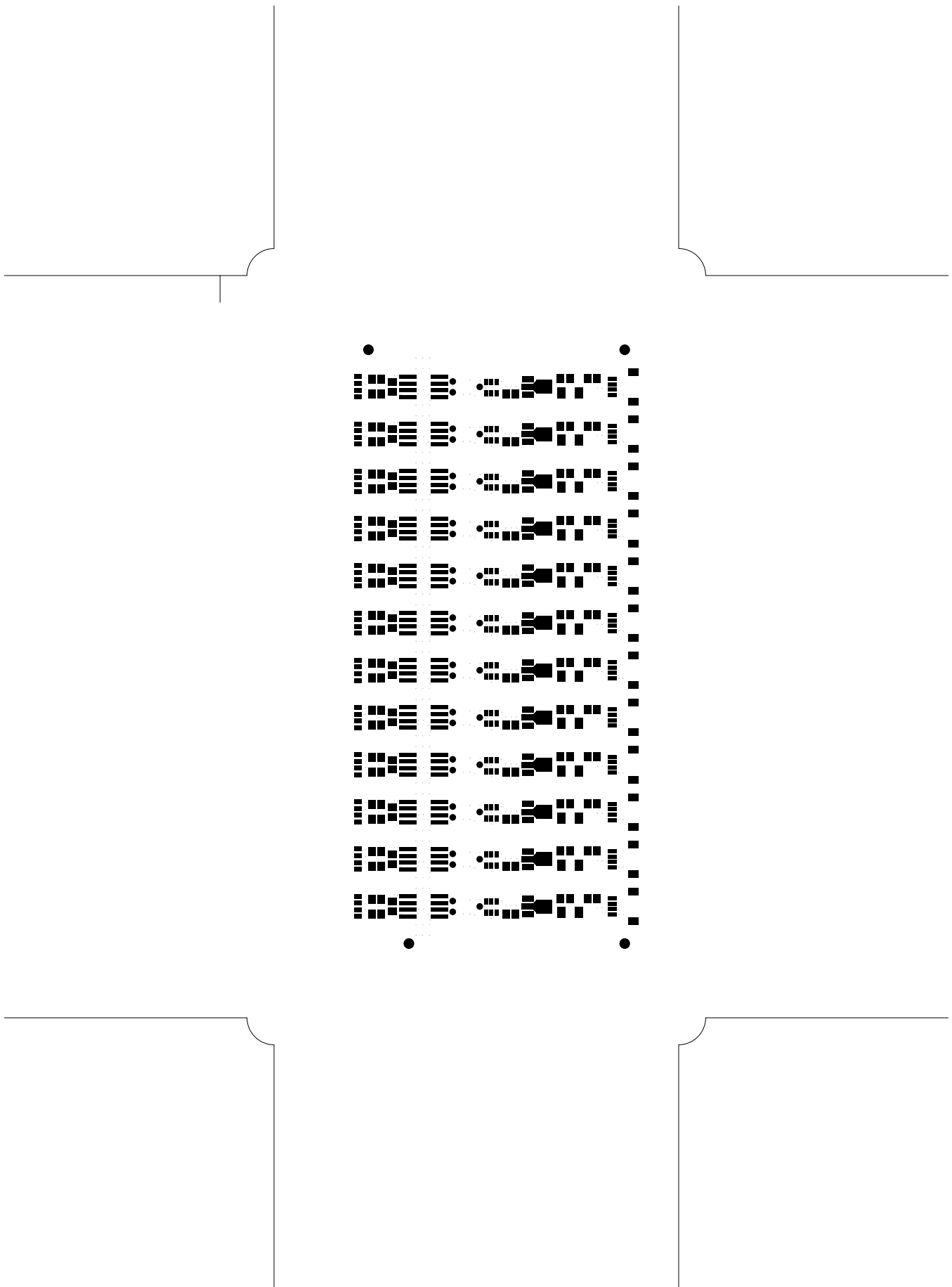


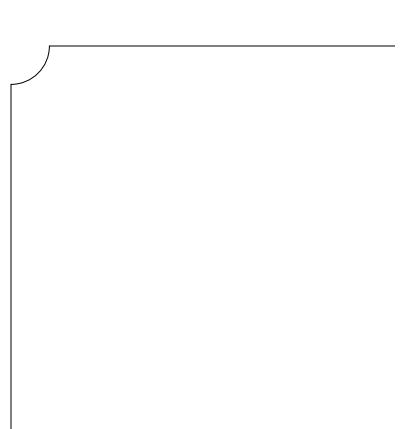
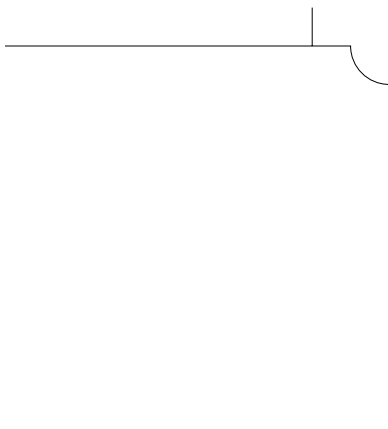
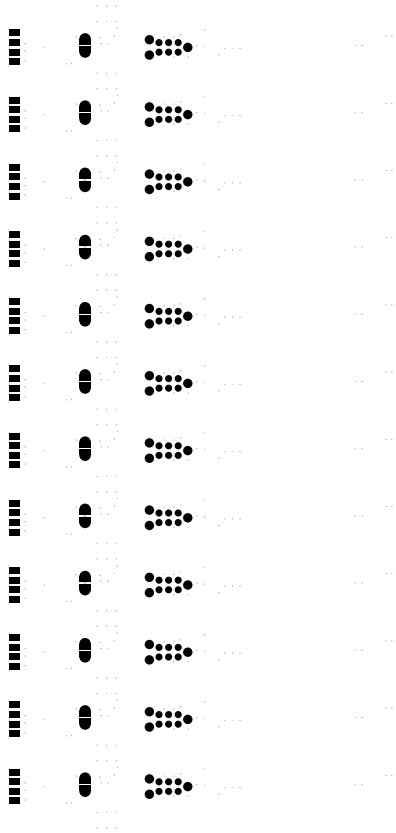
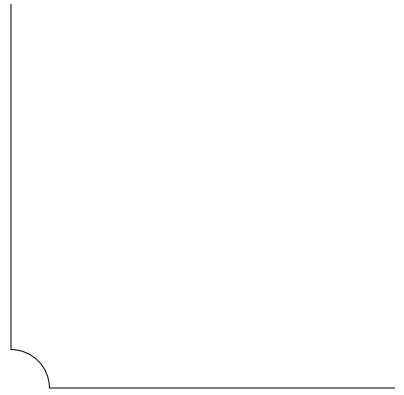
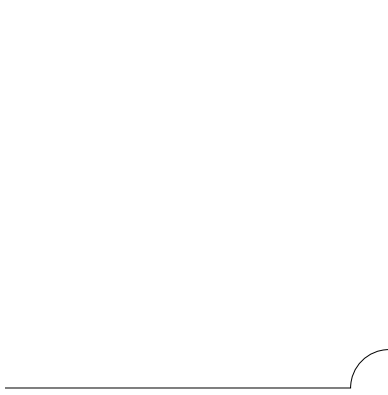
NO tabs, scoring, or breakaway rails on this edge.
Metal pads on this side should be coincident with board edge (<5mil).
Please do NOT pull back metal!

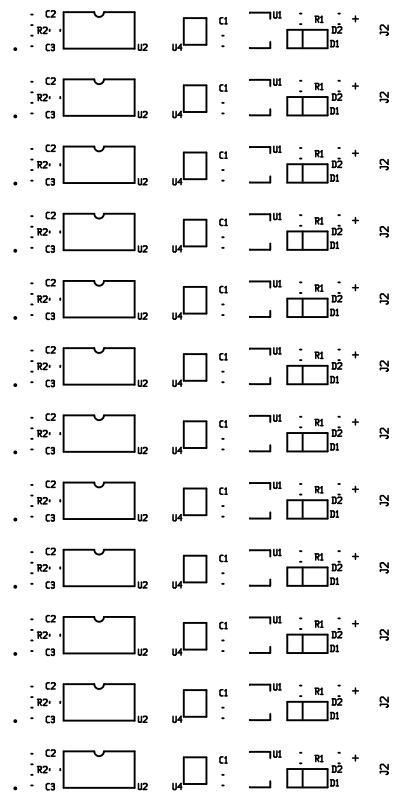




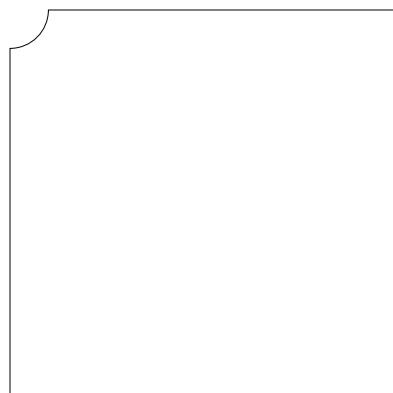
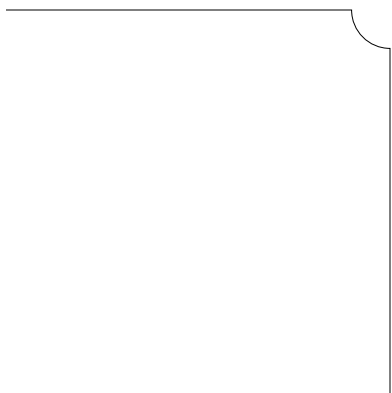
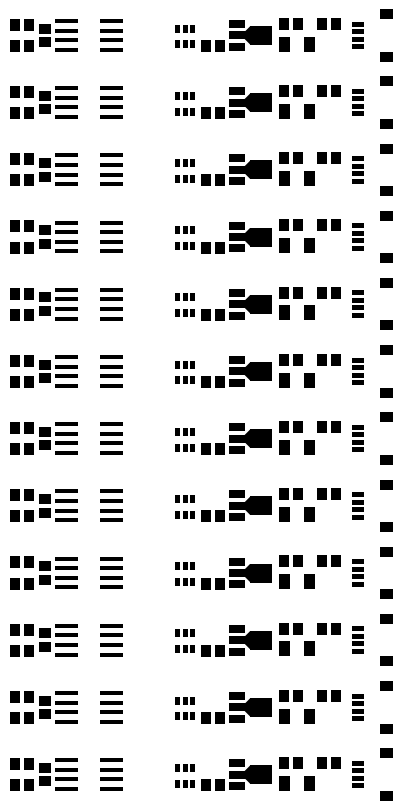
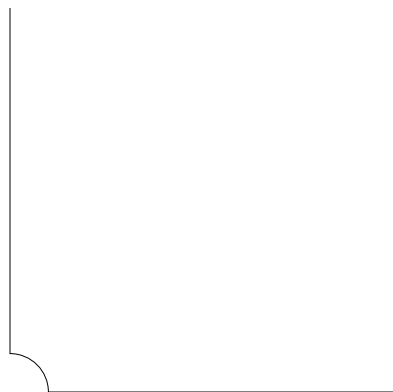
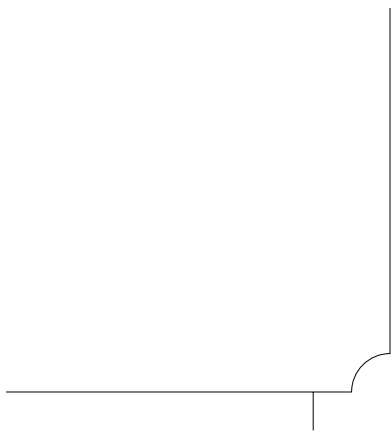


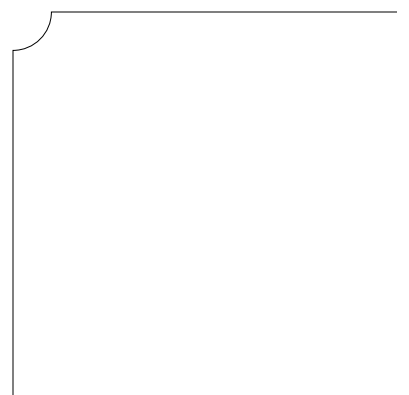
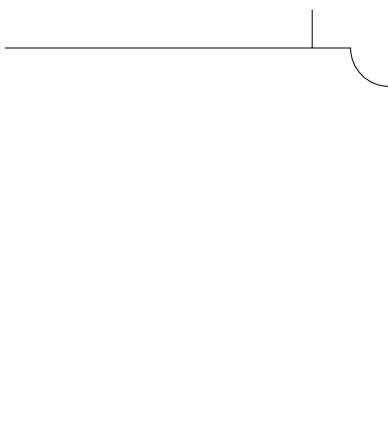
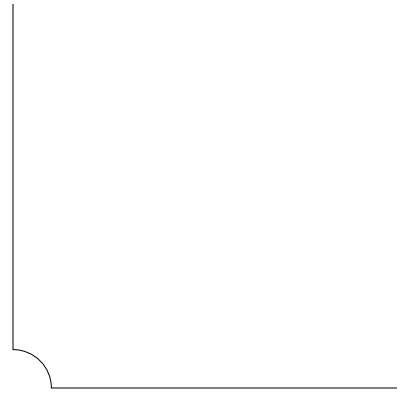
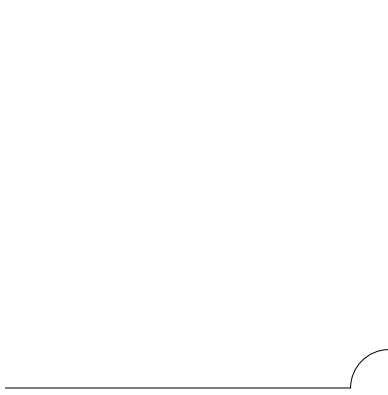




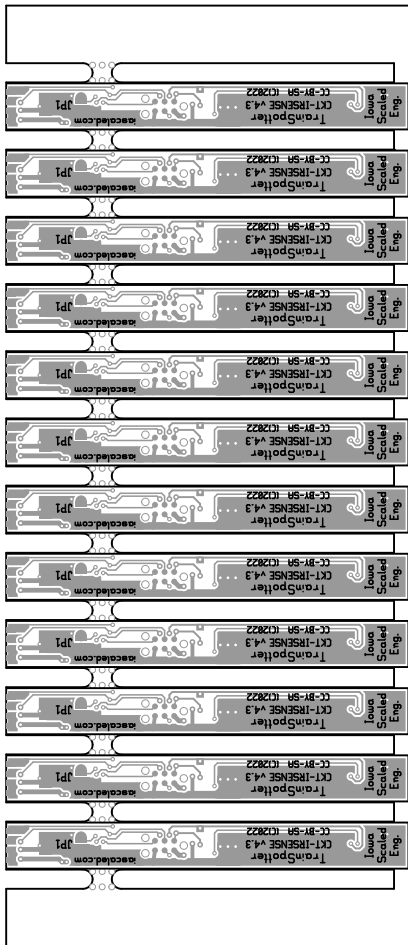


bottomsilk (mirrored), scale = 1:1.000
ckt-irsense-array.pcb





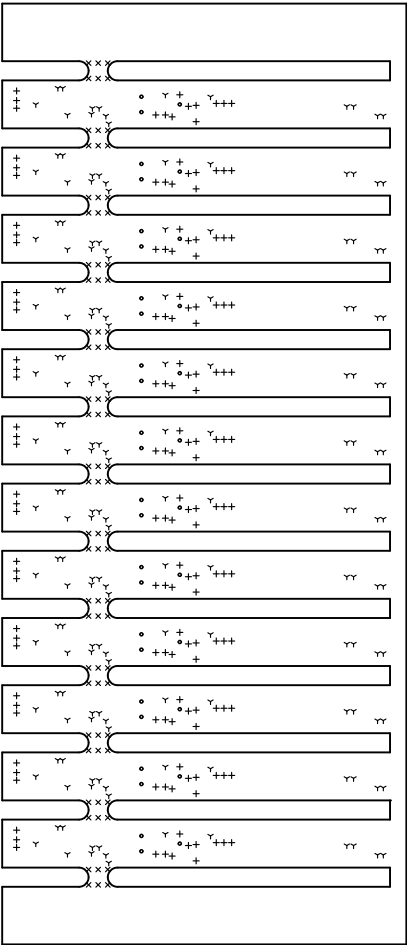
PLEASE DO NOT CUT BACK METAL!
Major bodge on this side showing no coincidence with pour edge (<2mil).
NO tape scoring or preformal tape on this edge.



There are 4 different drill sizes used in this layout, 450 holes total

Symbol	Diam. (Inch)	Count	Plated?	
Y	0.012	180	YES	Title: (unknown) - Fabrication Drawing
+	0.014	156	YES	Author: Michael Petersen
x	0.031	78	NO	Date: Thu Jun 22 03:03:34 2023 UTC
•	0.039	36	NO	

NO tabs, scoring, or breakaway rails on this edge.
Metal pads on this side should be coincident with board edge (<5mil).
Please do NOT pull back metal!



Board outline is the centerline of this path