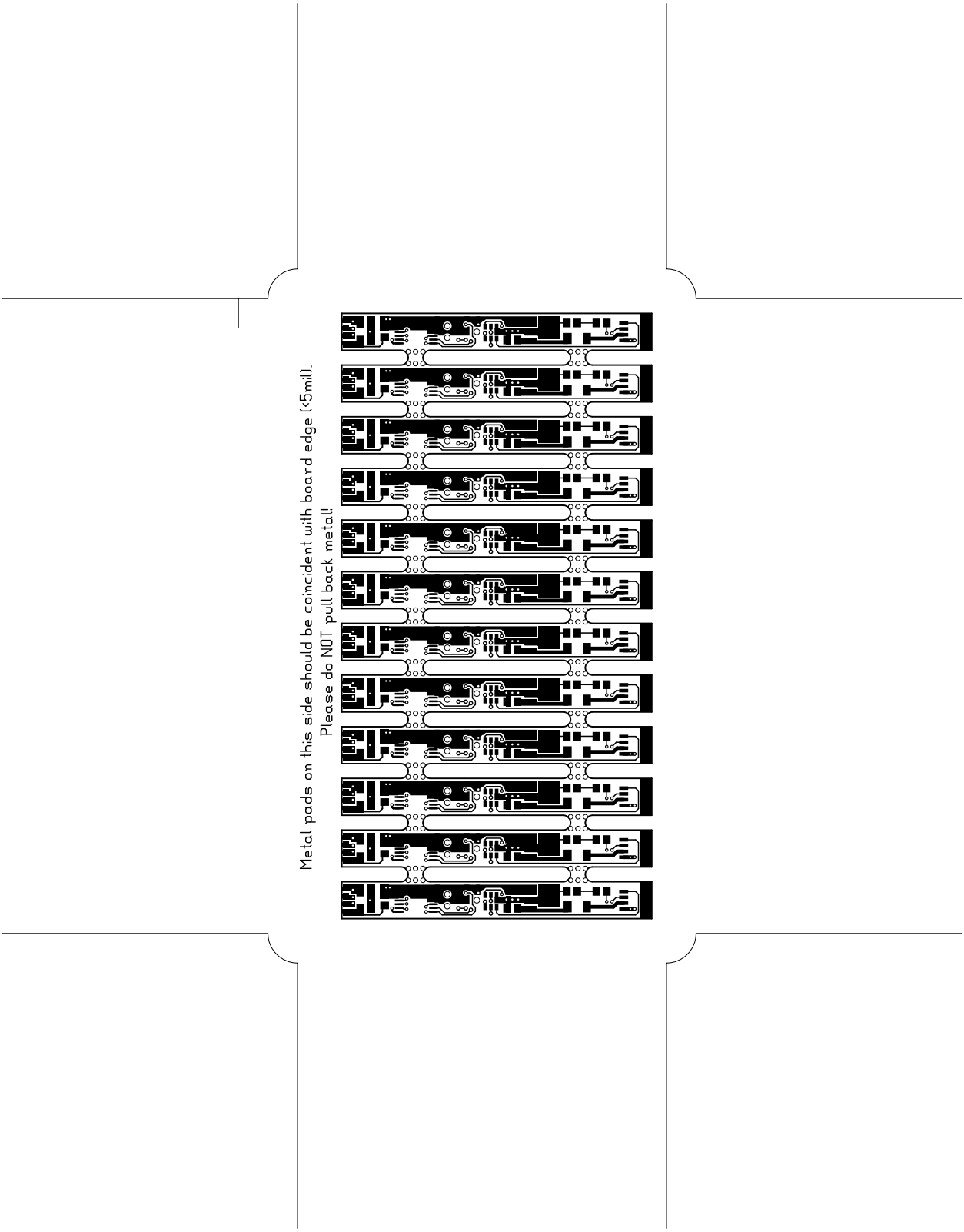
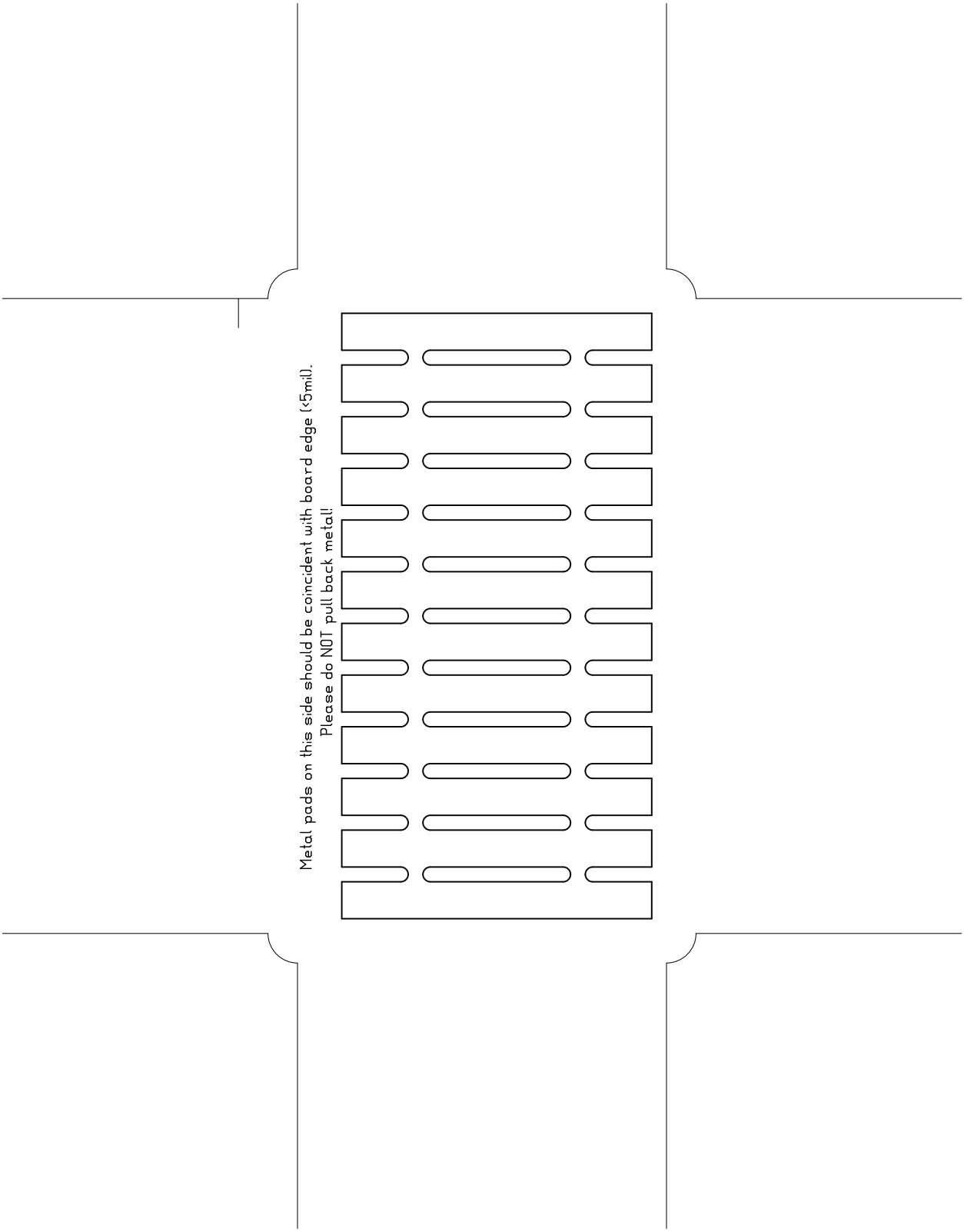
1. Table of Contents (This Page)
   Created on Thu Jul  2 15:16:04 2020
2. top
3. bottom
4. outline
5. plated-drill
6. unplated-drill
7. componentmask
8. soldermask
9. topsilk
10. bottomsilk
11. toppaste
12. bottompaste
13. topassembly
14. bottomassembly
15. fab

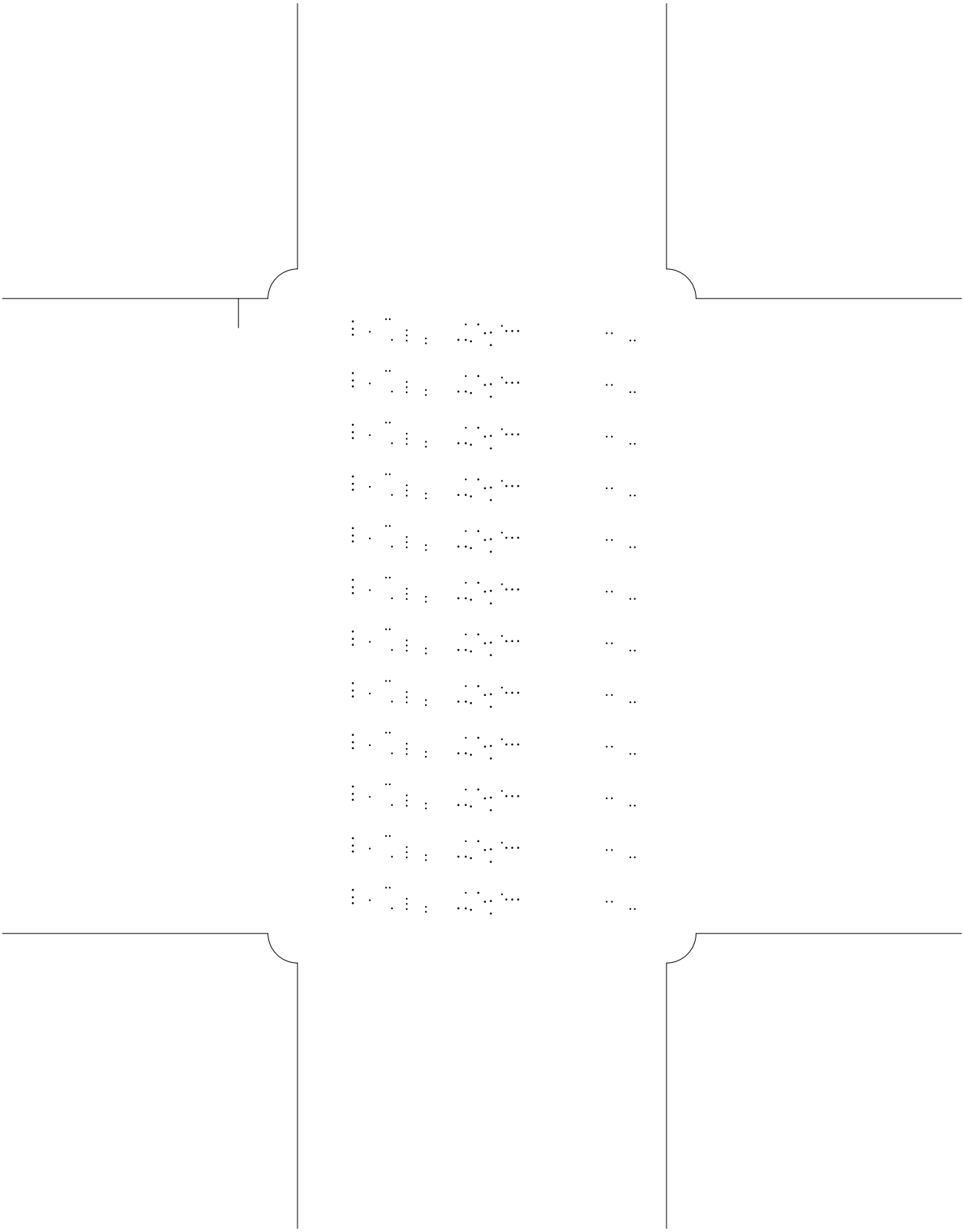



top, scale = 1:1.000
ckt-irsense-panel.pcb

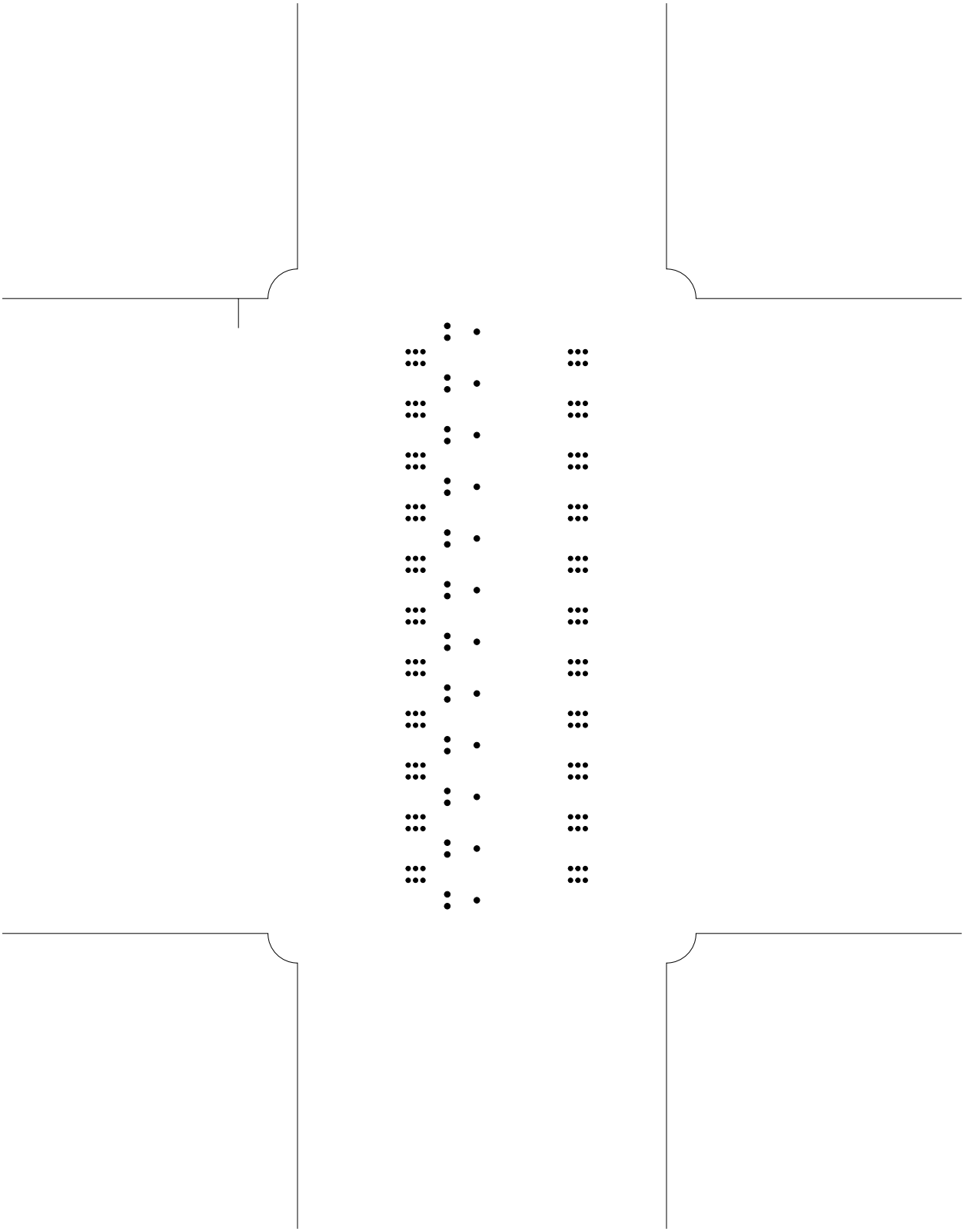
bottom (mirrored), scale = 1:1.000
ckt-irsense-panel.pcb



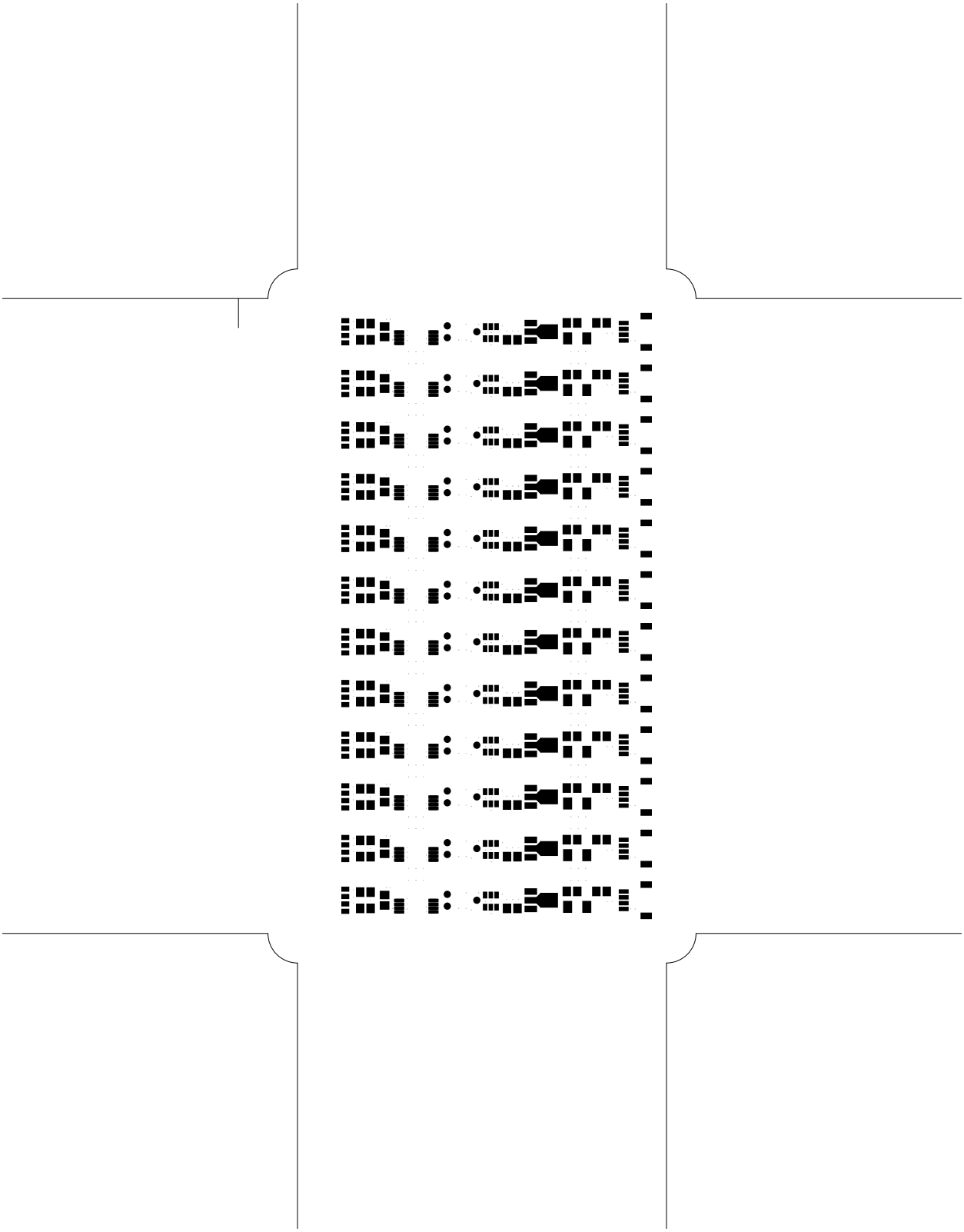

outline, scale = 1:1.000
ckt-irsense-panel.pcb



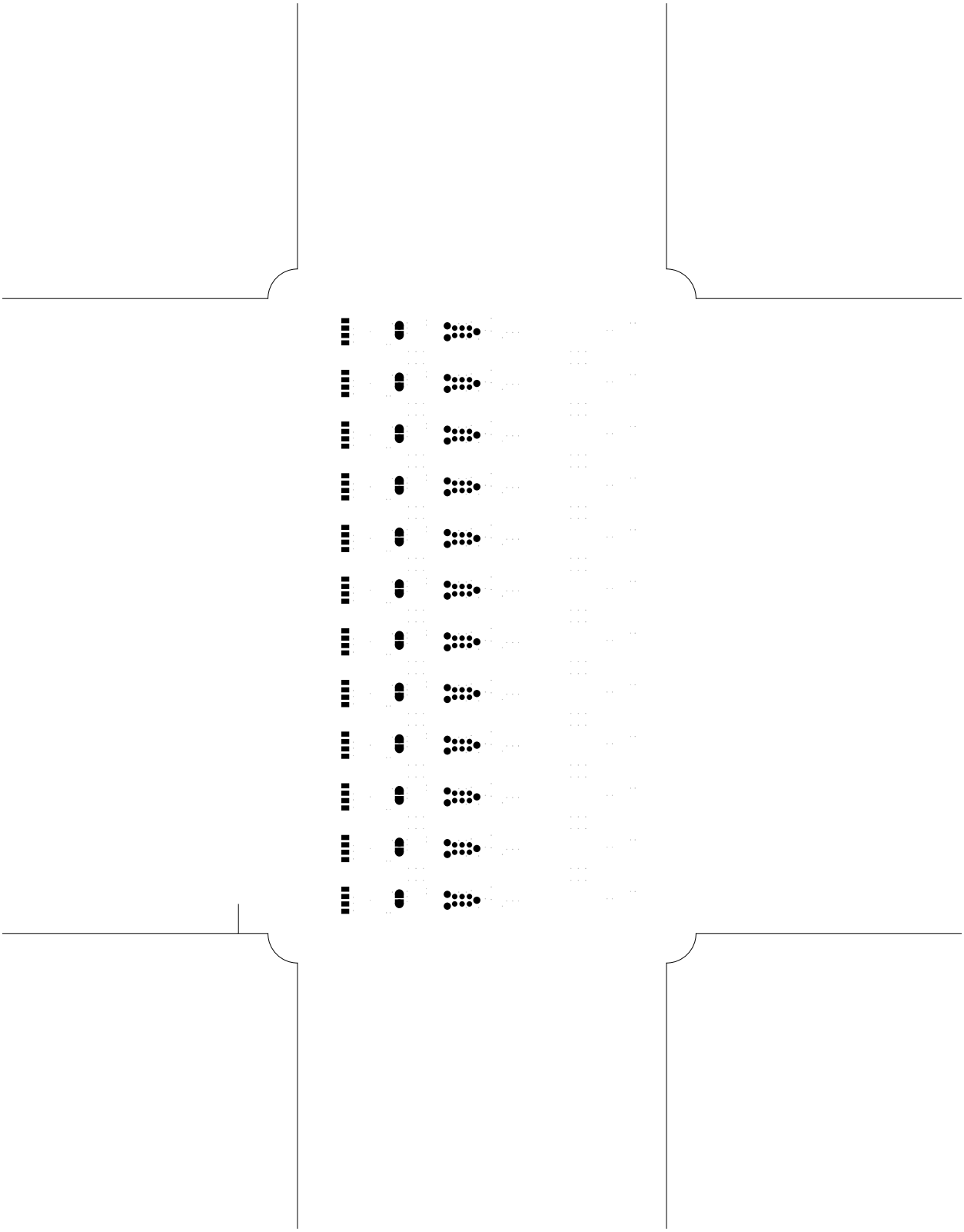
plated-drill, scale = 1:1.000
ckt-irsense-panel.pcb



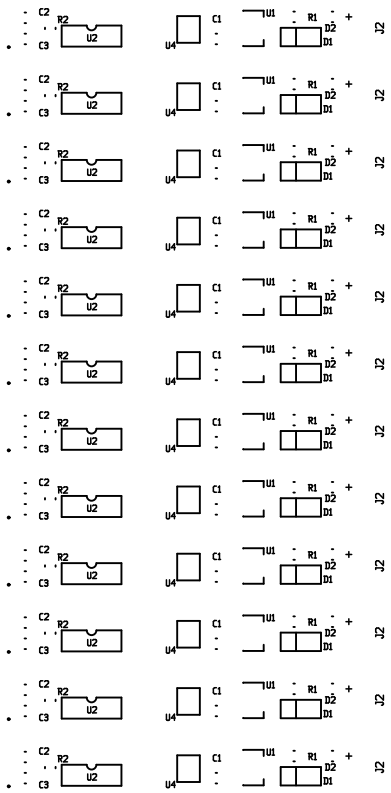
unplated-drill, scale = 1:1.000
ckt-irsense-panel.pcb



topmask, scale = 1:1.000
ckt-irsense-panel.pcb

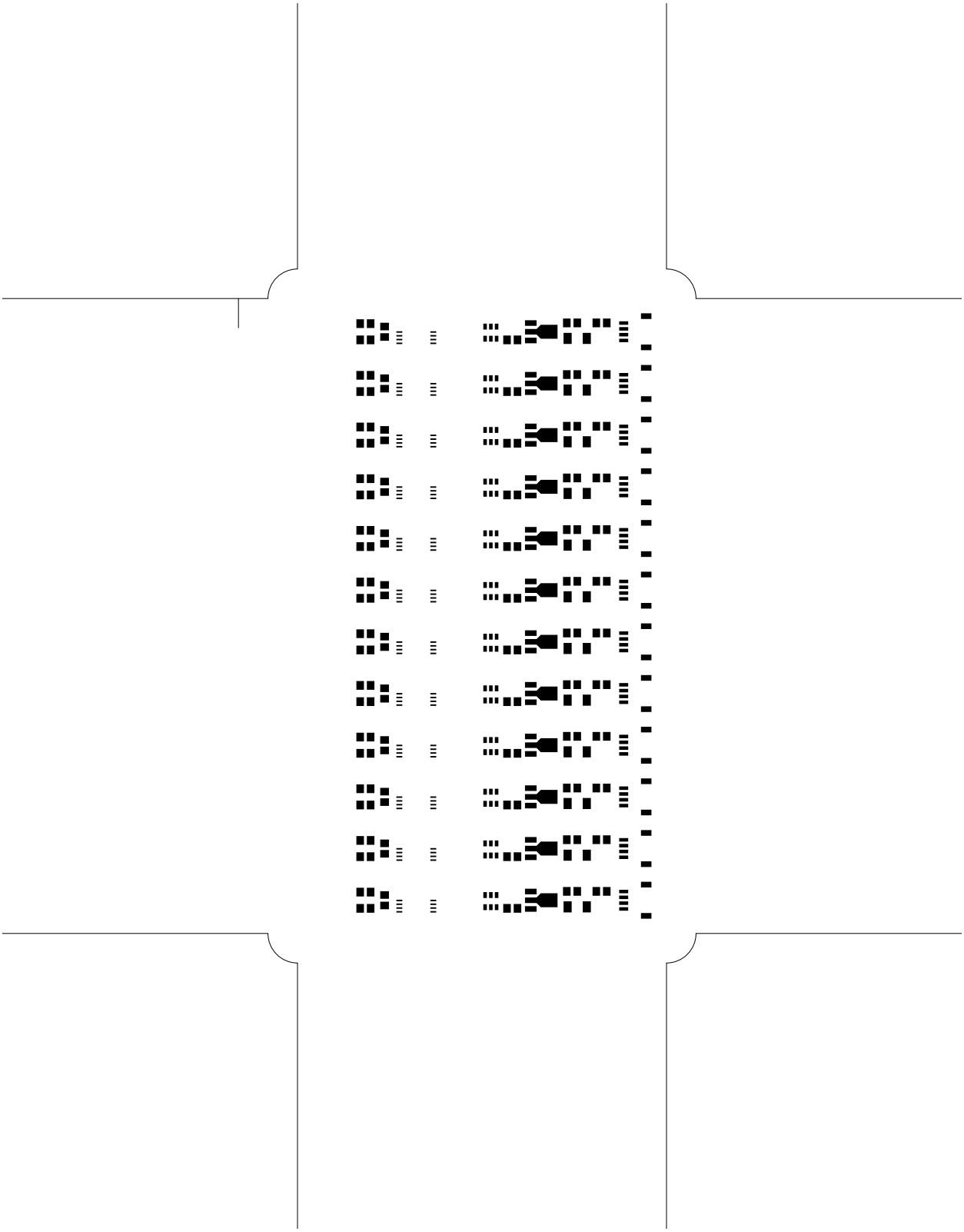


bottommask (mirrored), scale = 1:1.000
ckt-irsense-panel.pcb

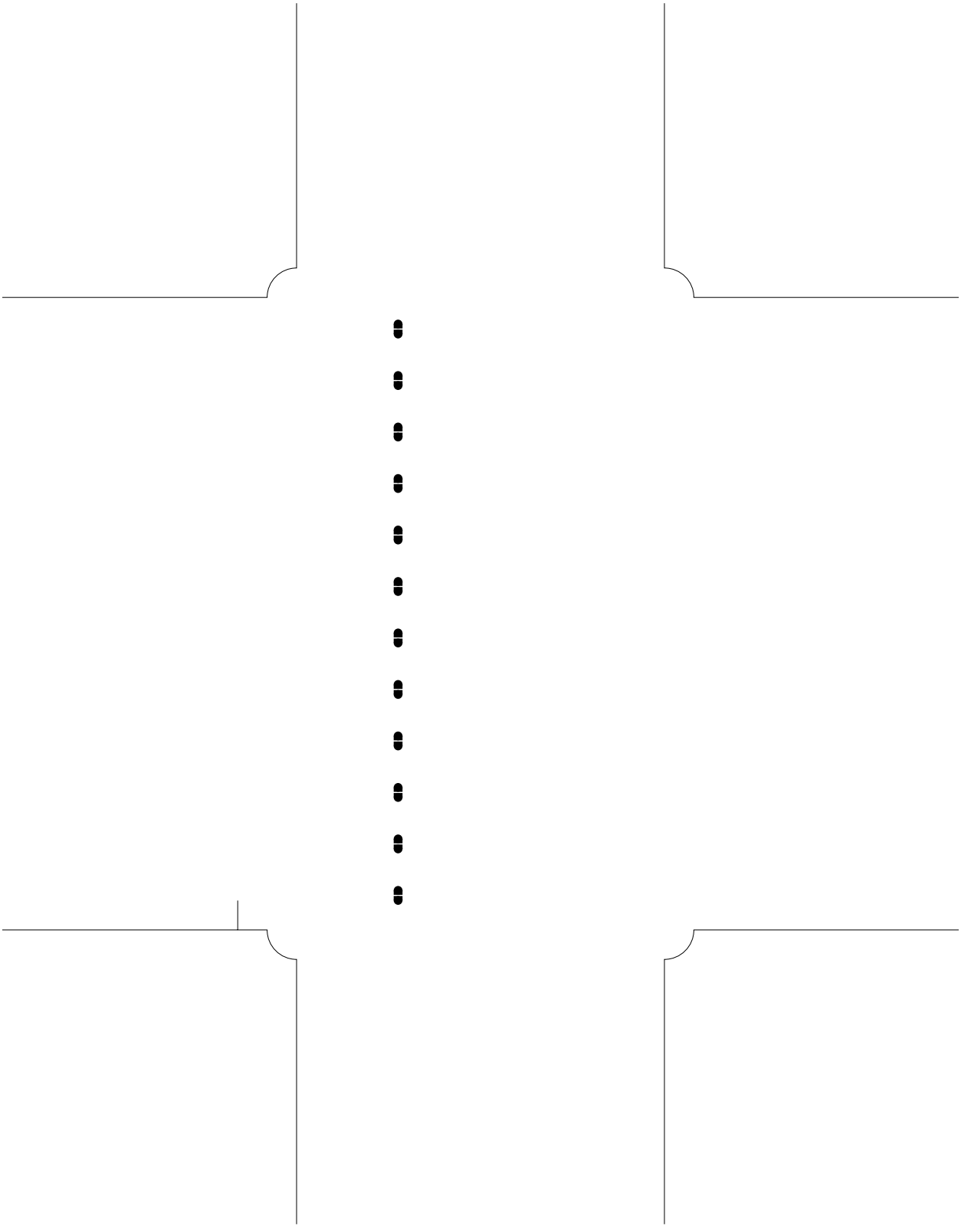



topsilk, scale = 1:1.000
ckt-irsense-panel.pcb

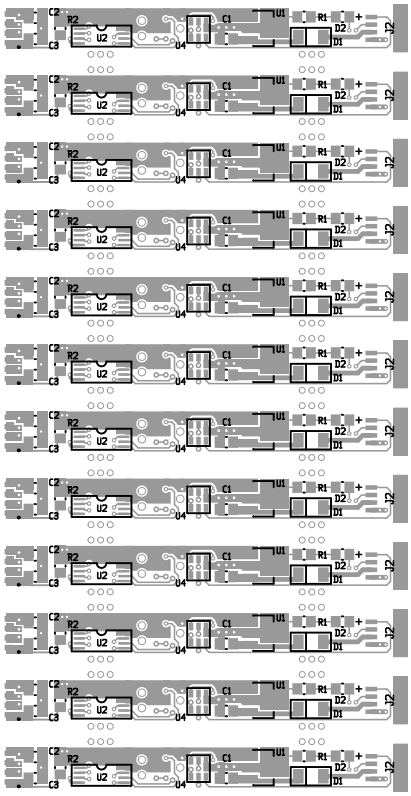
bottomsilk (mirrored), scale = 1:1.000
ckt-irsense-panel.pcb



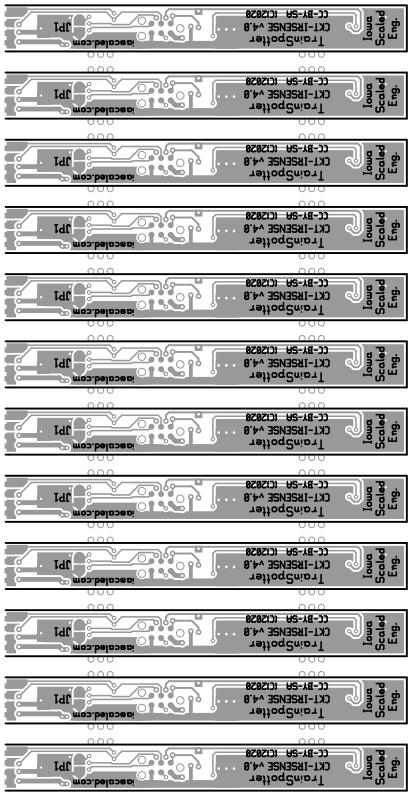
toppaste, scale = 1:1.000
ckt-irsense-panel.pcb



bottompaste (mirrored), scale = 1:1.000
ckt-irsense-panel.pcb




topassembly, scale = 1:1.000
ckt-irsense-panel.pcb

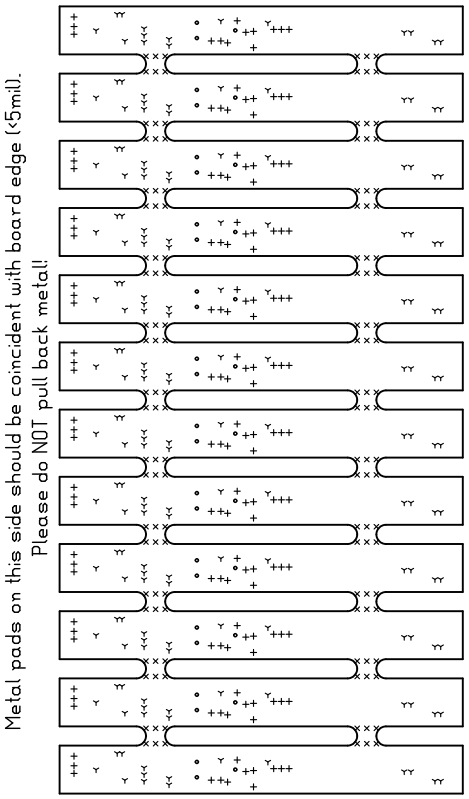



bottomassembly (mirrored), scale = 1:1.000
ckt-irsense-panel.pcb

There are 4 different drill sizes used in this layout, 504 holes total

| Symbol | Diam. (Inch) | Count | Plated? |
|---|---|---|---|
| ˅ | 0.012 | 180 | YES |
| + | 0.014 | 156 | YES |
| × | 0.031 | 132 | NO |
| ∘ | 0.039 | 36 | NO |

Title: (unknown) - Fabrication Drawing
Author: Michael Petersen
Date: Thu 02 Jul 2020 09:16:04 PM GMT UTC



Metal pads on this side should be coincident with board edge (<5mil).
Please do NOT pull back metal!

Board outline is the centerline of this path

fab, scale = 1:1.000
ckt-irsense-panel.pcb