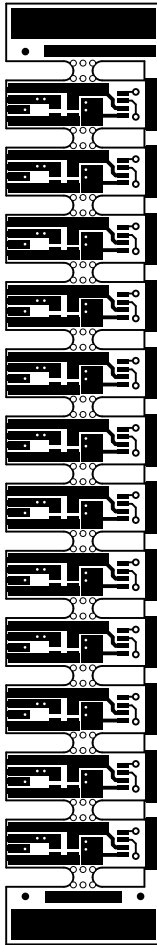


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Created on Sun Jan 21 17:33:21 2024

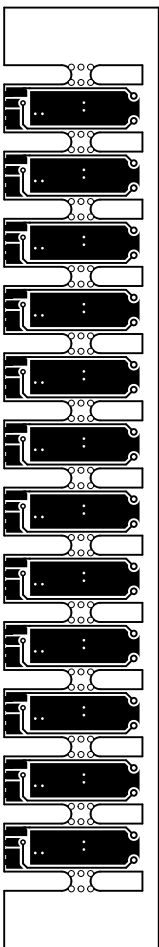
2. top
3. bottom
4. outline
5. plated-drill
6. unplated-drill
7. componentmask
8. soldermask
9. topsilk
10. bottomsilk
11. toppaste
12. topassembly
13. bottomassembly
14. fab

NO tabs, scoring, or breakaway rails on this edge.
Metal pads on this side should be coincident with board edge (<5mil).
Please do NOT pull back metal!

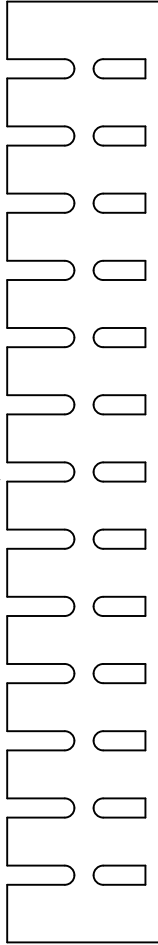


Tabs should be solid on this edge. No holes.

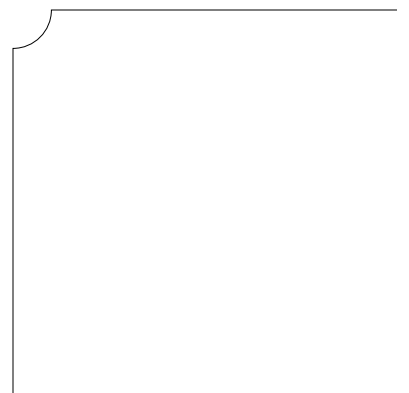
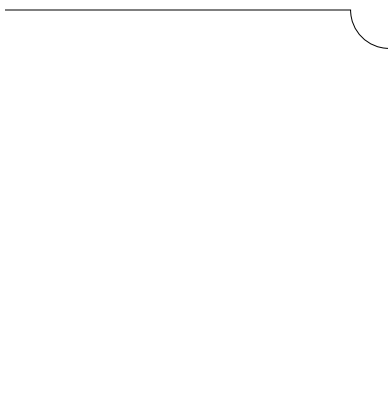
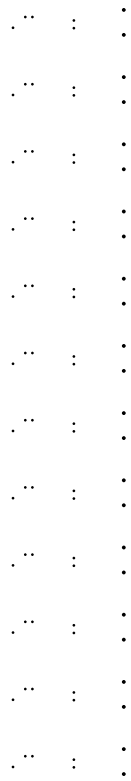
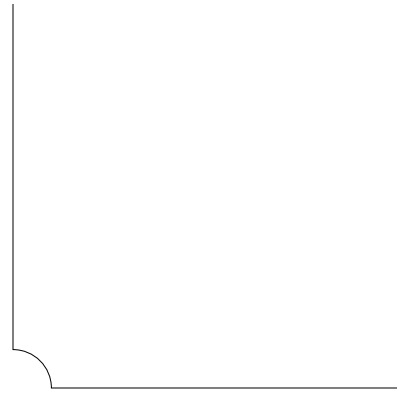
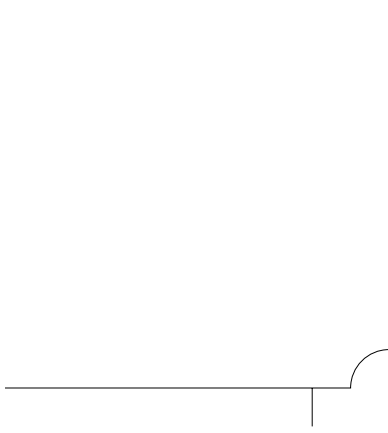
Top edge only. No holes.
Please do NOT drill back metal!
Metal bords on this side should be coincident with pour edge (<2mil).
No tape scoring or predrilled holes on this edge.

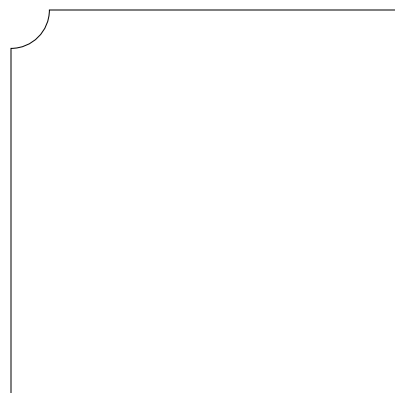
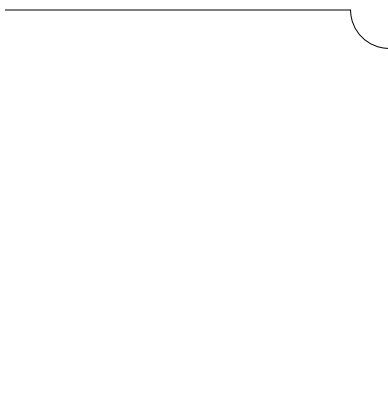
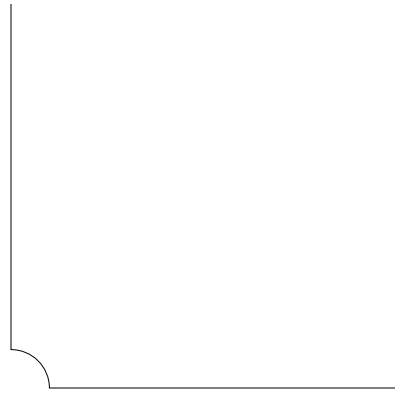
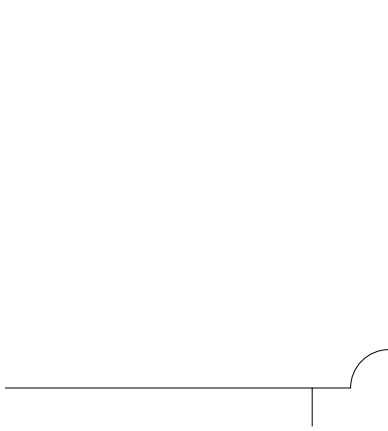


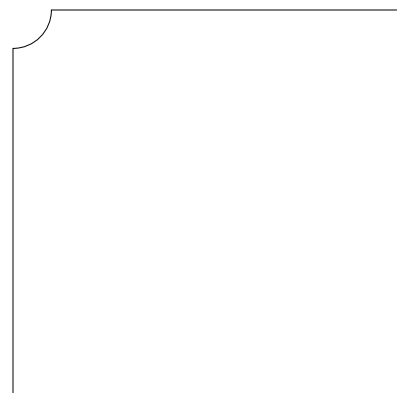
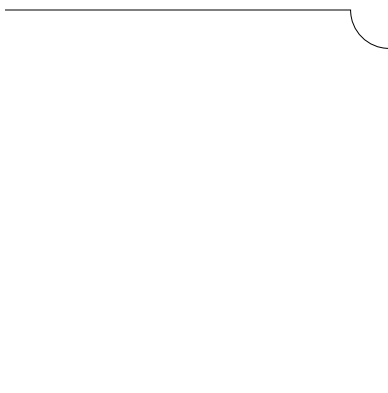
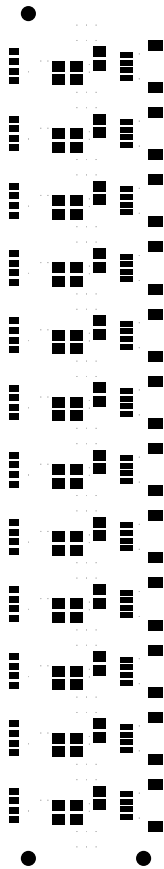
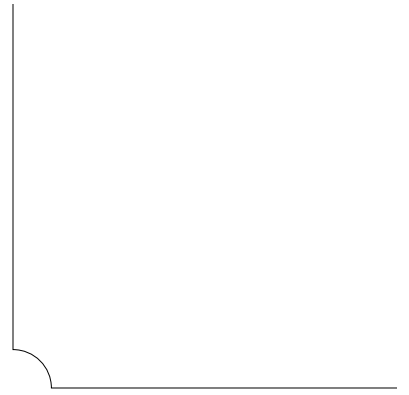
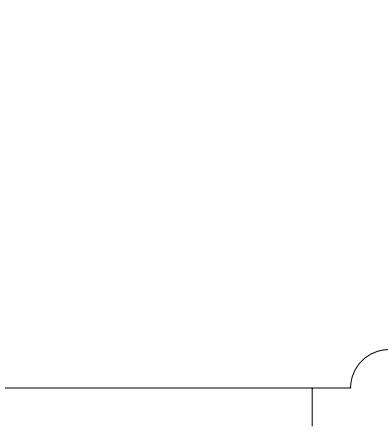
NO tabs, scoring, or breakaway rails on this edge.
Metal pads on this side should be coincident with board edge (<5mil).
Please do NOT pull back metal!

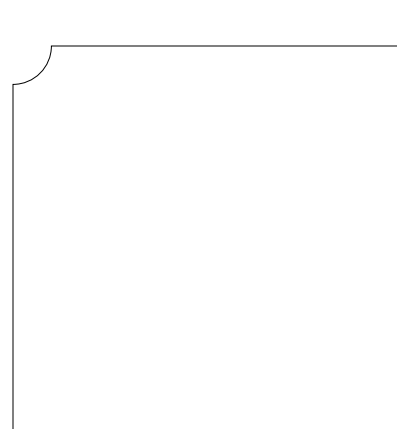
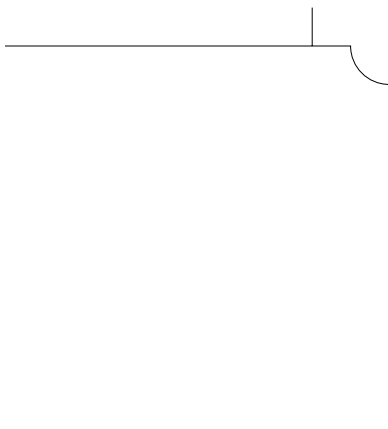
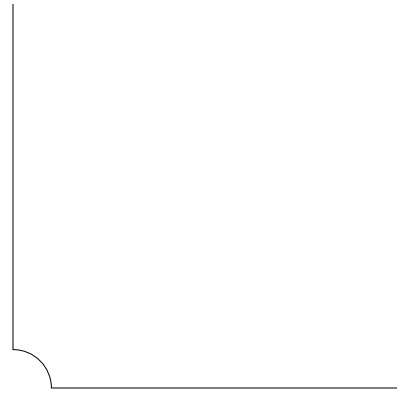
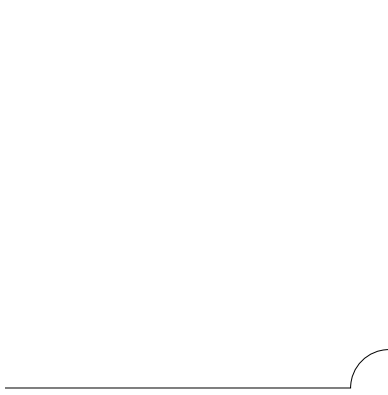


Tabs should be solid on this edge. No holes.

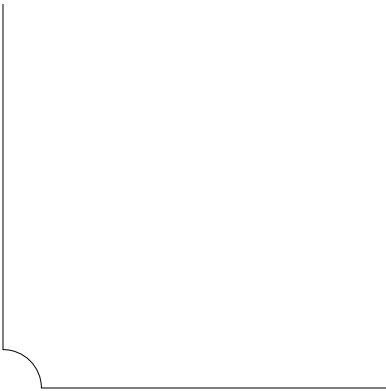
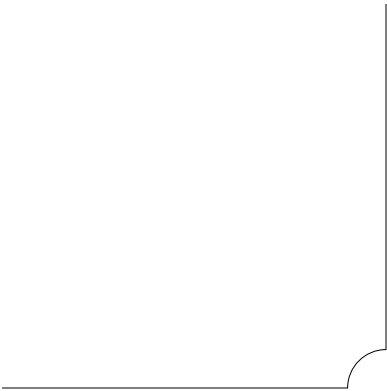




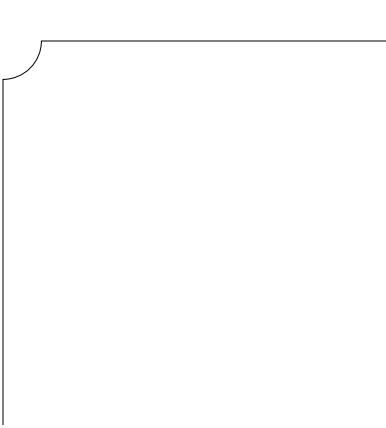
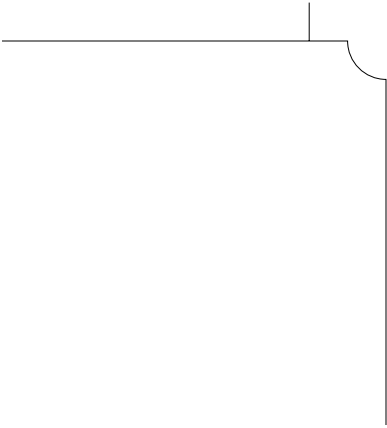


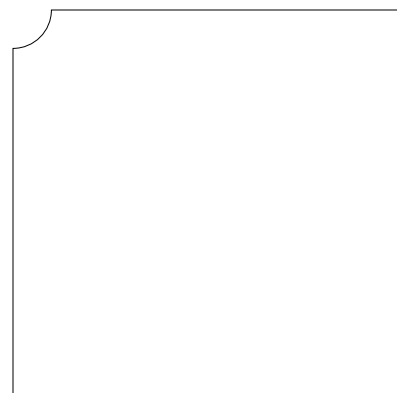
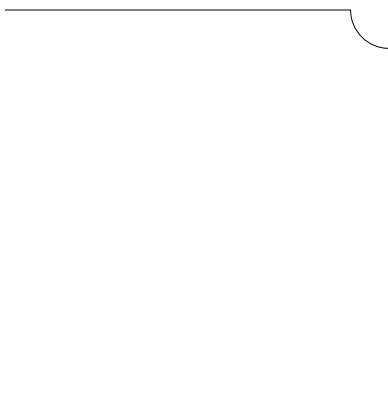
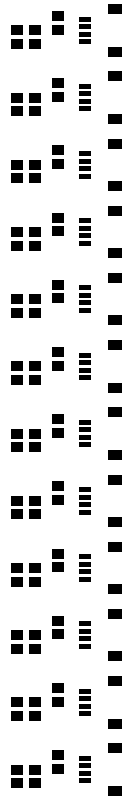
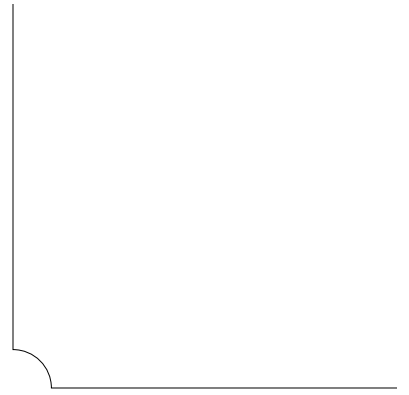
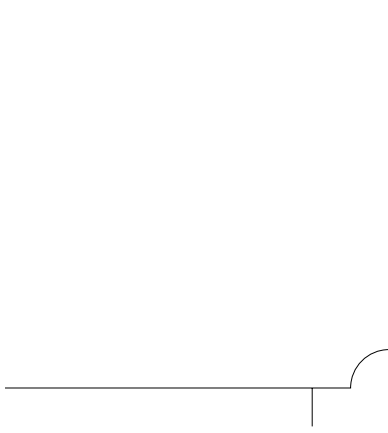


bottommask (mirrored), scale = 1:1.000
i2c-irsense-array.pcb

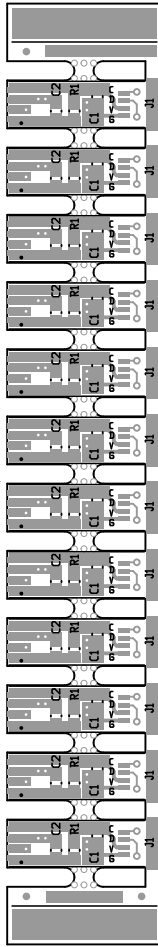


IDWA SCALED ENG
I2C-IRSENSE v2.1
IDWA SCALED ENG
I2C-IRSENSE v2.1
IDWA SCALED ENG
I2C-IRSENSE v2.1
IDWA SCALED ENG
I2C-IRSENSE v2.1
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I2C-IRSENSE v2.1
IDWA SCALED ENG
I2C-IRSENSE v2.1



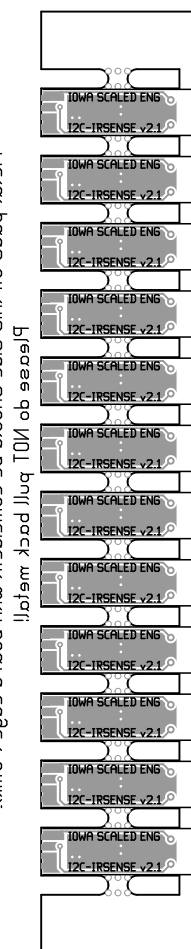


NO tabs, scoring, or breakaway rails on this edge.
Metal pads on this side should be coincident with board edge (<5mil).
Please do NOT pull back metal!



Tabs should be solid on this edge. No holes.

Major bugs on this side should be coincident with power edge (<2mil).
No tape scoring or preformal route on this edge.



Top edge of board on this edge. No holes.

There are 3 different drill sizes used in this layout, 162 holes total

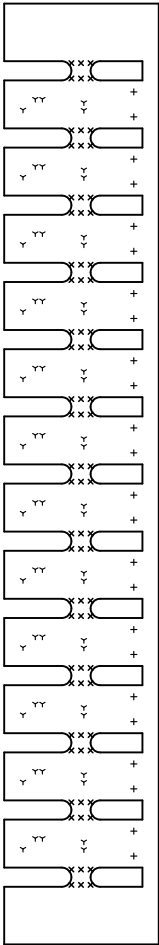
Symbol	Diam. (Inch)	Count	Plated?
Y	0.014	60	YES
+	0.020	24	YES
x	0.031	78	NO

Title: (unknown) - Fabrication Drawing

Author: Michael Petersen

Date: Mon Jan 22 00:33:21 2024 UTC

NO tabs, scoring, or breakaway rails on this edge.
Metal pads on this side should be coincident with board edge (<5mil).
Please do NOT pull back metal!



Tabs should be solid on this edge. No holes.

Board outline is the centerline of this path