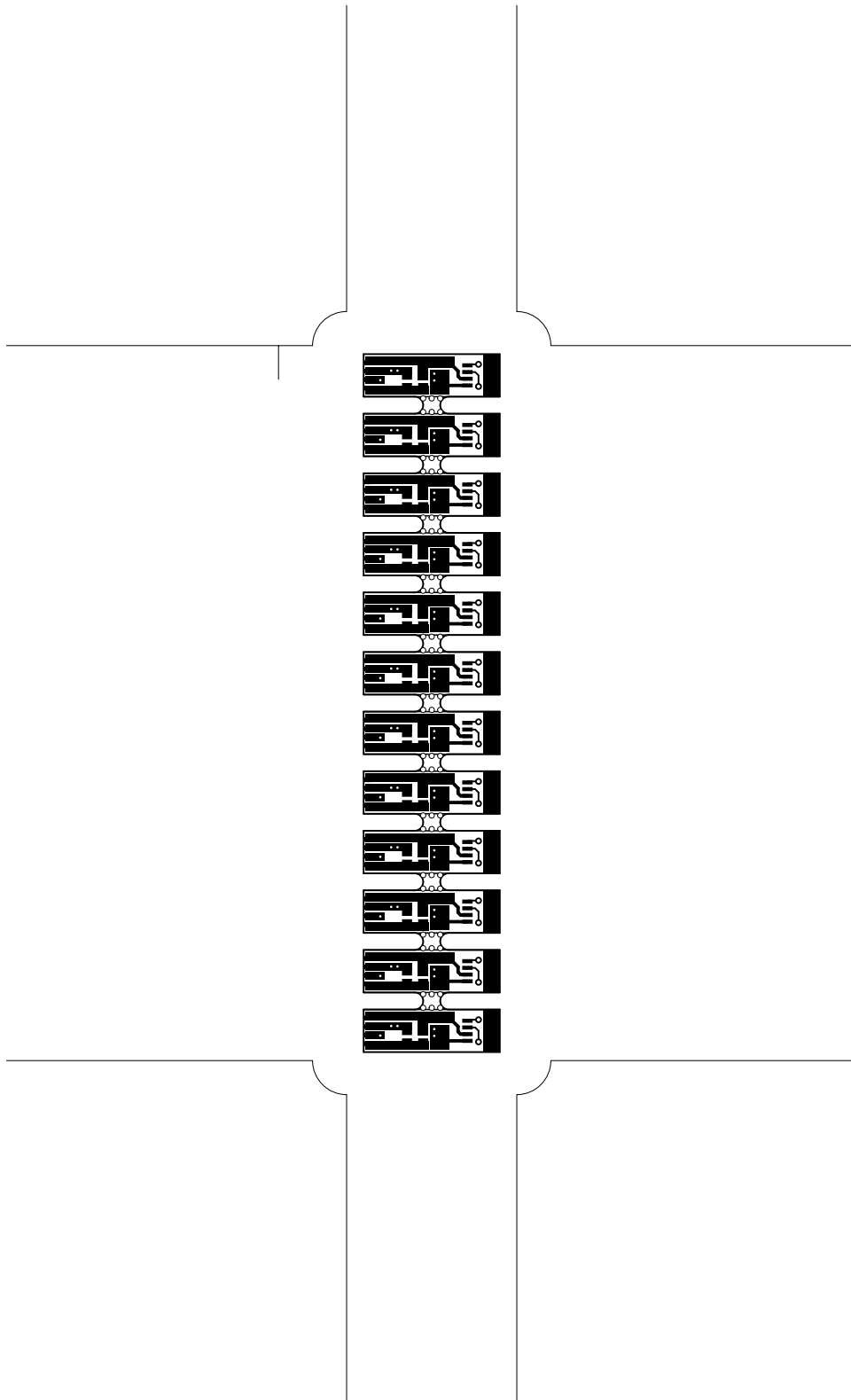
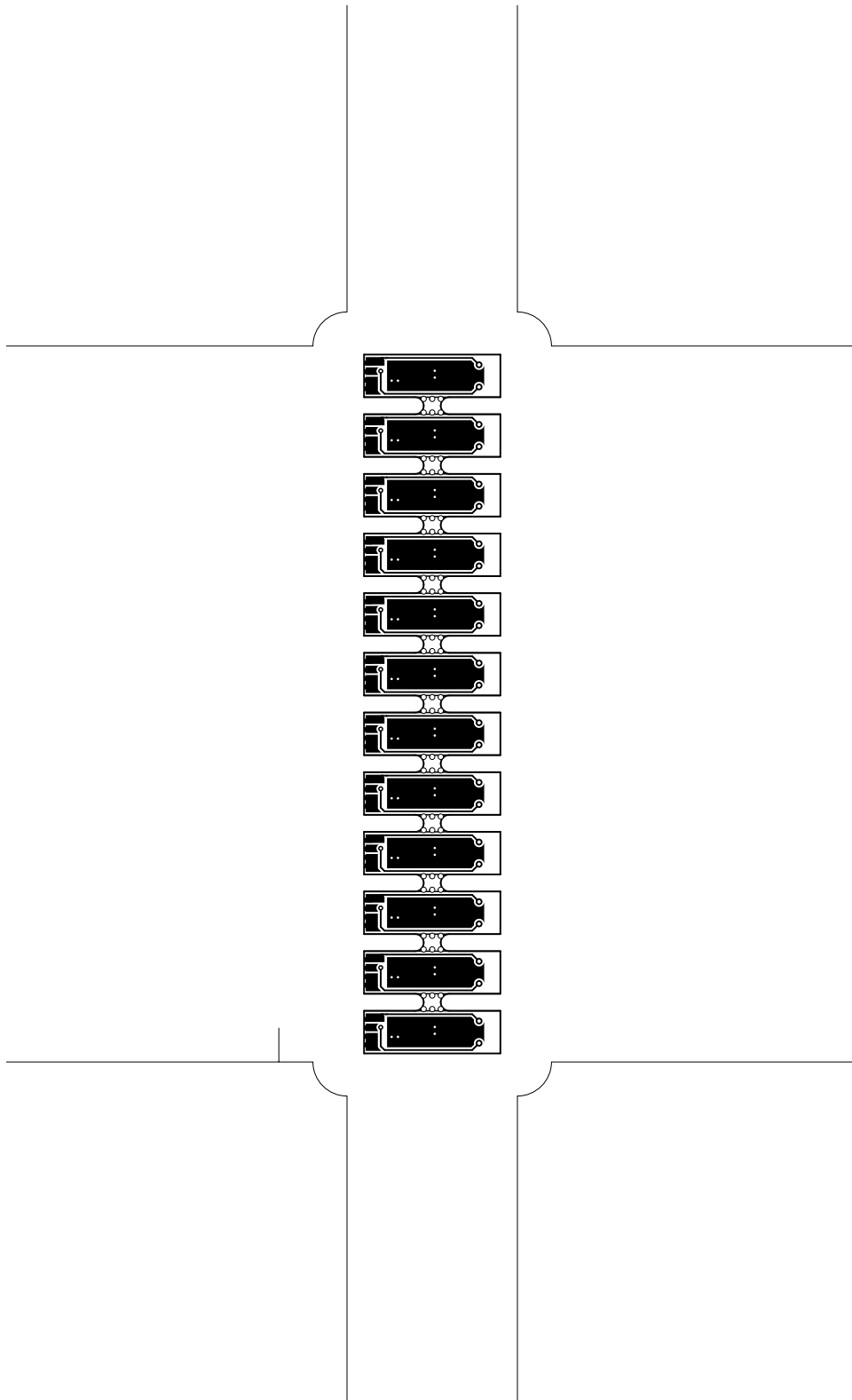


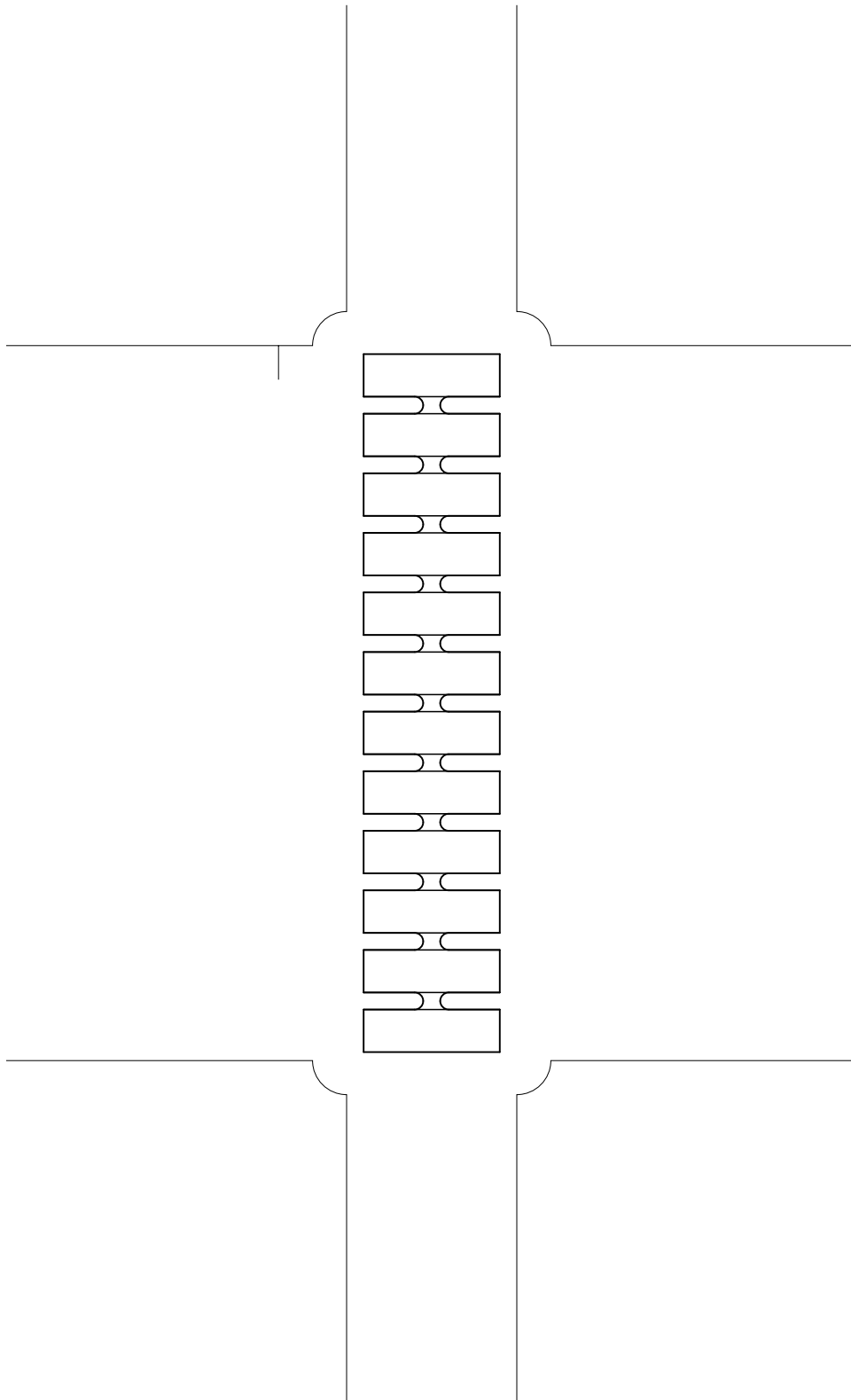
1. Table of Contents (This Page)

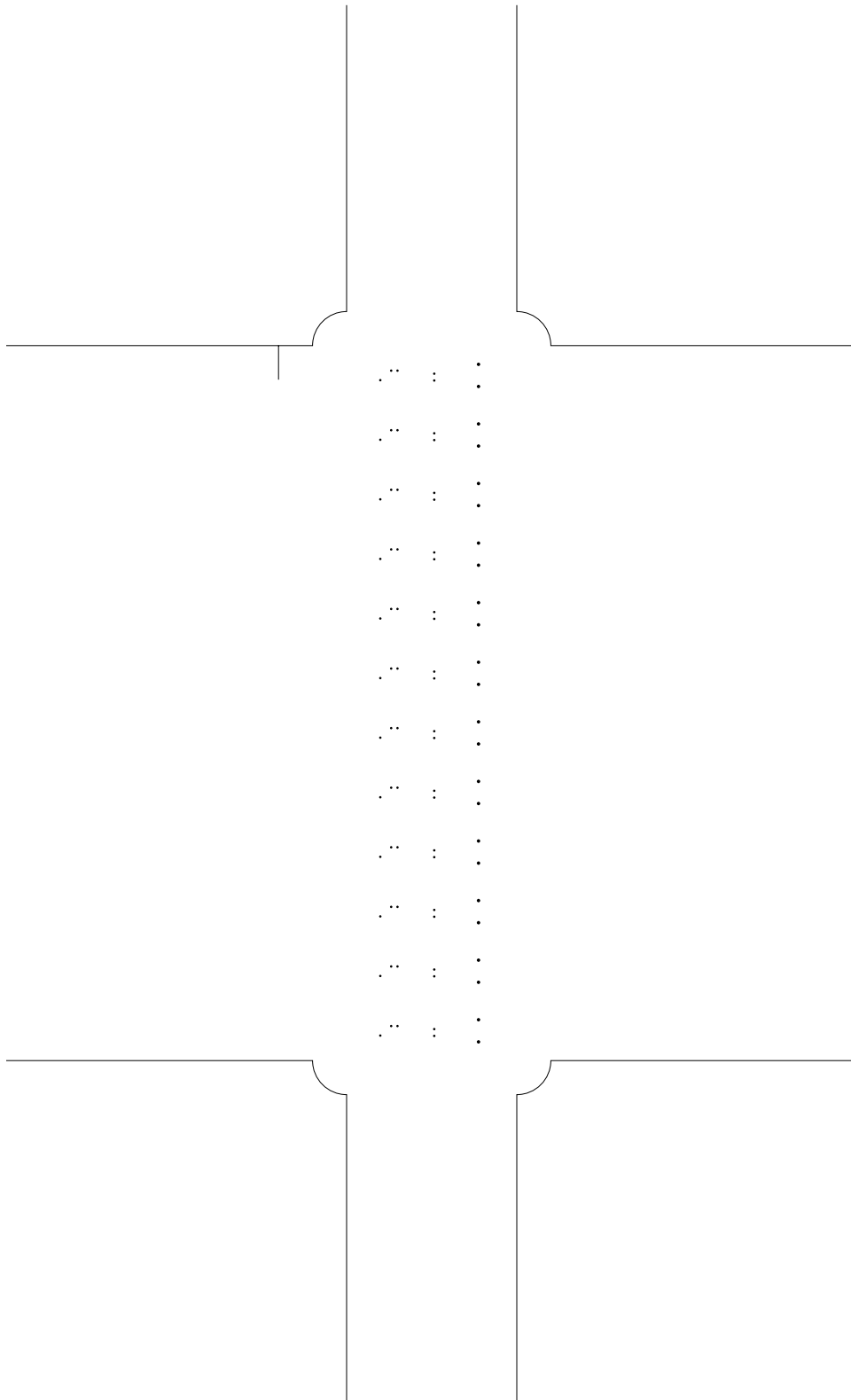
Created on Sun Jan 28 18:15:10 2018

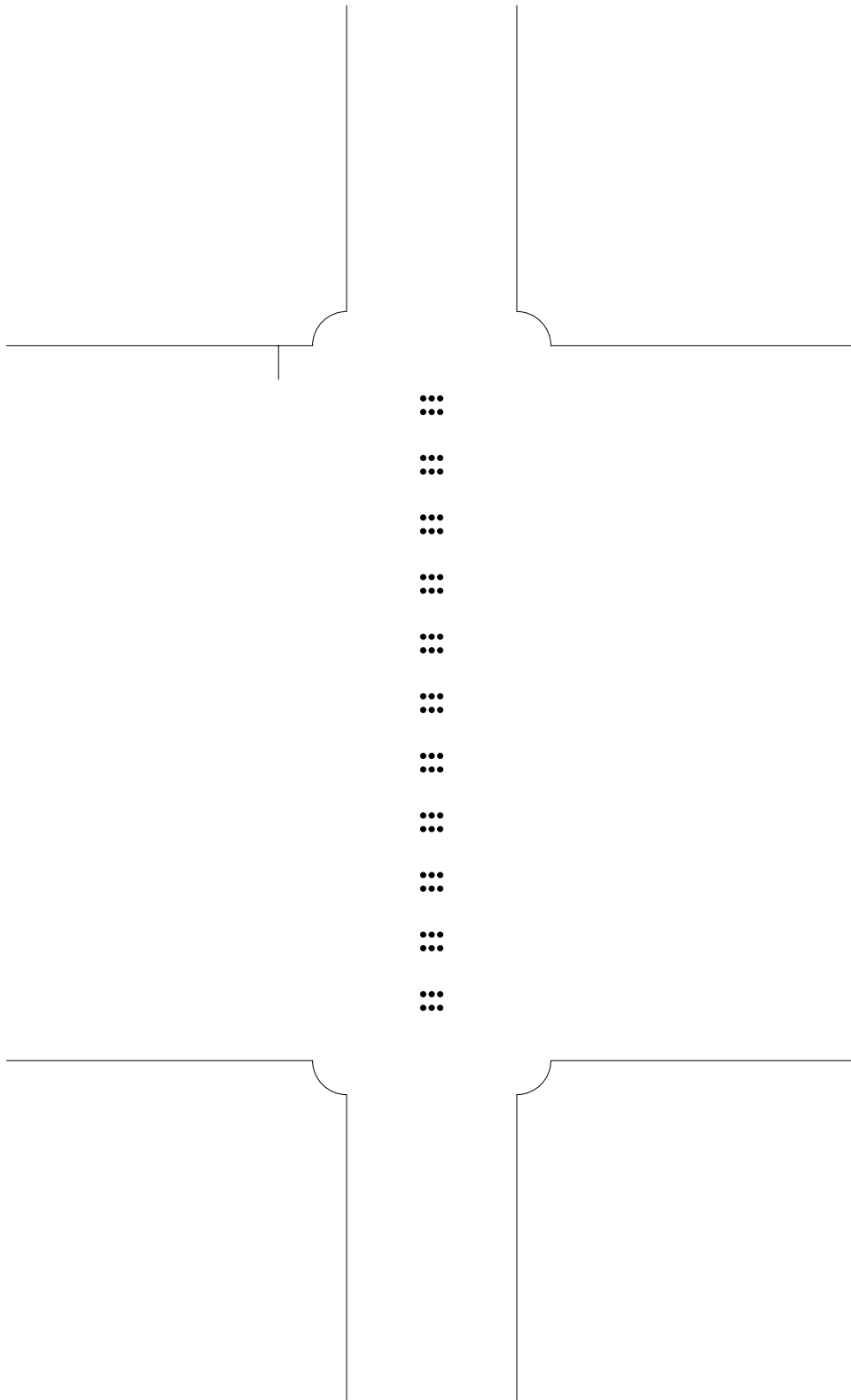
- 2. top
- 3. bottom
- 4. outline
- 5. plated-drill
- 6. unplated-drill
- 7. componentmask
- 8. soldermask
- 9. topsilk
- 10. bottomsilk
- 11. toppaste
- 12. topassembly
- 13. bottomassembly
- 14. fab

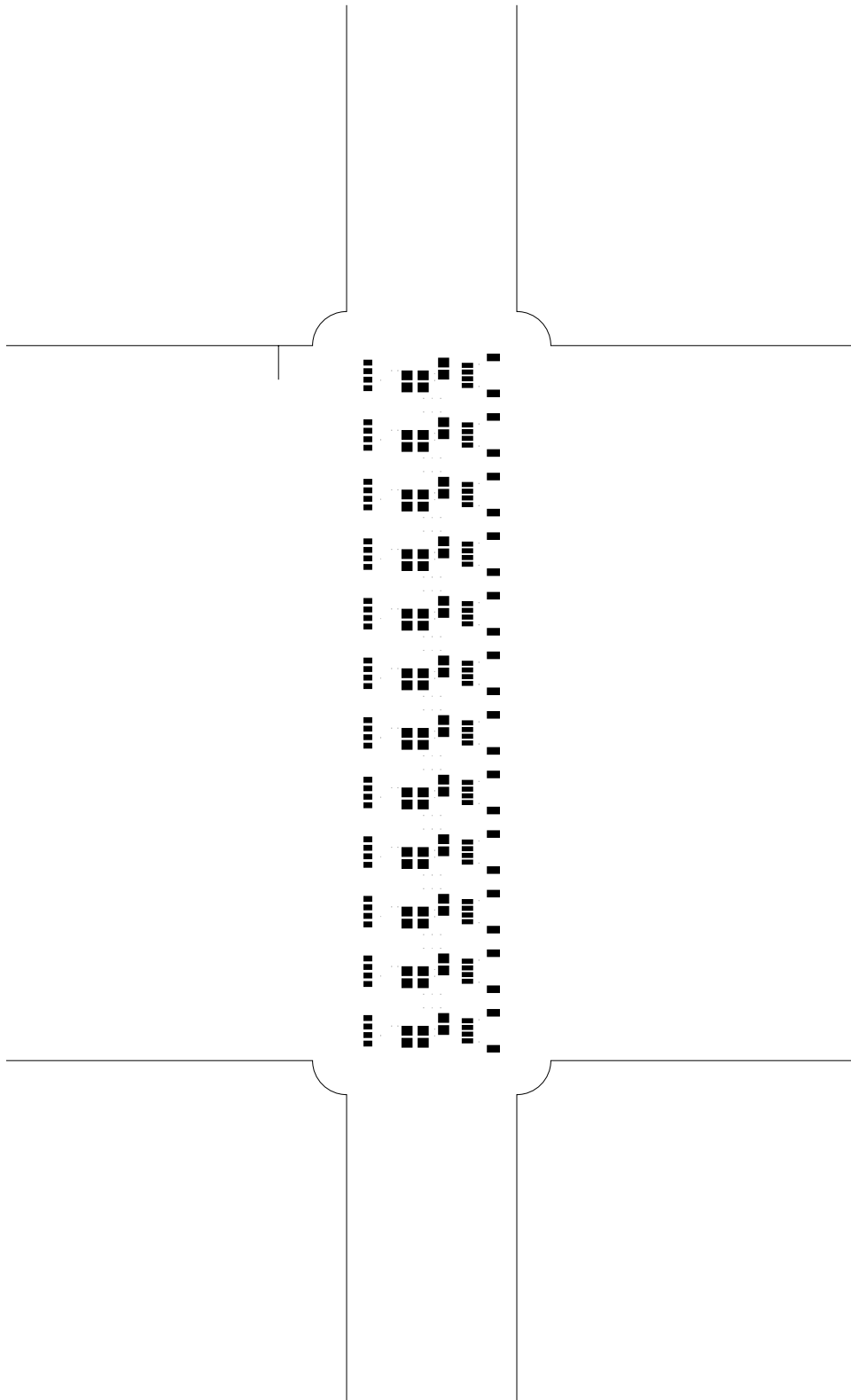


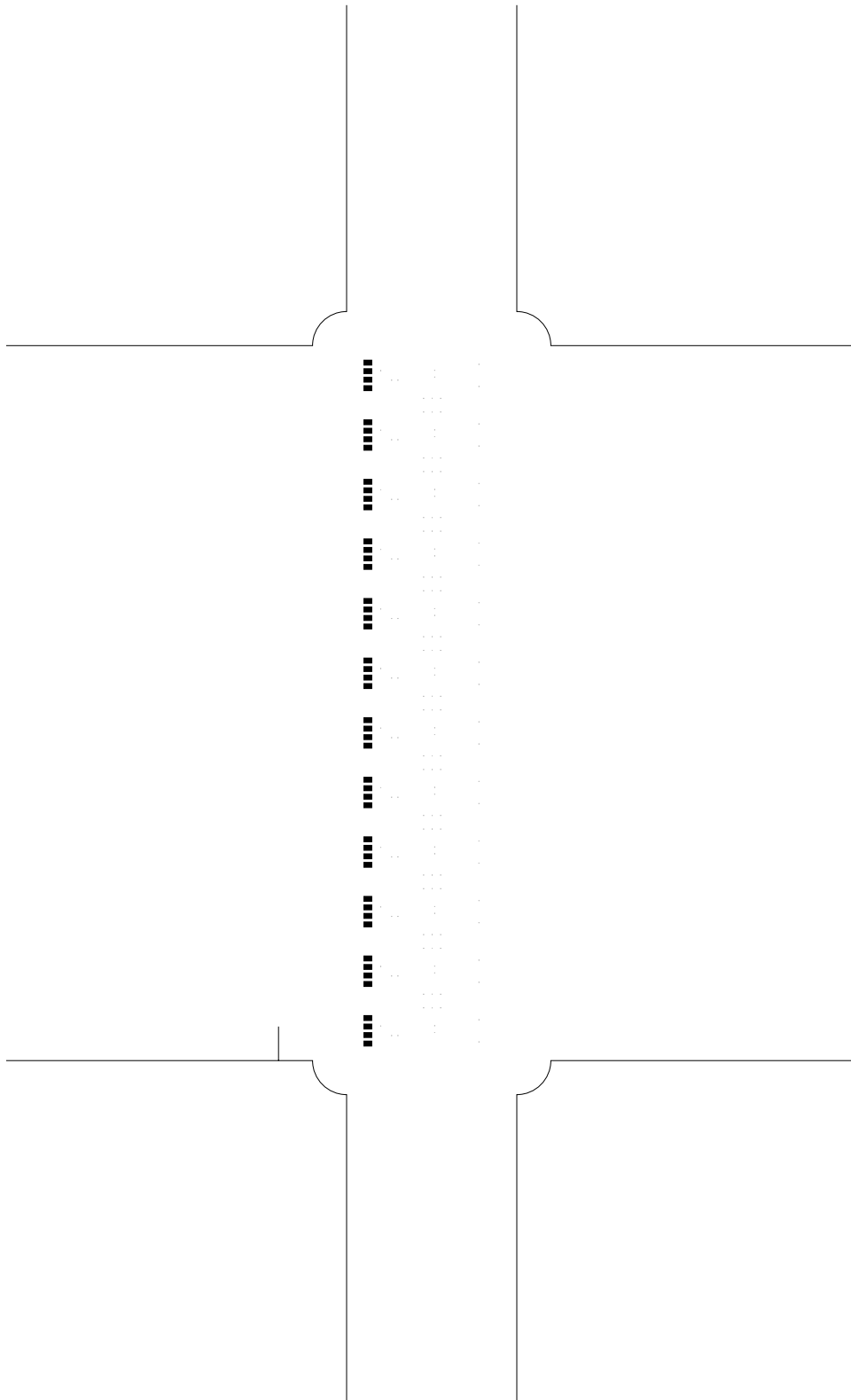




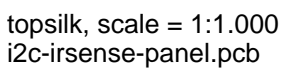


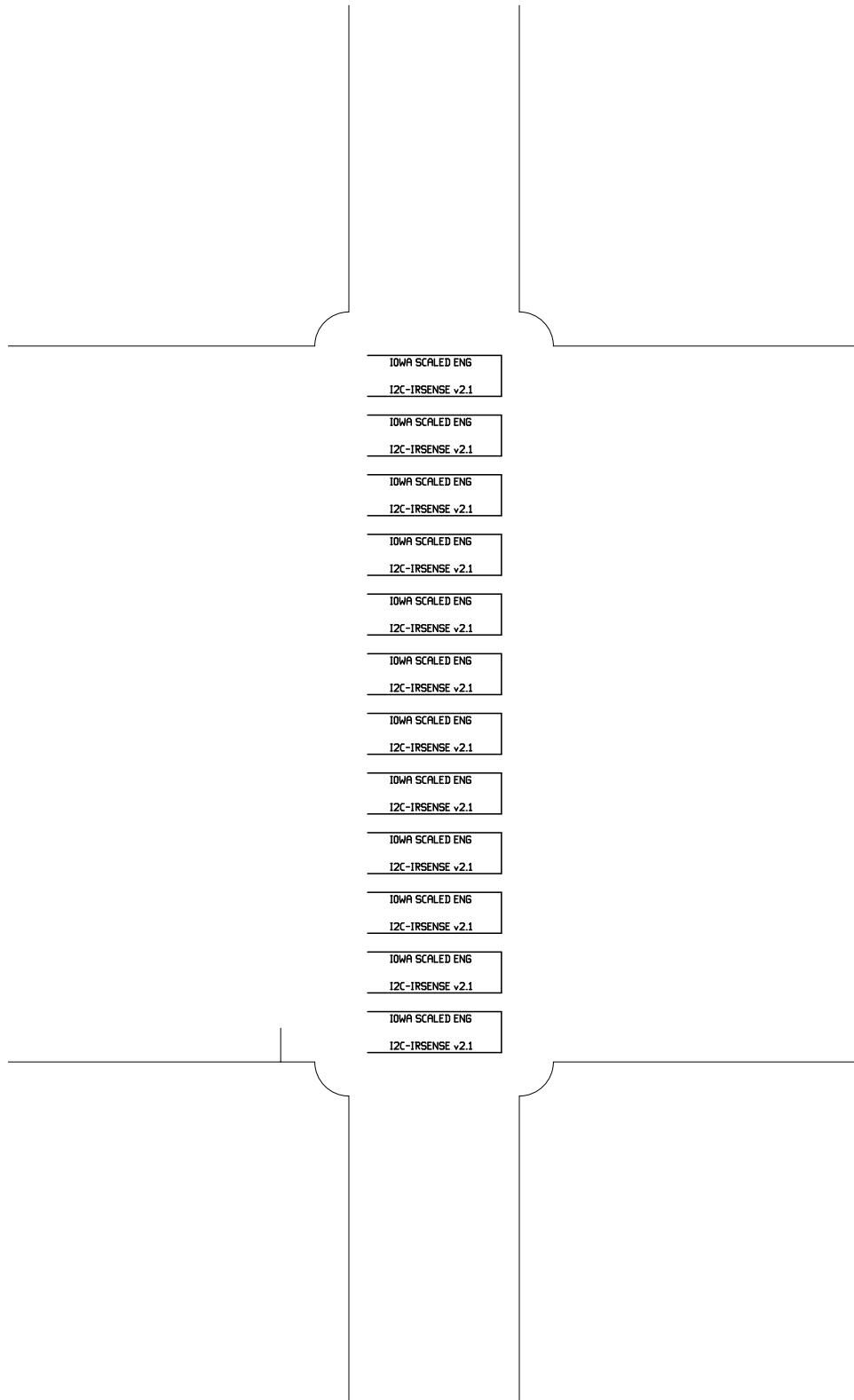


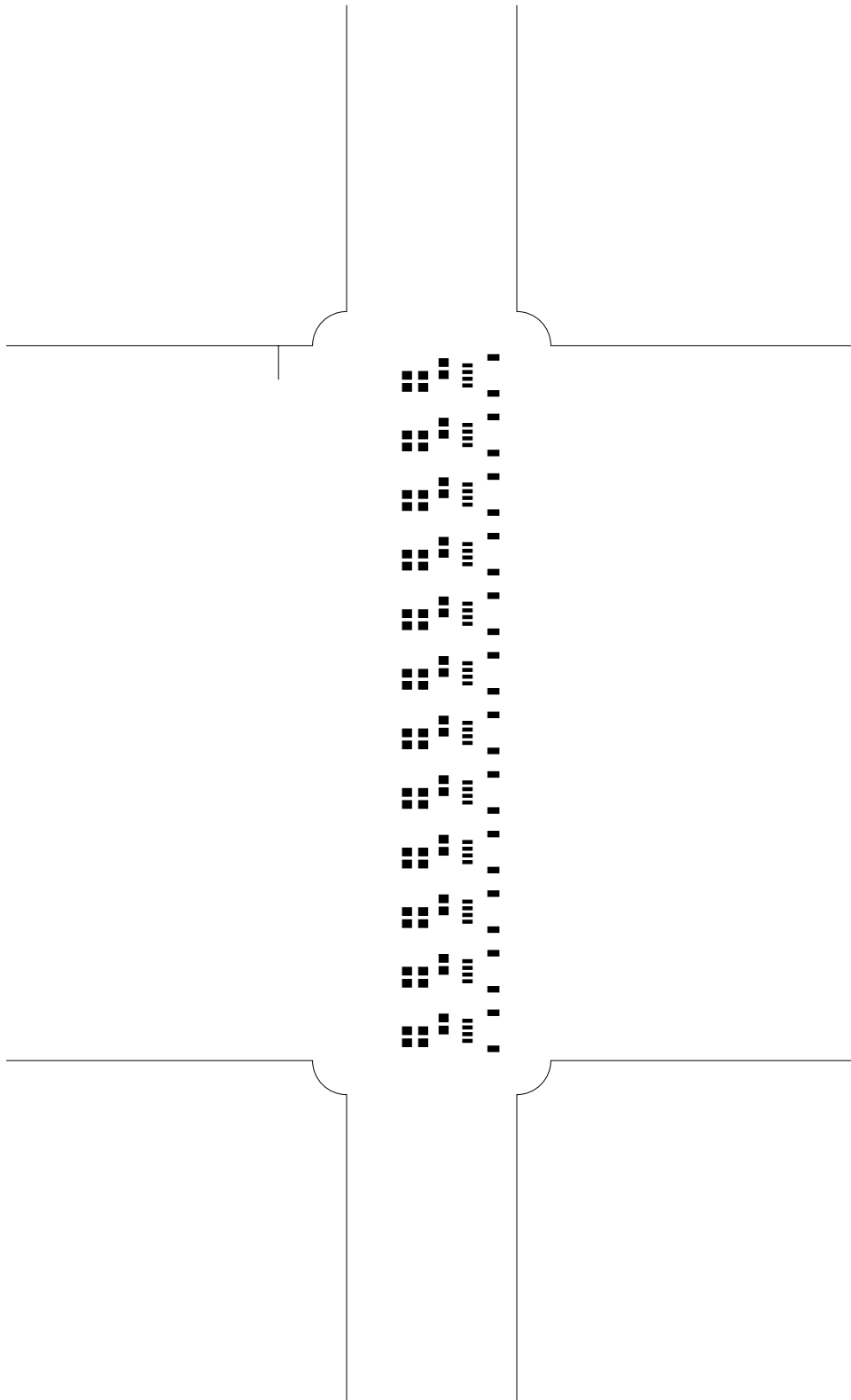


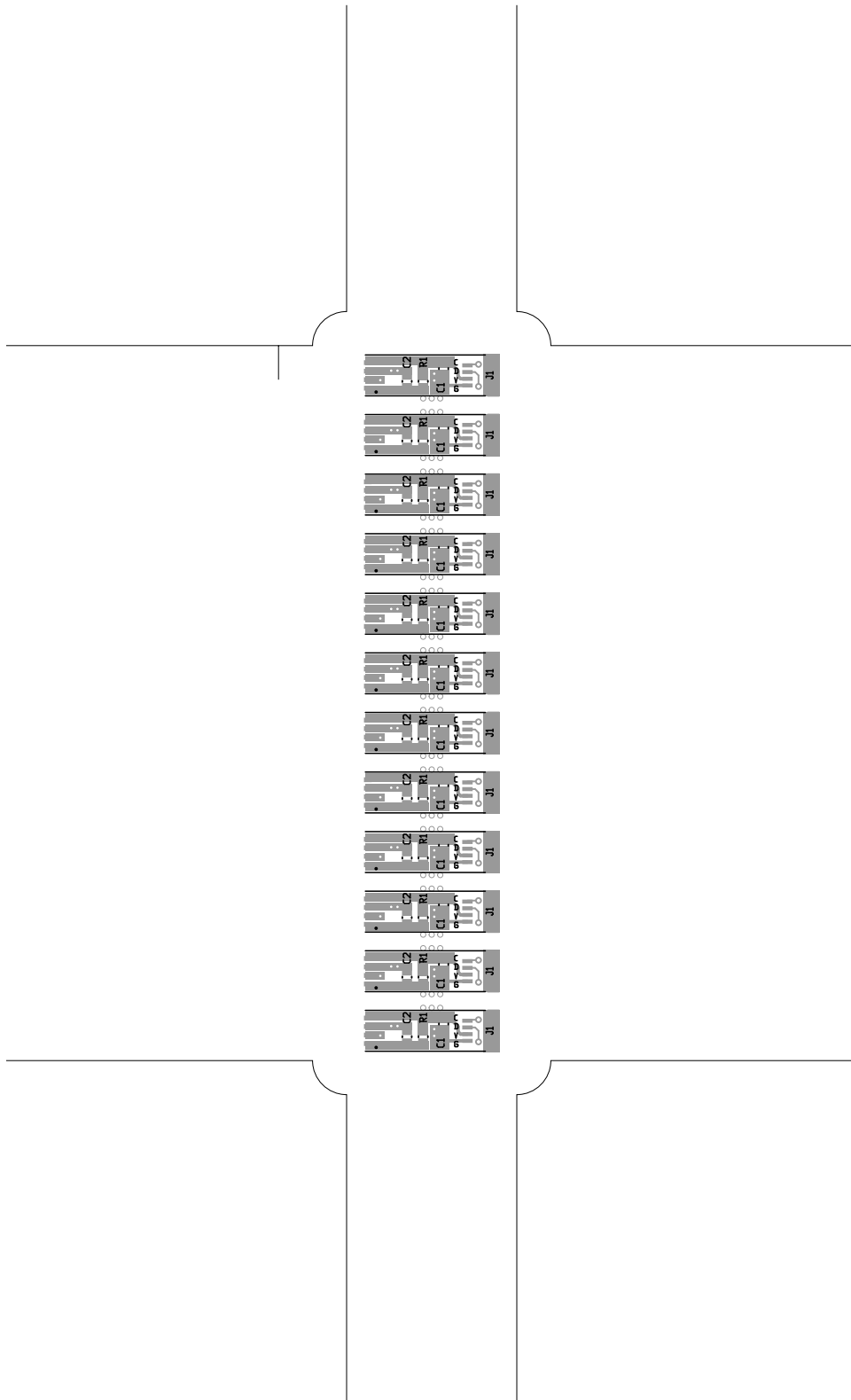


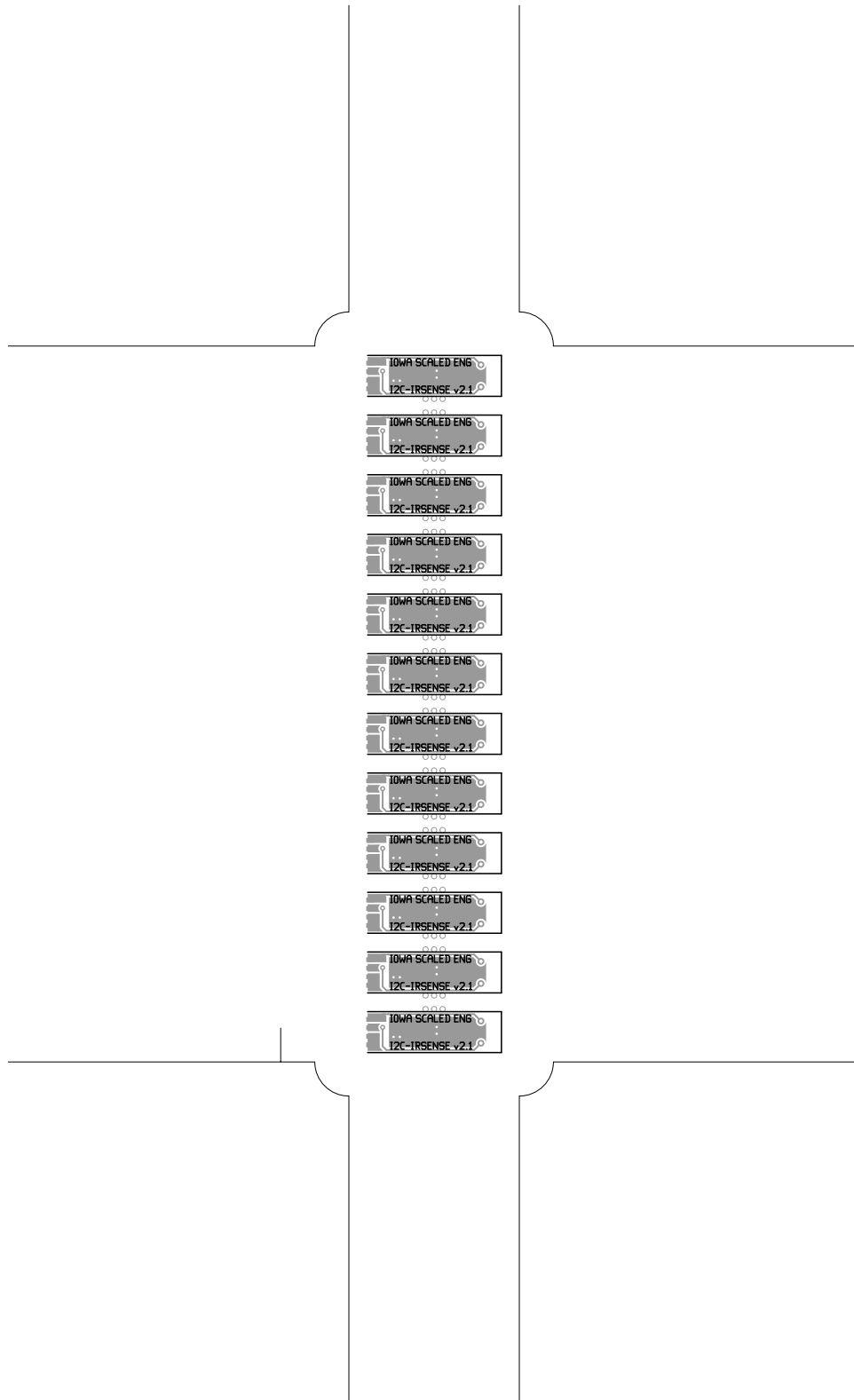
bottommask (mirrored), scale = 1:1.000
i2c-irsense-panel.pcb









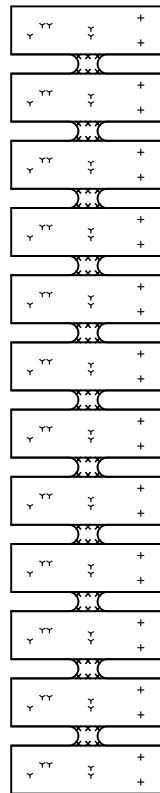


bottomassembly (mirrored), scale = 1:1.000
i2c-irsense-panel.pcb

Symbol	Diam. (Inch)	Count	Plated?
✓	0.014	60	YES
+	0.020	24	YES
x	0.031	66	NO

Author: Michael Petersen

Date: Mon 29 Jan 2018 01:15:10 AM GMT UTC



Board outline is the centerline of this path