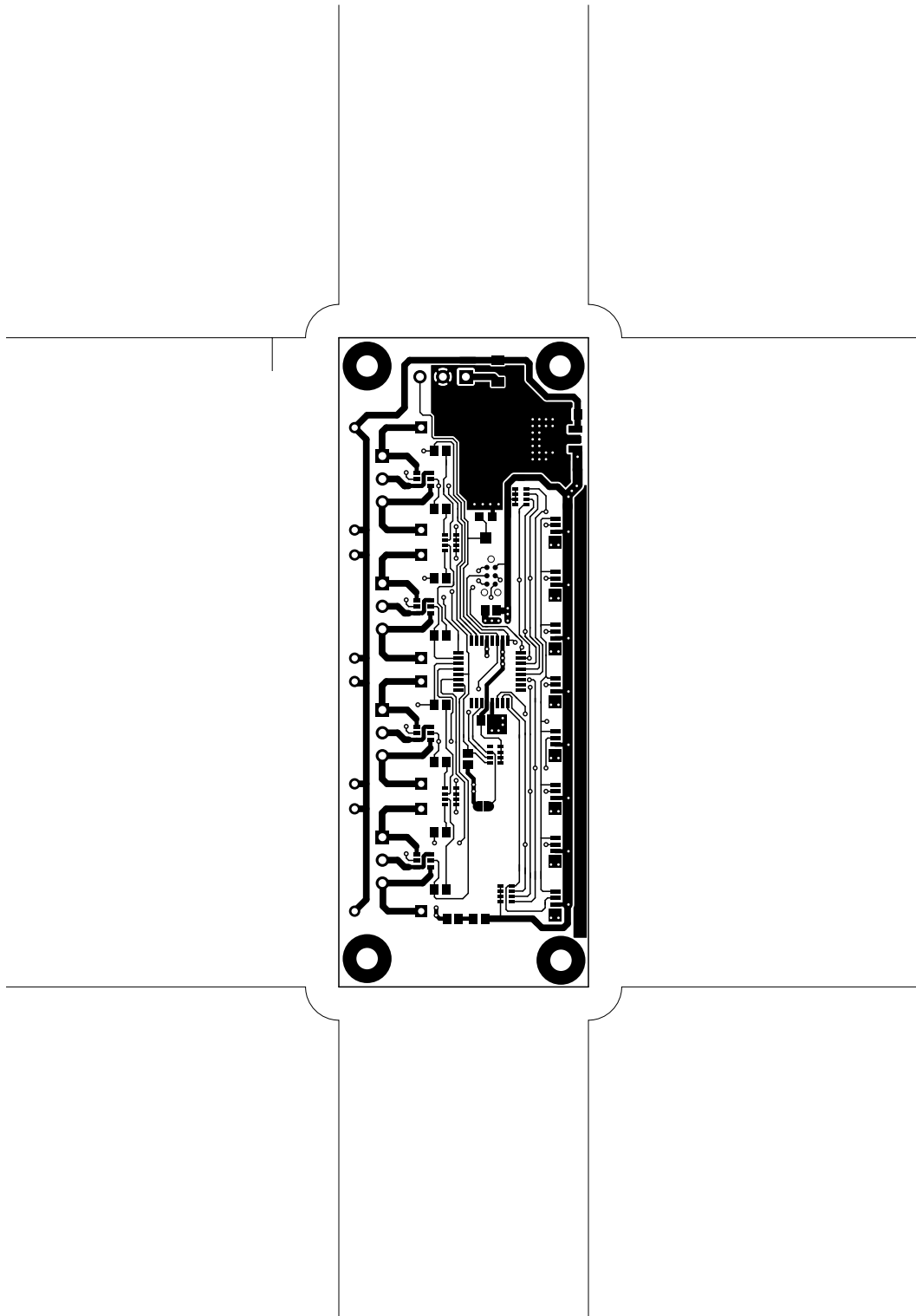
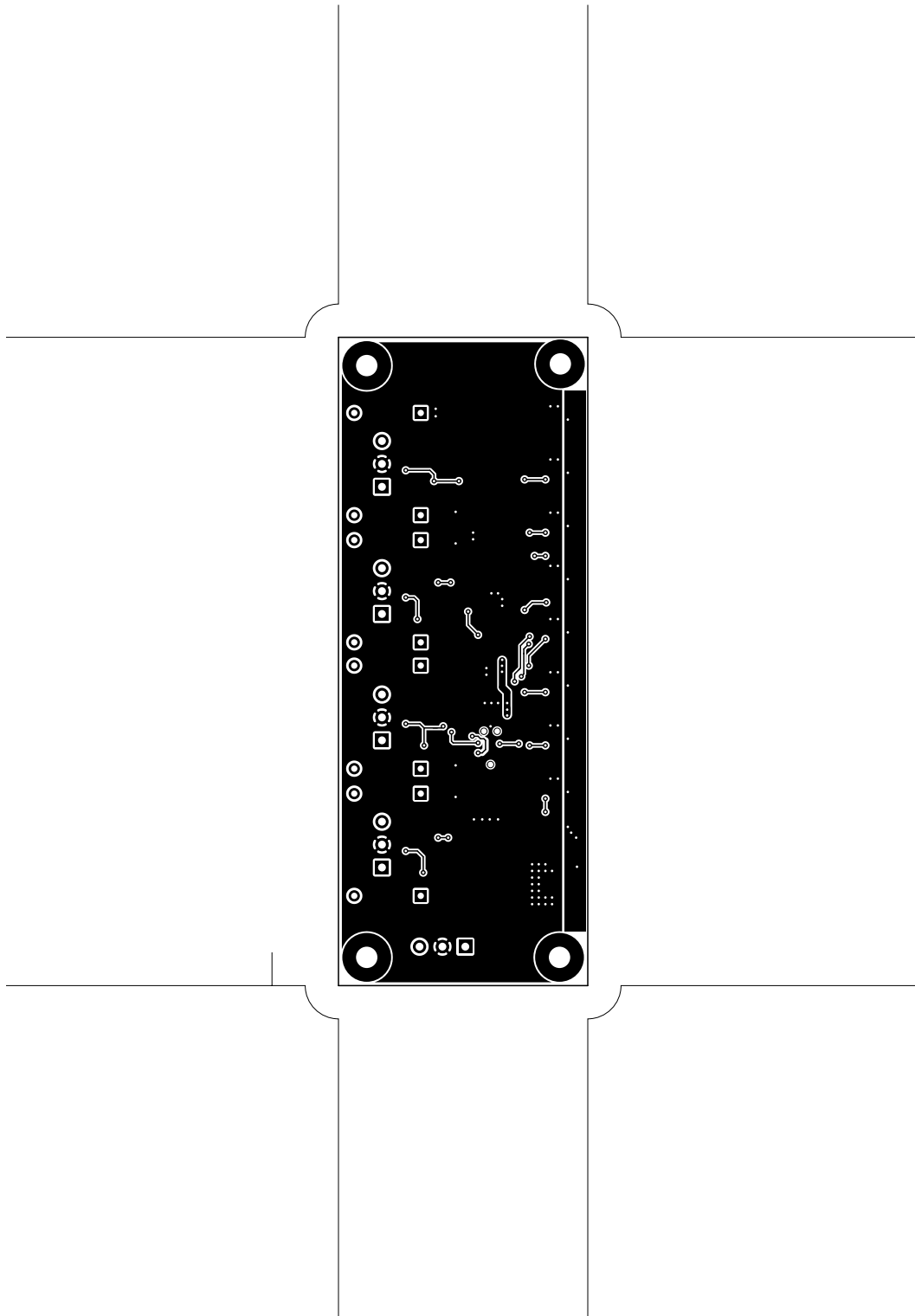


1. Table of Contents (This Page)

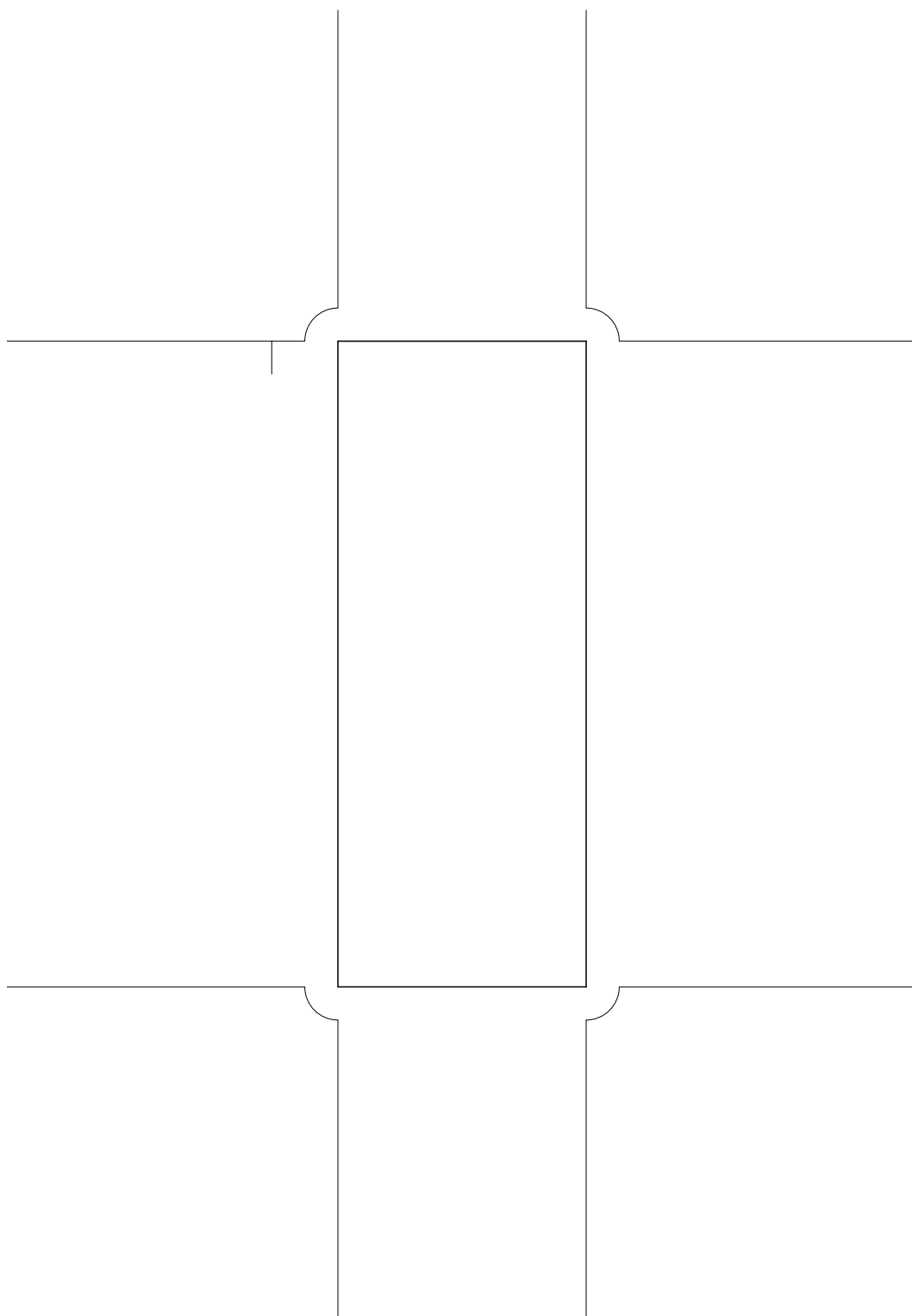
Created on Thu Feb 20 13:43:15 2020

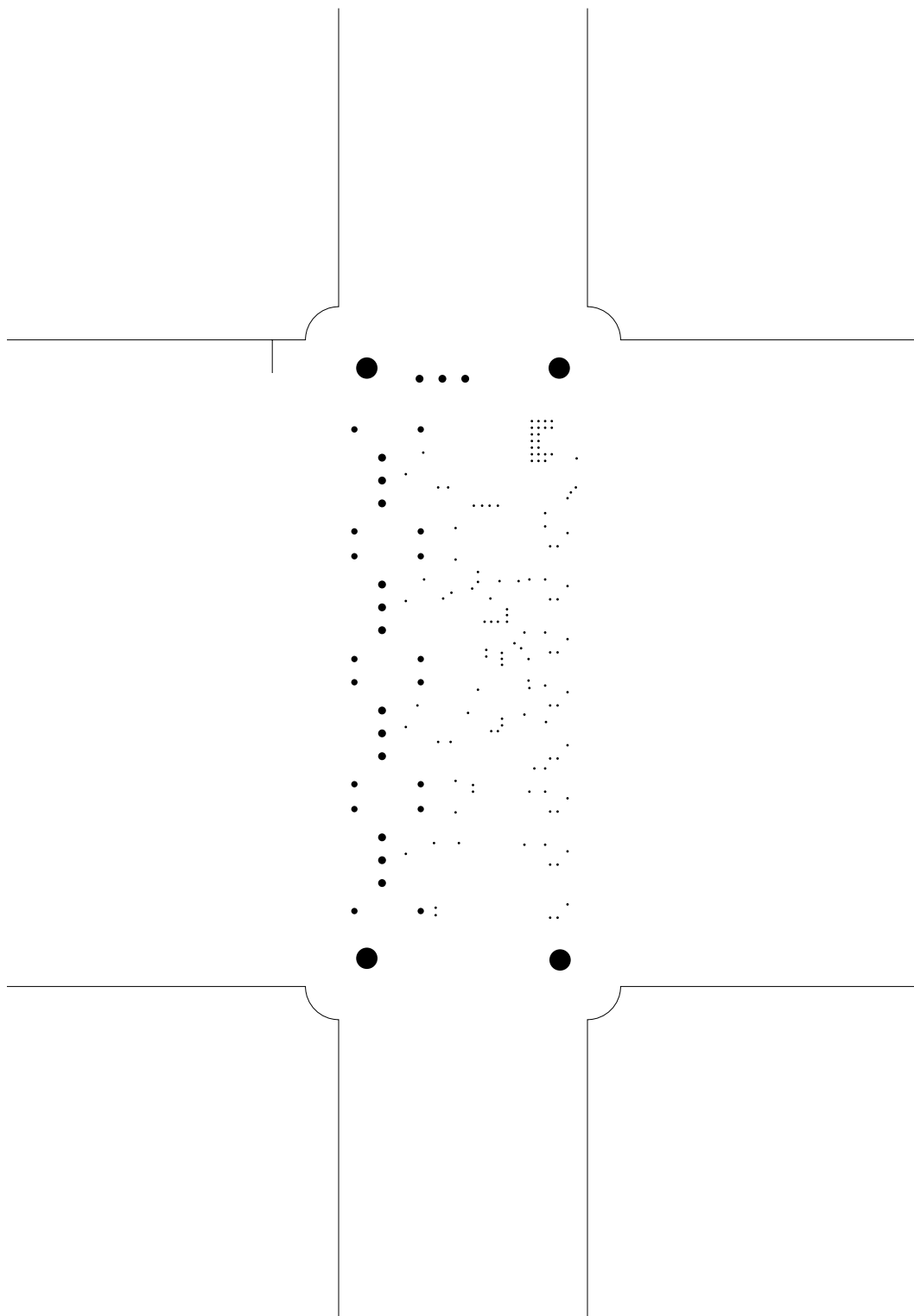
- 2. top
- 3. bottom
- 4. outline
- 5. plated-drill
- 6. unplated-drill
- 7. componentmask
- 8. soldermask
- 9. topsilk
- 10. bottomsilk
- 11. toppaste
- 12. topassembly
- 13. bottomassembly
- 14. fab

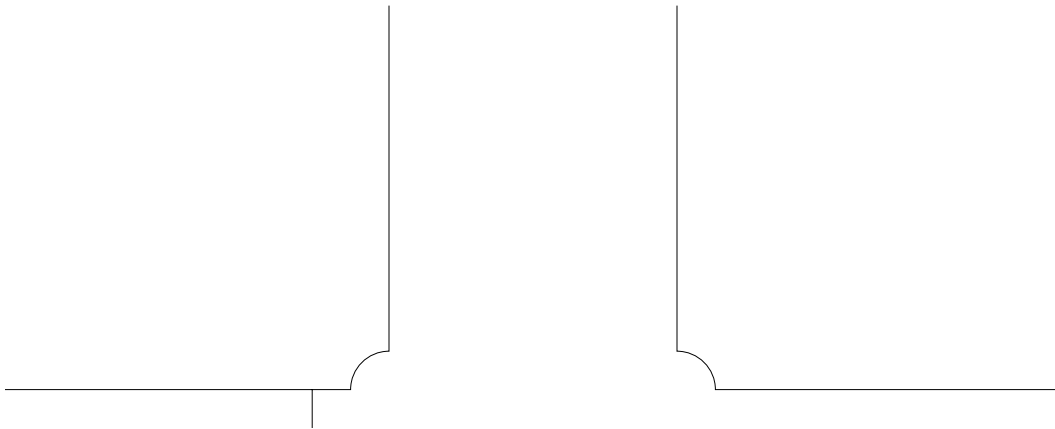




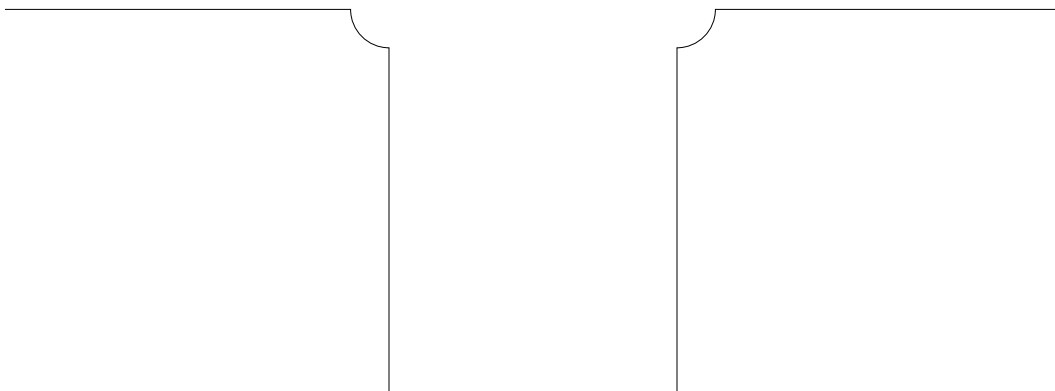
bottom (mirrored), scale = 1:1.000
ckt-irsense-8.pcb

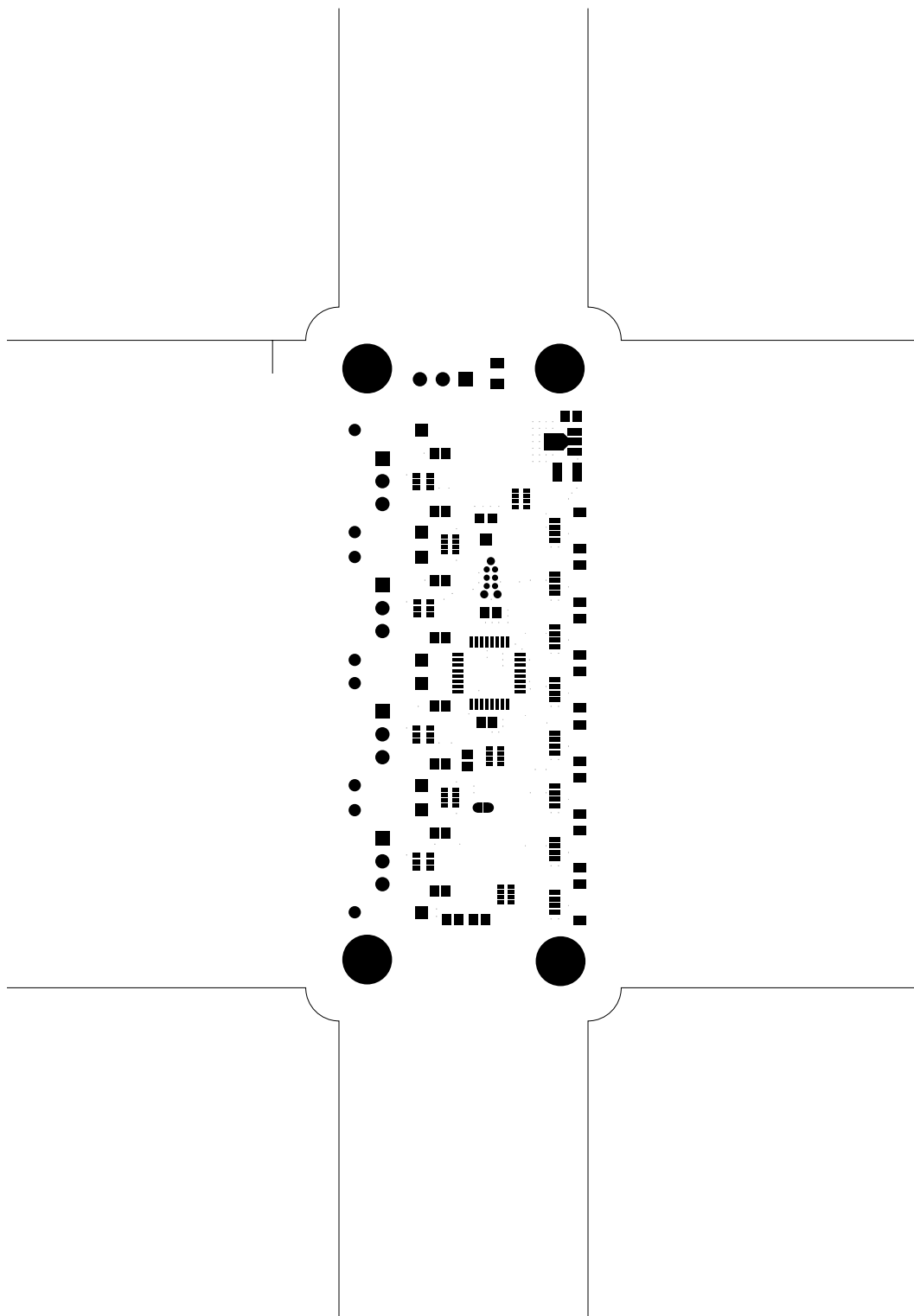


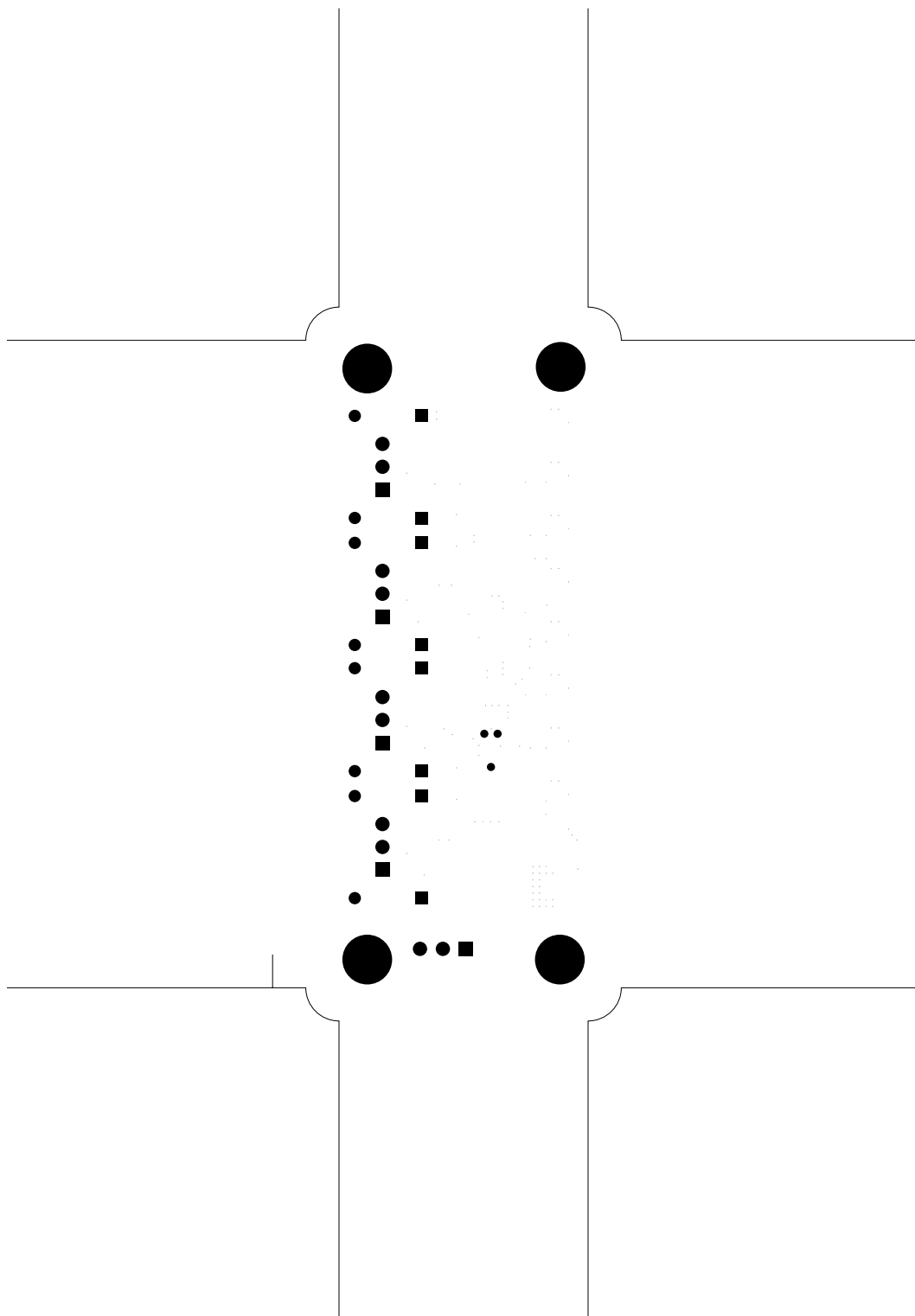


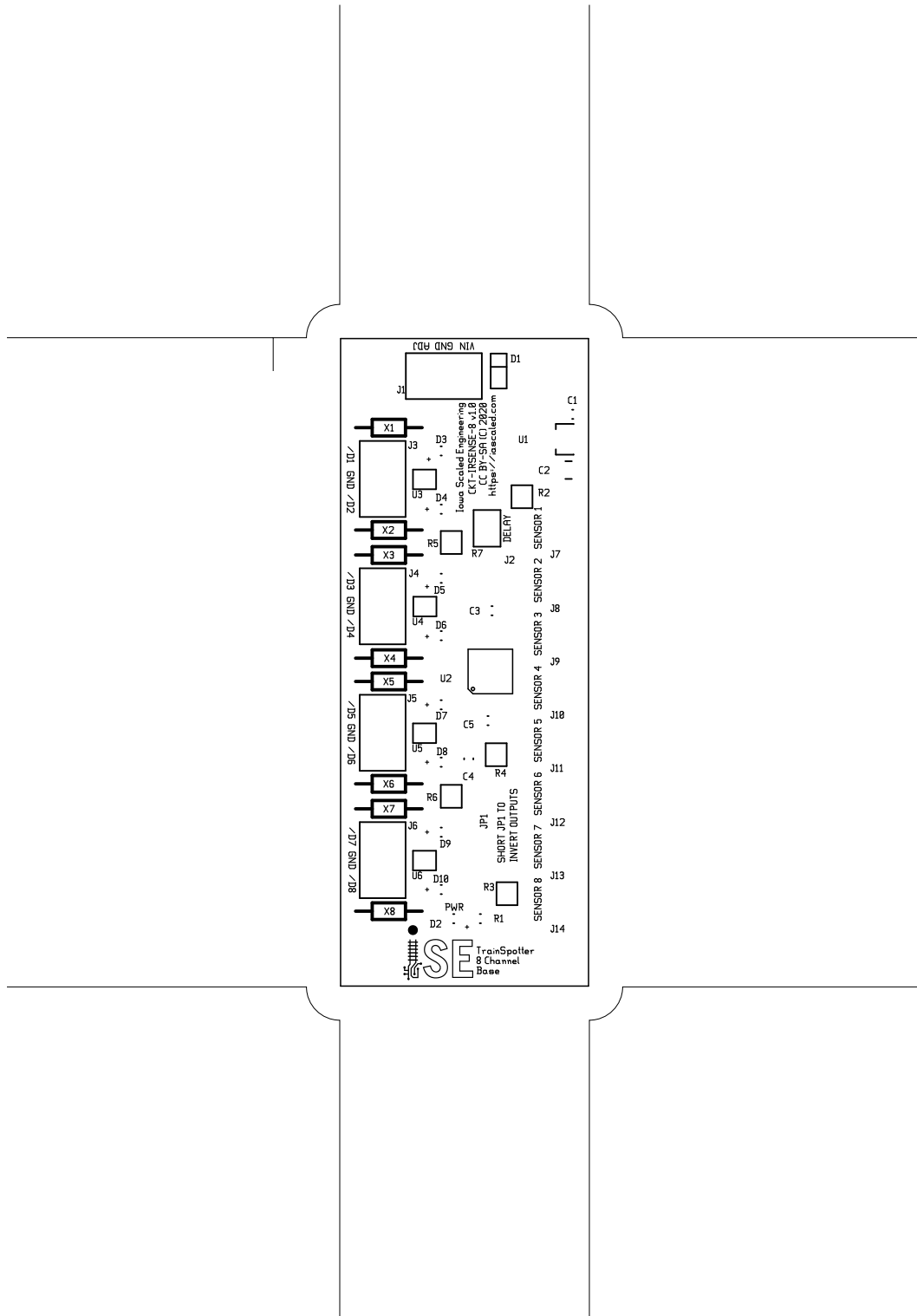


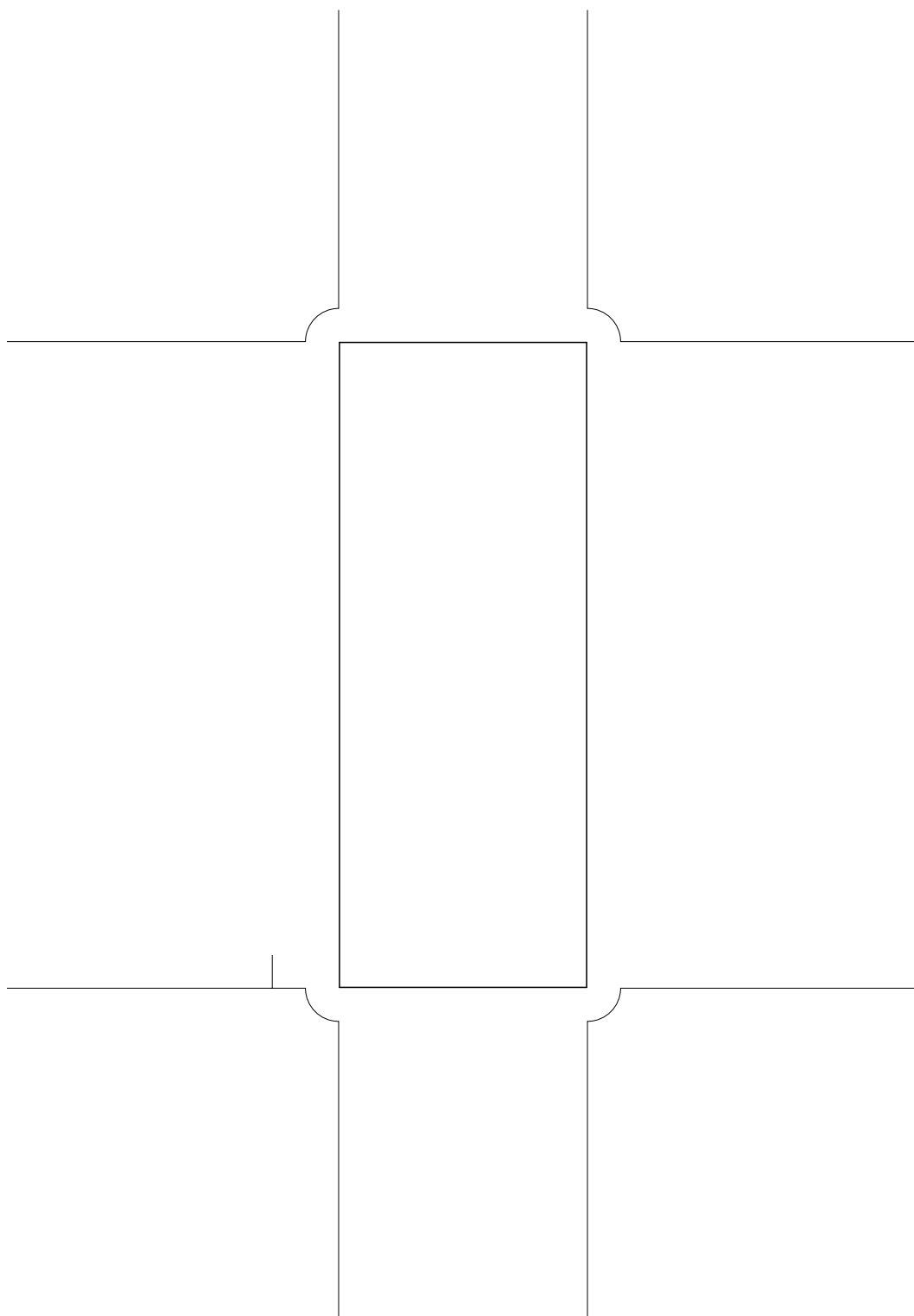
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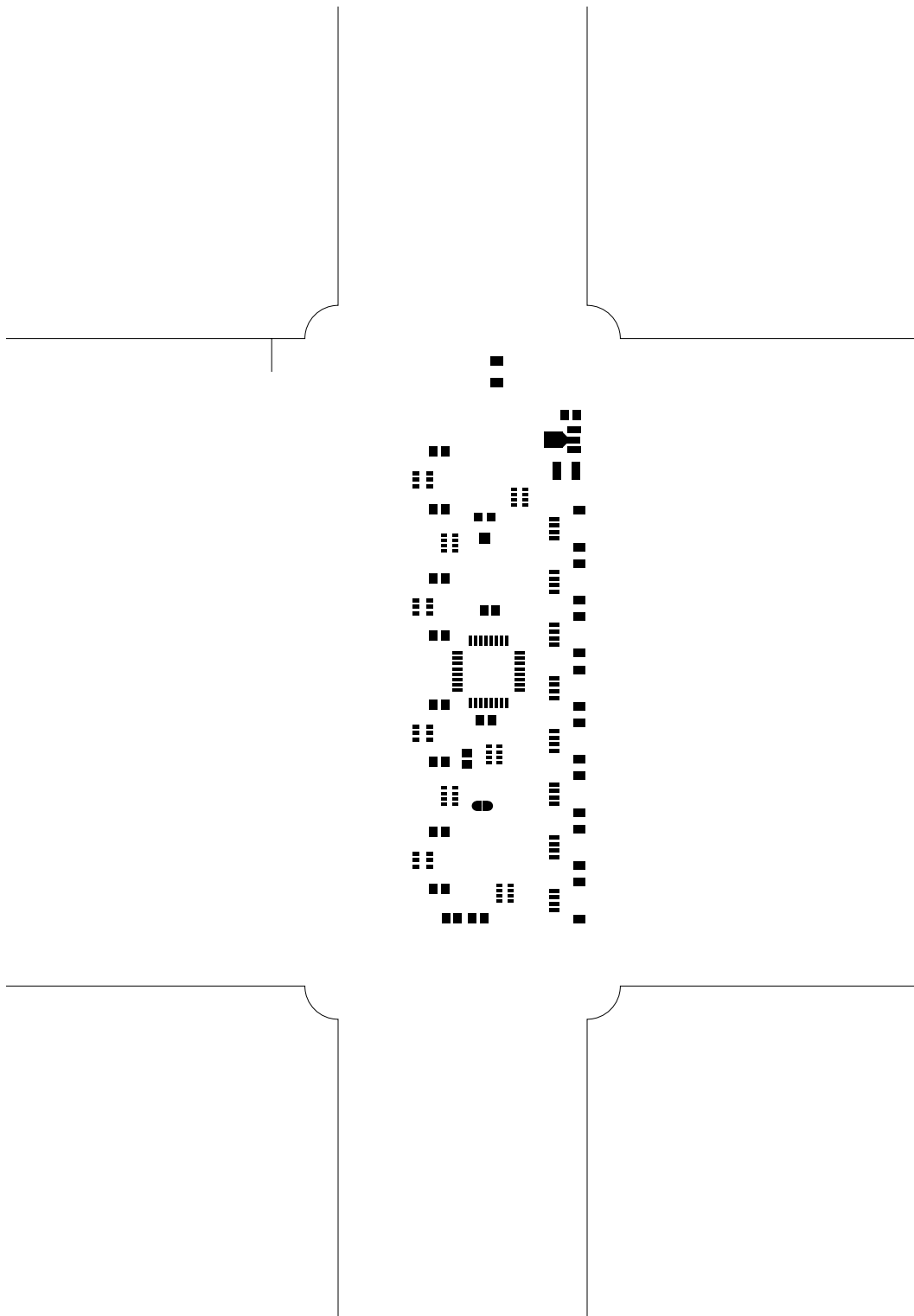


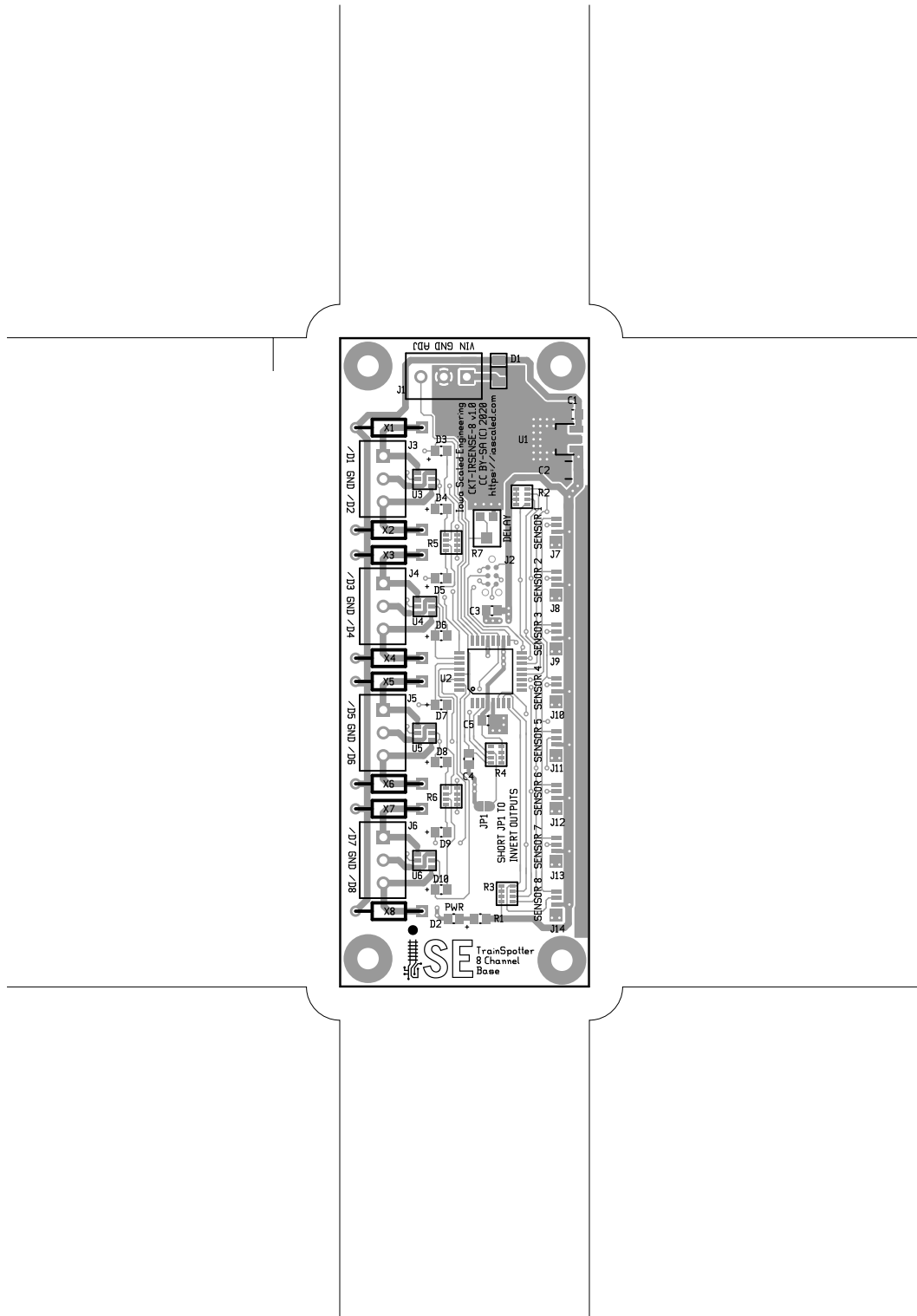


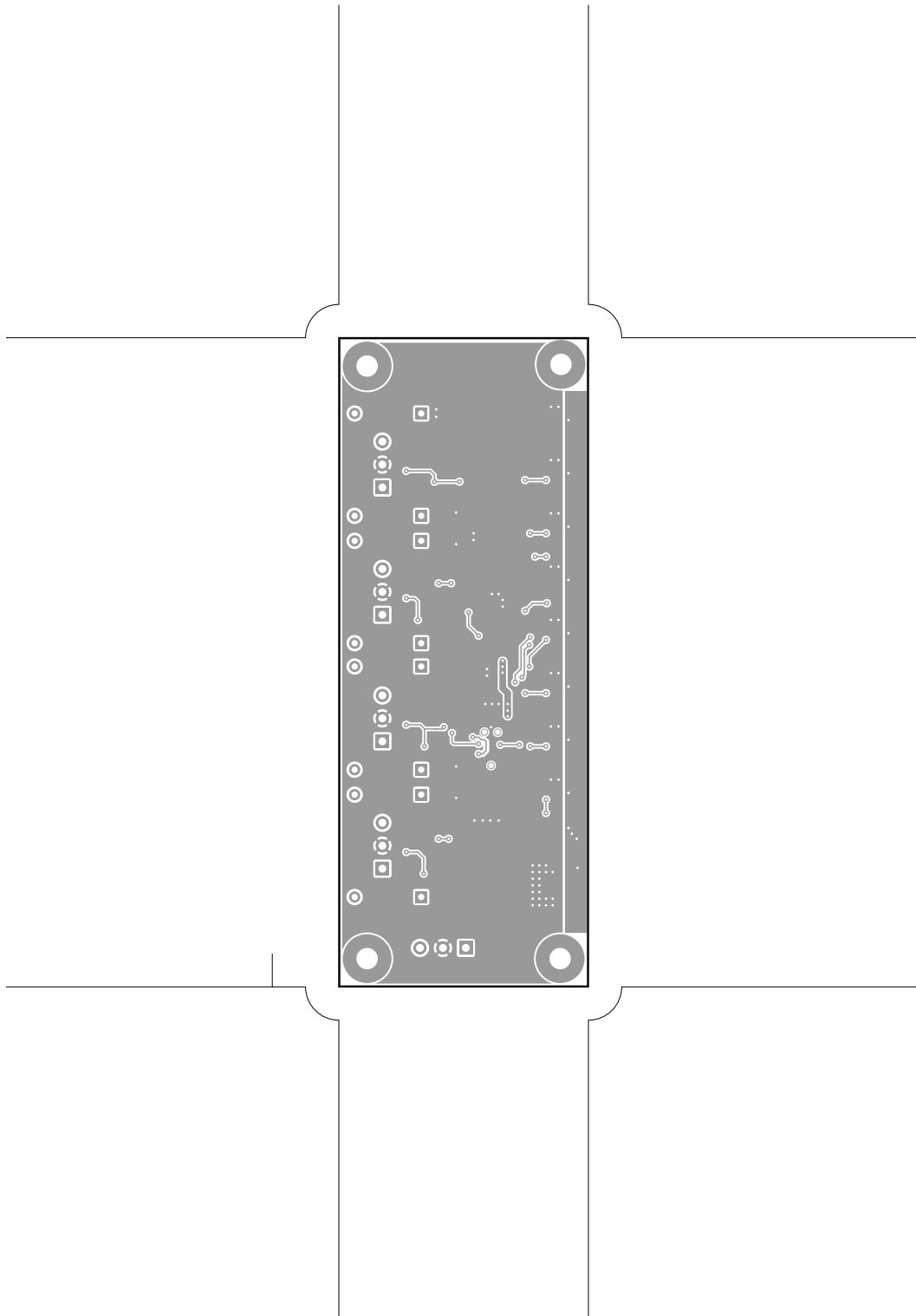












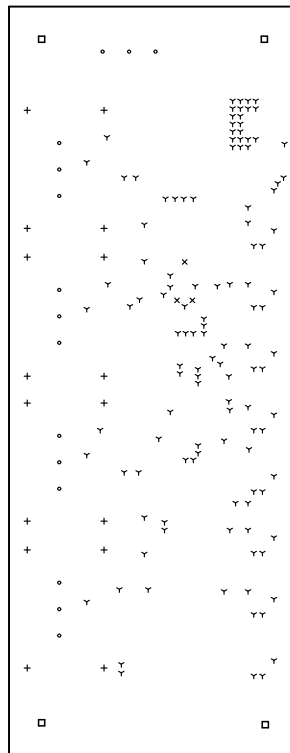
There are 5 different drill sizes used in this layout, 157 holes total

Symbol	Diam. (Inch)	Count	Plated?
Y	0.015	119	YES
+	0.038	16	YES
x	0.039	3	NO
o	0.047	15	YES
□	0.129	4	YES

Title: (unknown) - Fabrication Drawing

Author: Nathan D. Holmes

Date: Thu Feb 20 20:43:15 2020 UTC



Board outline is the centerline of this path