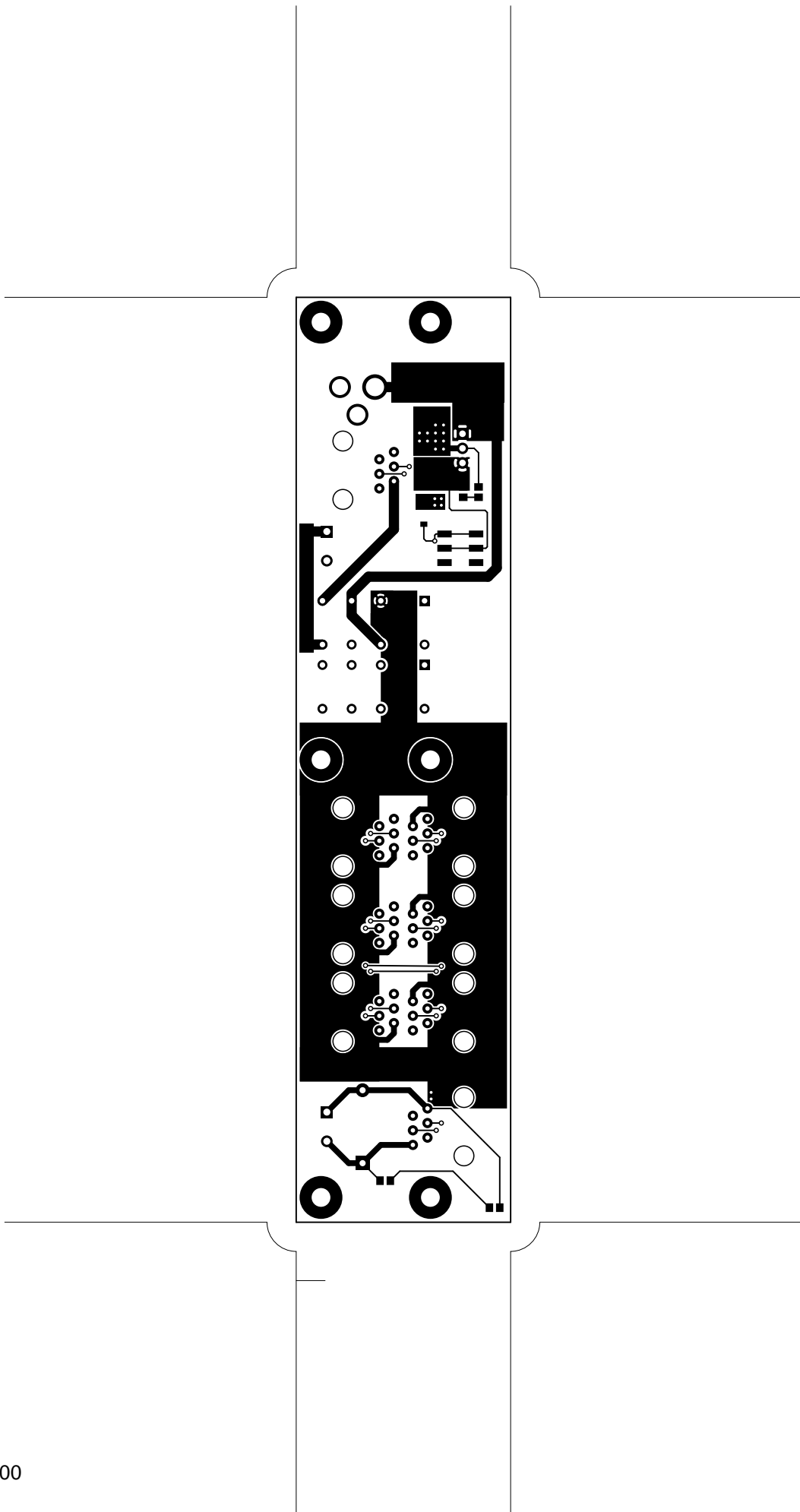
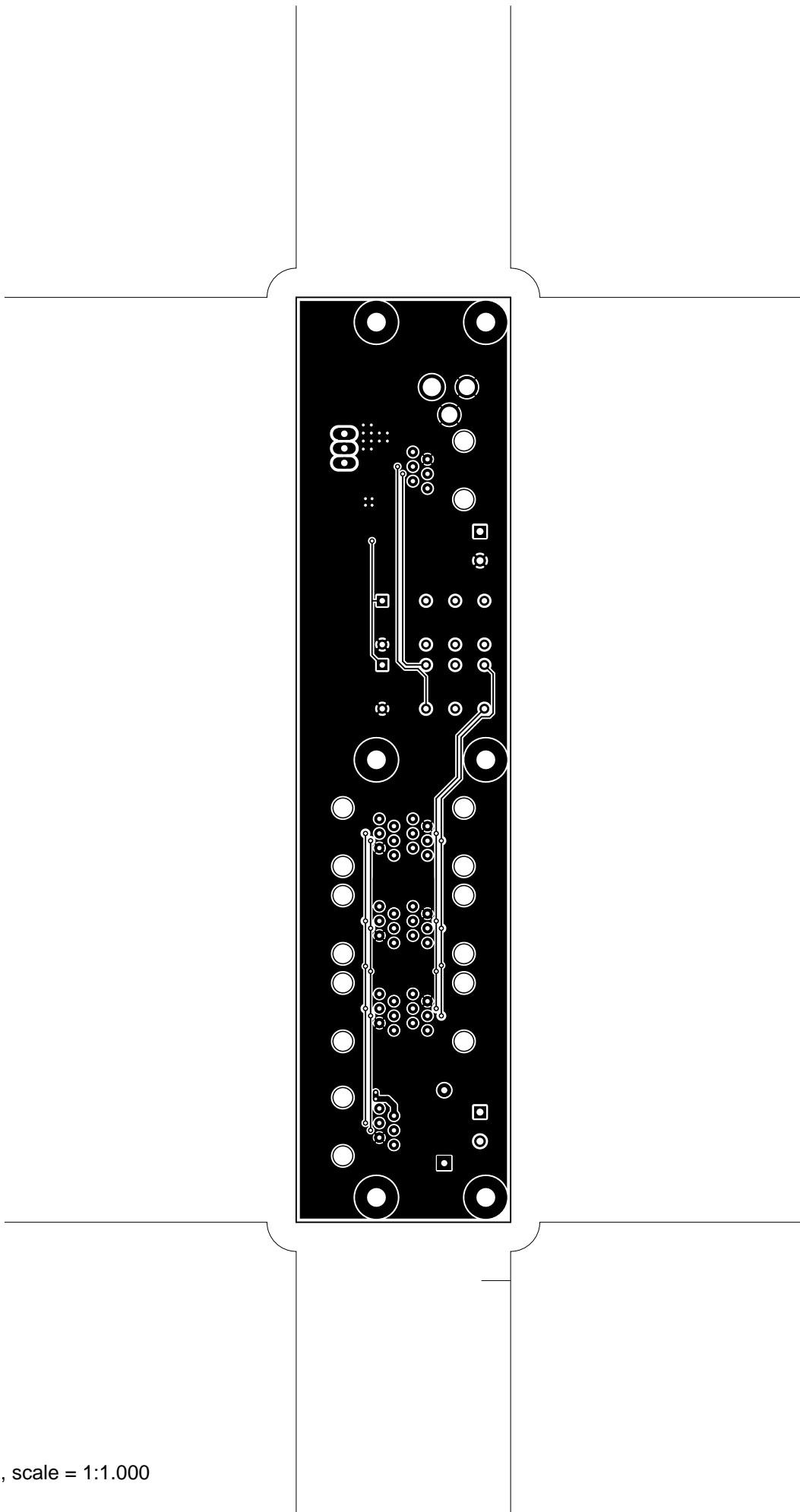


1. Table of Contents (This Page)

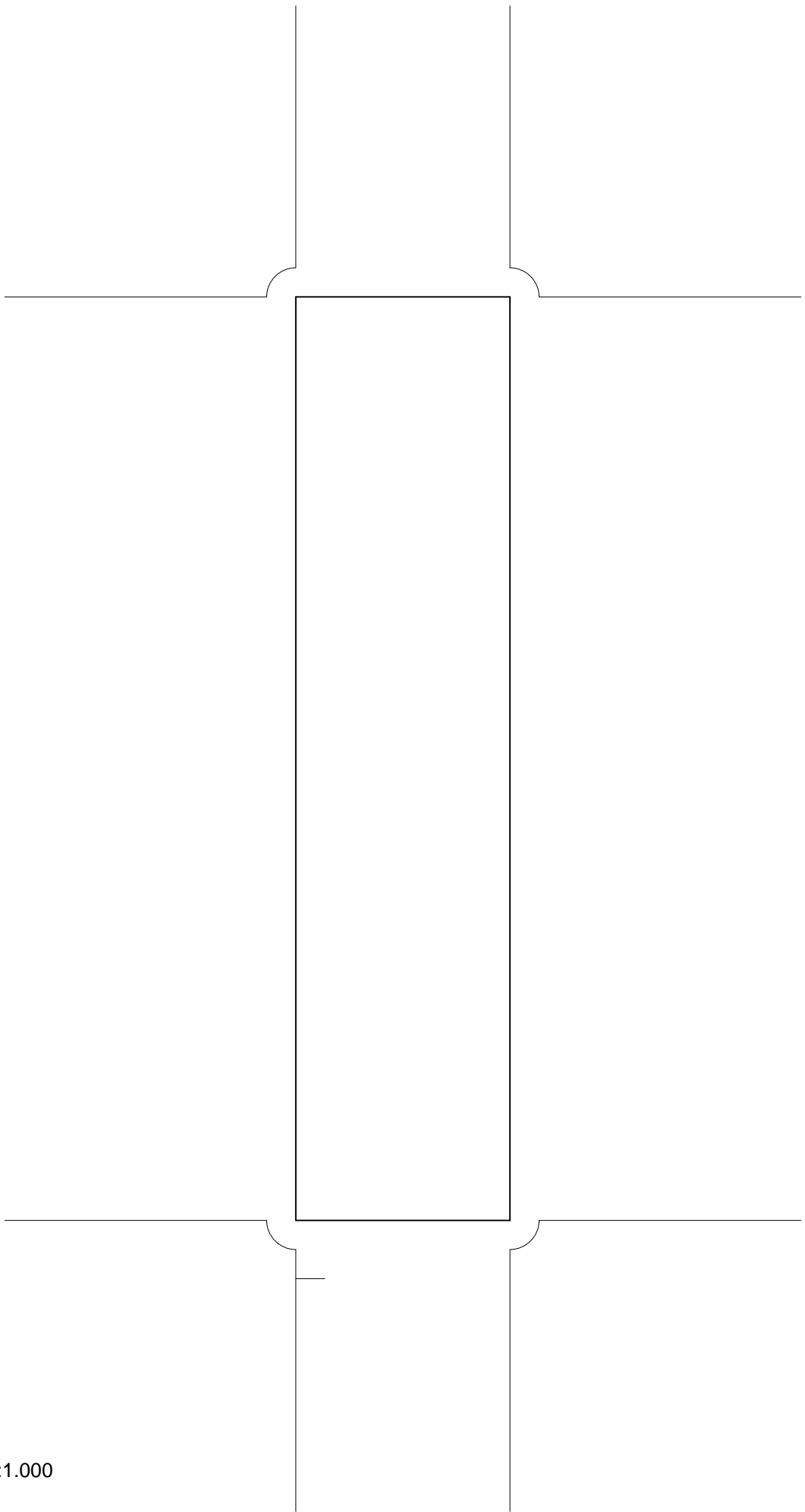
Created on Thu Nov 29 14:53:53 2018

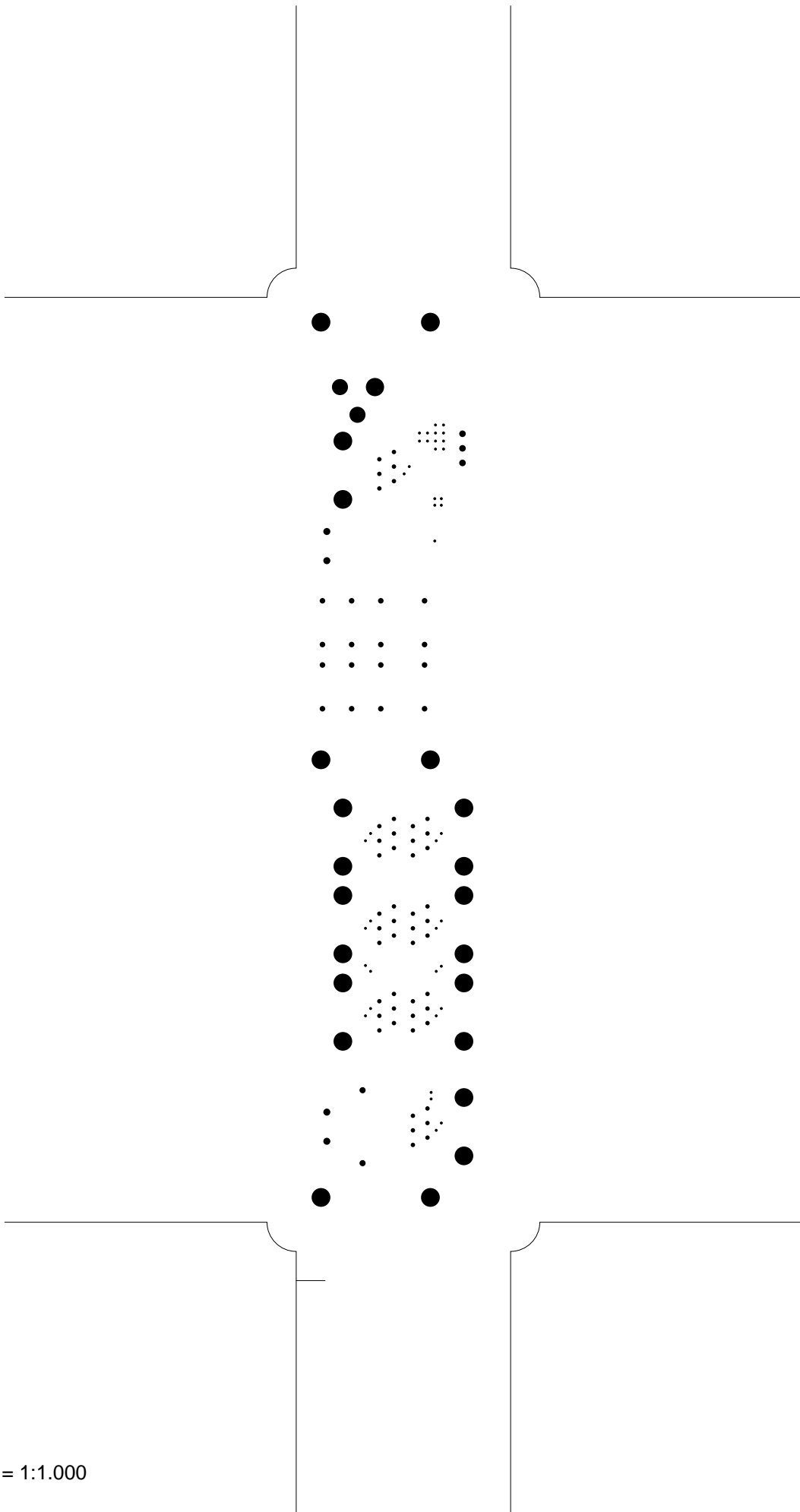
- 2. top
- 3. bottom
- 4. outline
- 5. plated-drill
- 6. componentmask
- 7. soldermask
- 8. topsilk
- 9. bottomsilk
- 10. toppaste
- 11. topassembly
- 12. bottomassembly
- 13. fab

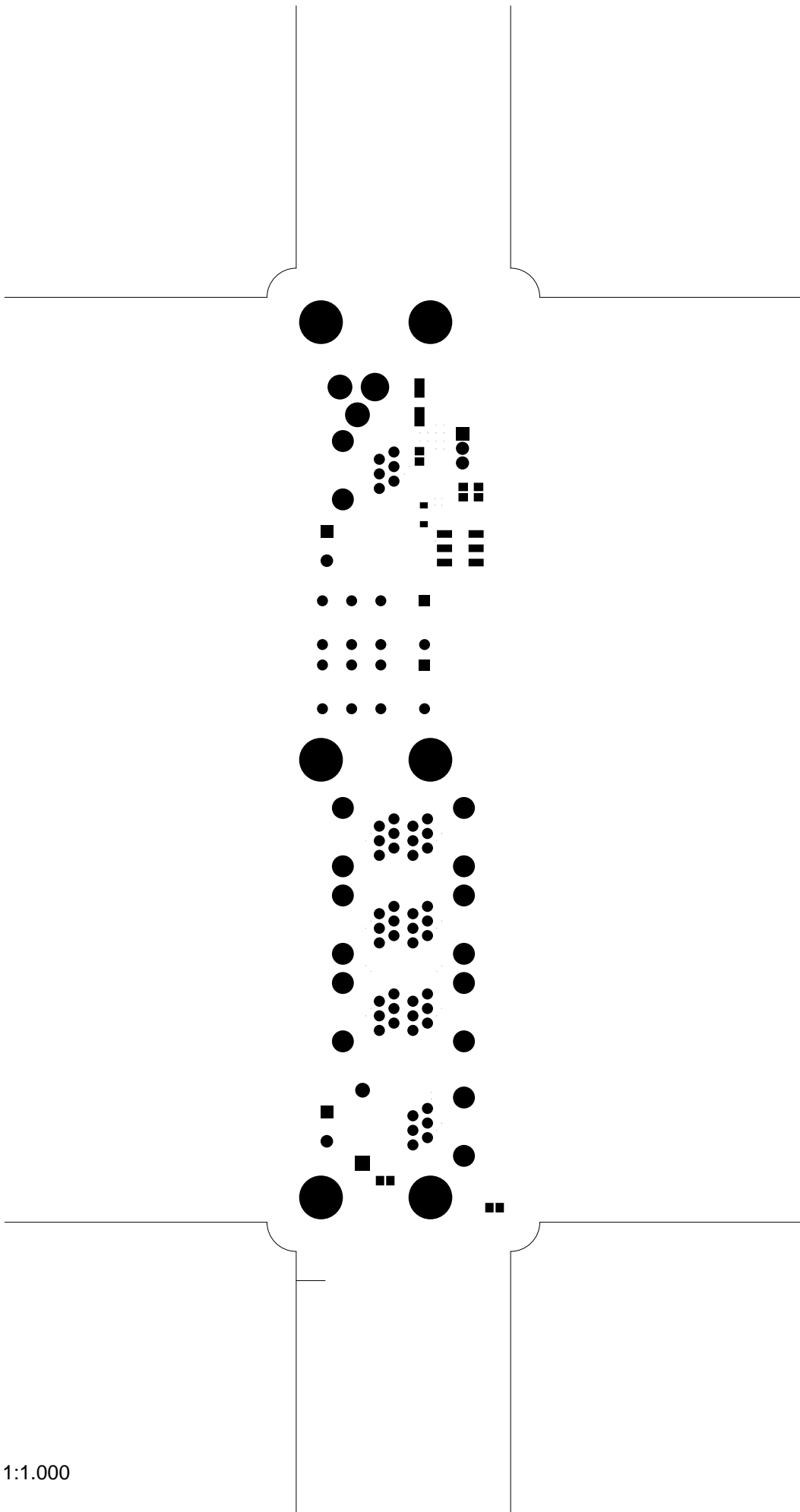


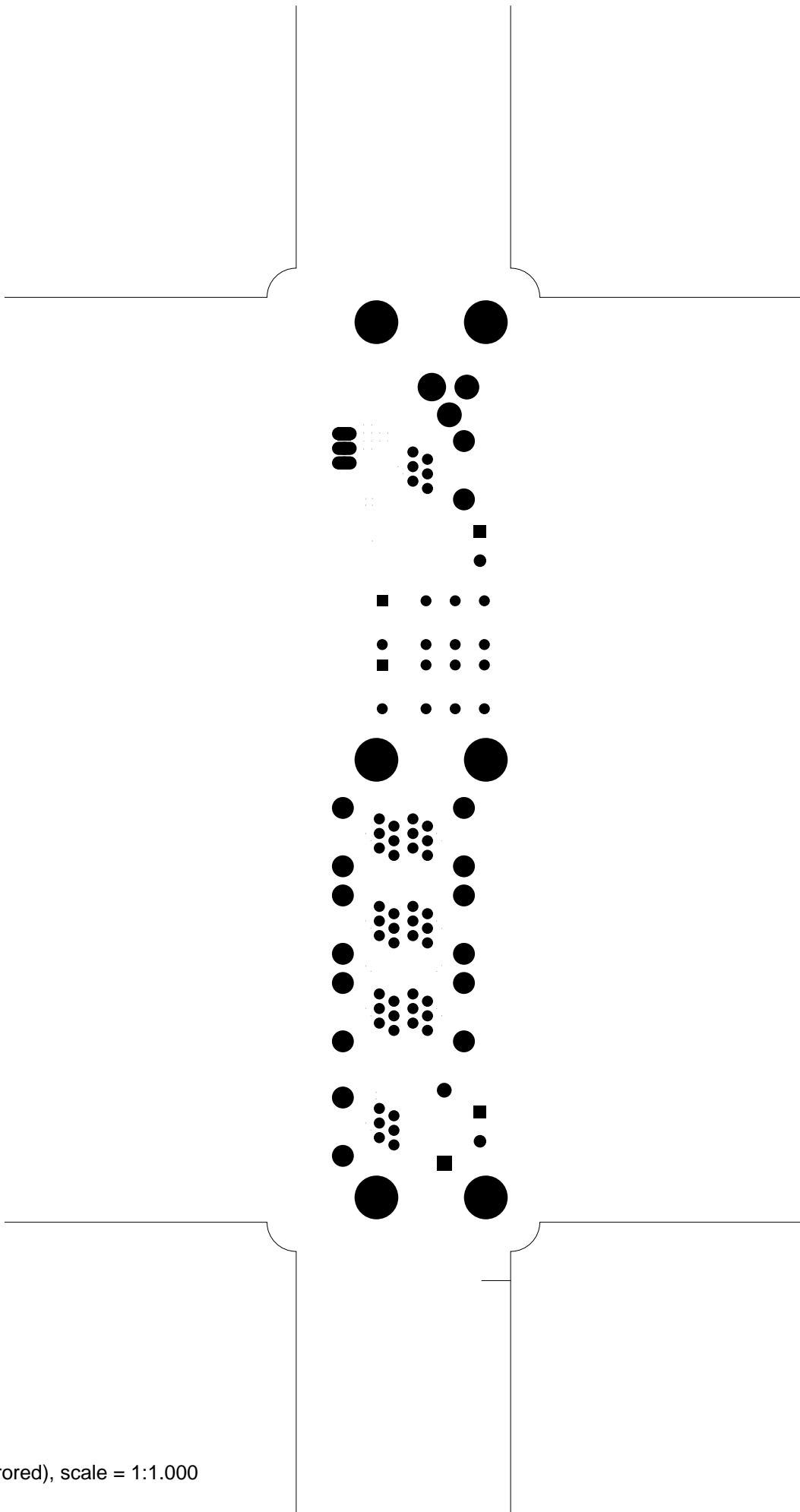


bottom (mirrored), scale = 1:1.000
ckt-ncesw.pcb

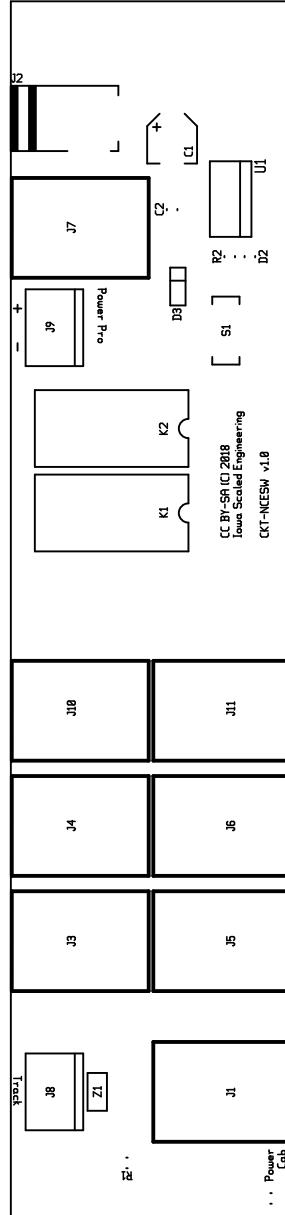


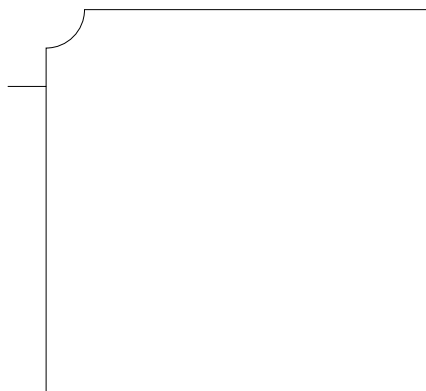
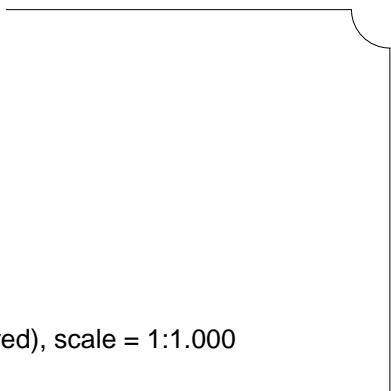
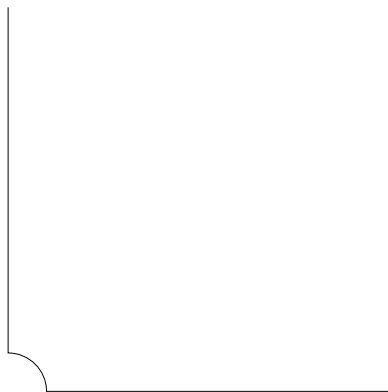
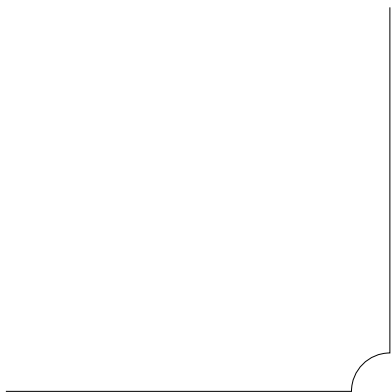


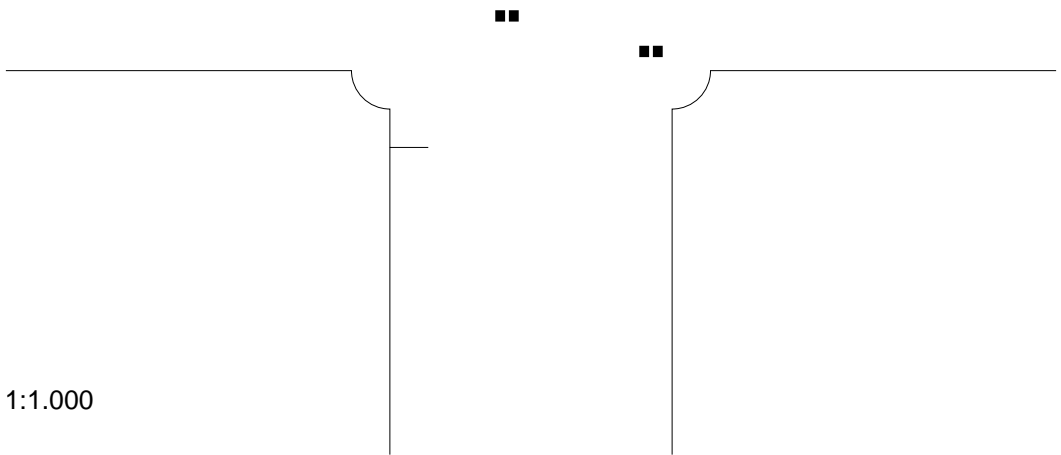
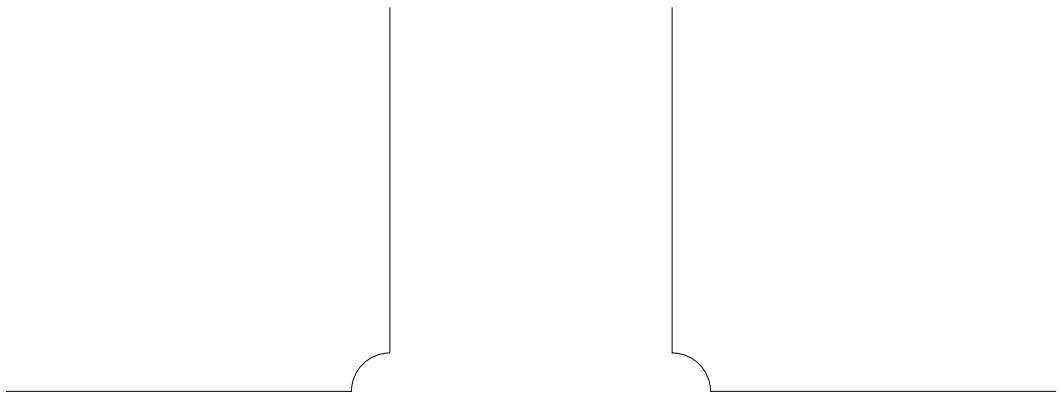


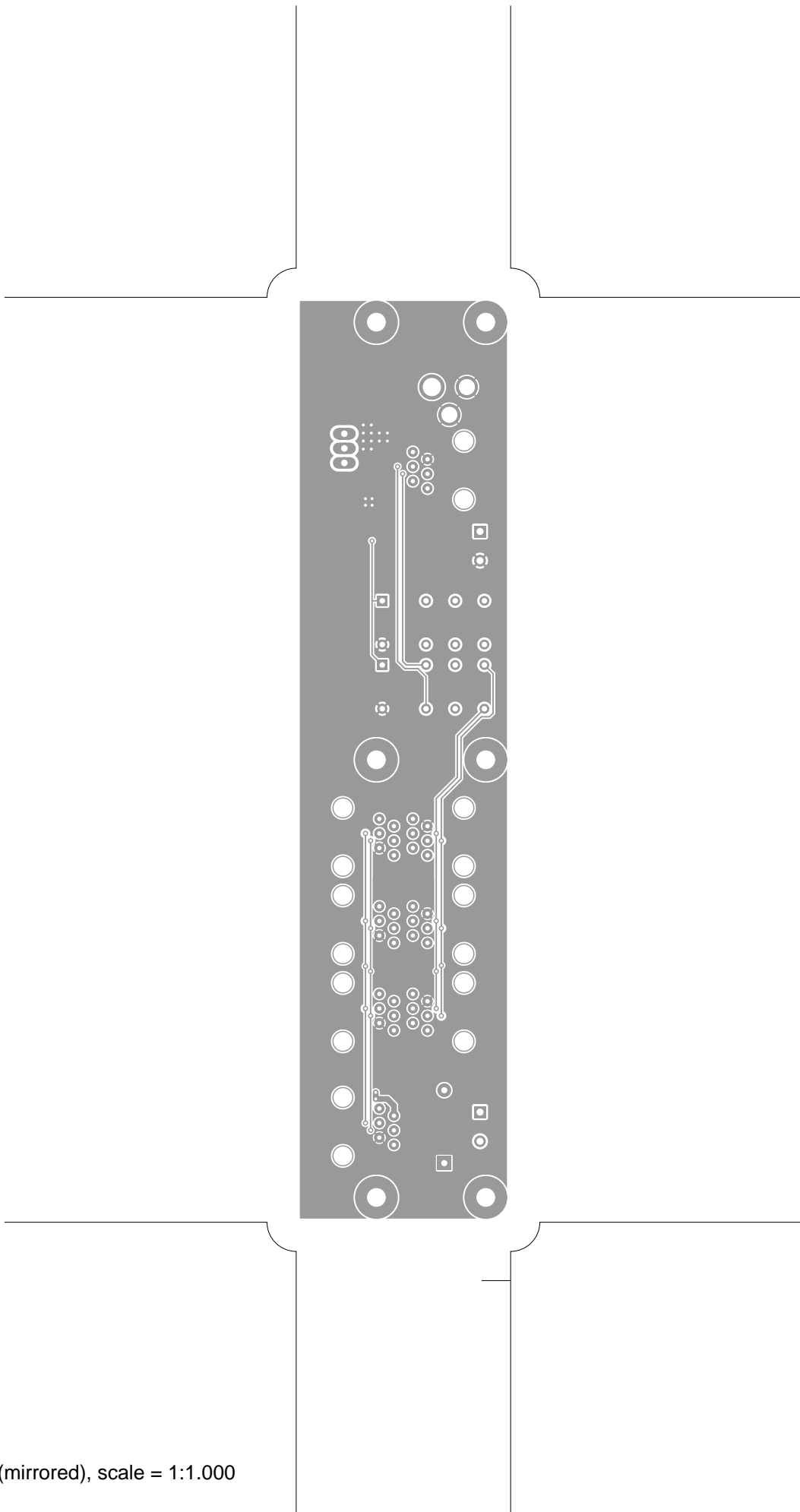


bottommask (mirrored), scale = 1:1.000
ckt-ncesw.pcb







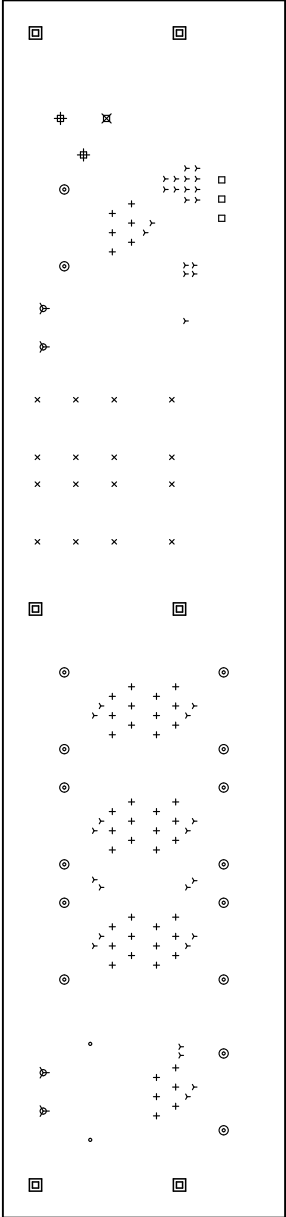


bottomassembly (mirrored), scale = 1:1.000
ckt-ncesw.pcb

There are 10 different drill sizes used in this layout, 137 holes total

Symbol	Diam. (Inch)	Count	Plated?
γ	0.020	39	YES
+	0.030	48	YES
x	0.038	16	YES
°	0.042	2	YES
□	0.045	3	YES
▽	0.048	4	YES
⊕	0.110	2	YES
⊗	0.125	1	YES
⊙	0.128	16	YES
⊠	0.129	6	YES

Title: (unknown) - Fabrication Drawing
Author: Michael Petersen
Date: Thu 29 Nov 2018 09:53:53 PM GMT UTC



Board outline is the centerline of this path