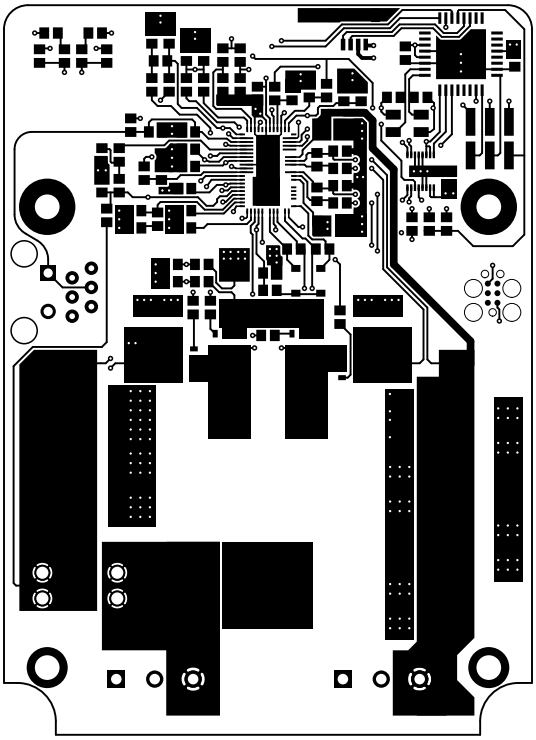
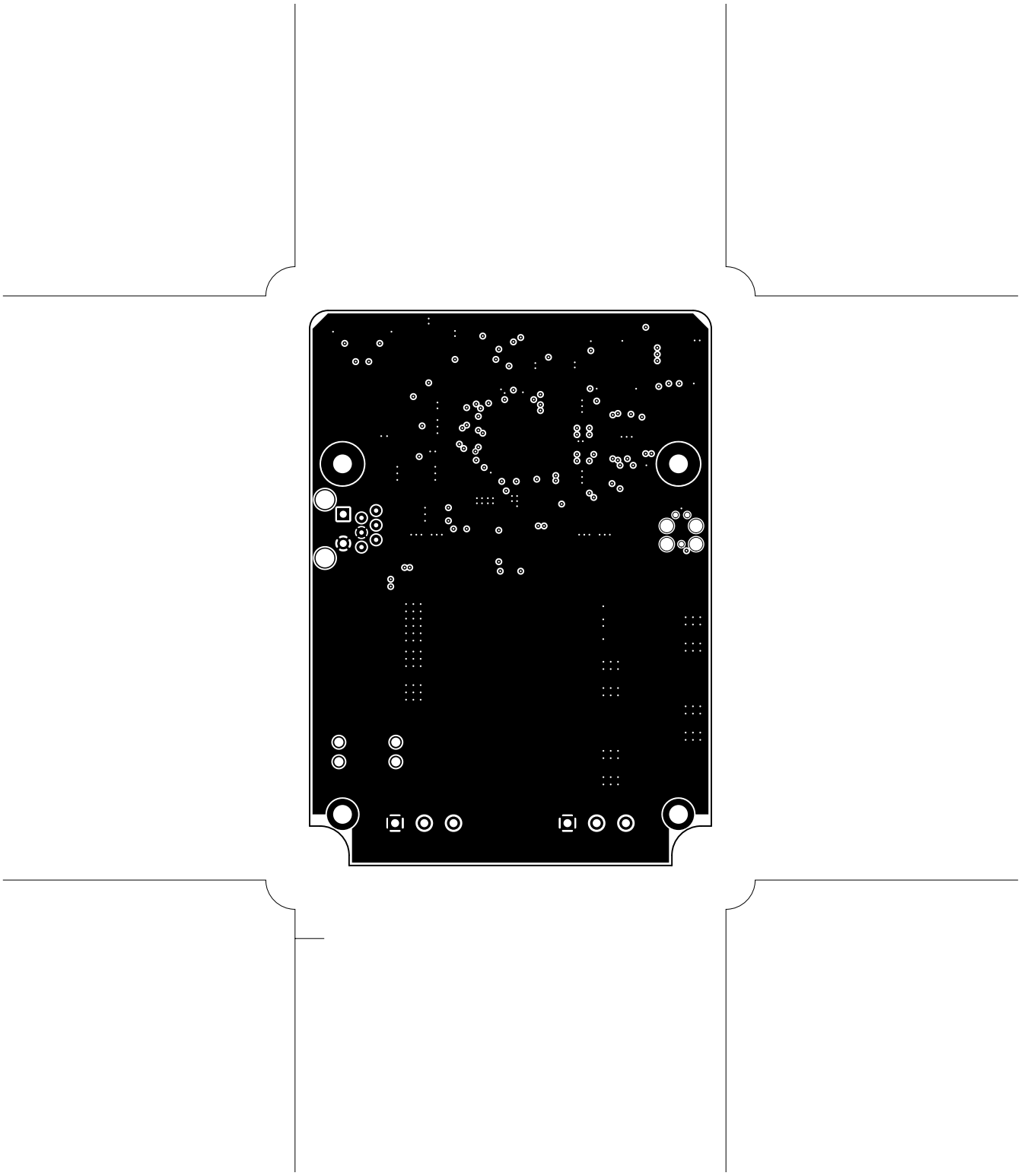
1. Table of Contents (This Page)
   Created on Sun Dec 30 12:41:08 2018
2. component
3. GND
4. VDD
5. solder
6. outline
7. plated-drill
8. unplated-drill
9. componentmask
10. soldermask
11. topsilk
12. bottomsilk
13. toppaste
14. topassembly
15. bottomassembly
16. fab

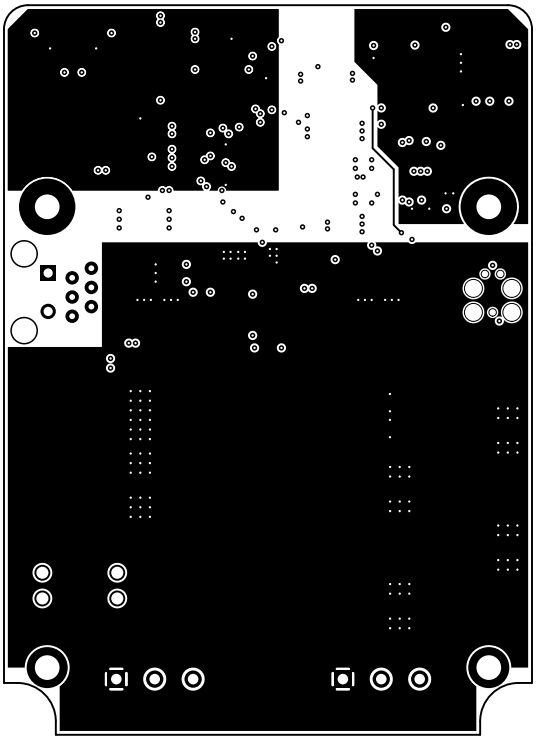
ckt-sbc.pcb



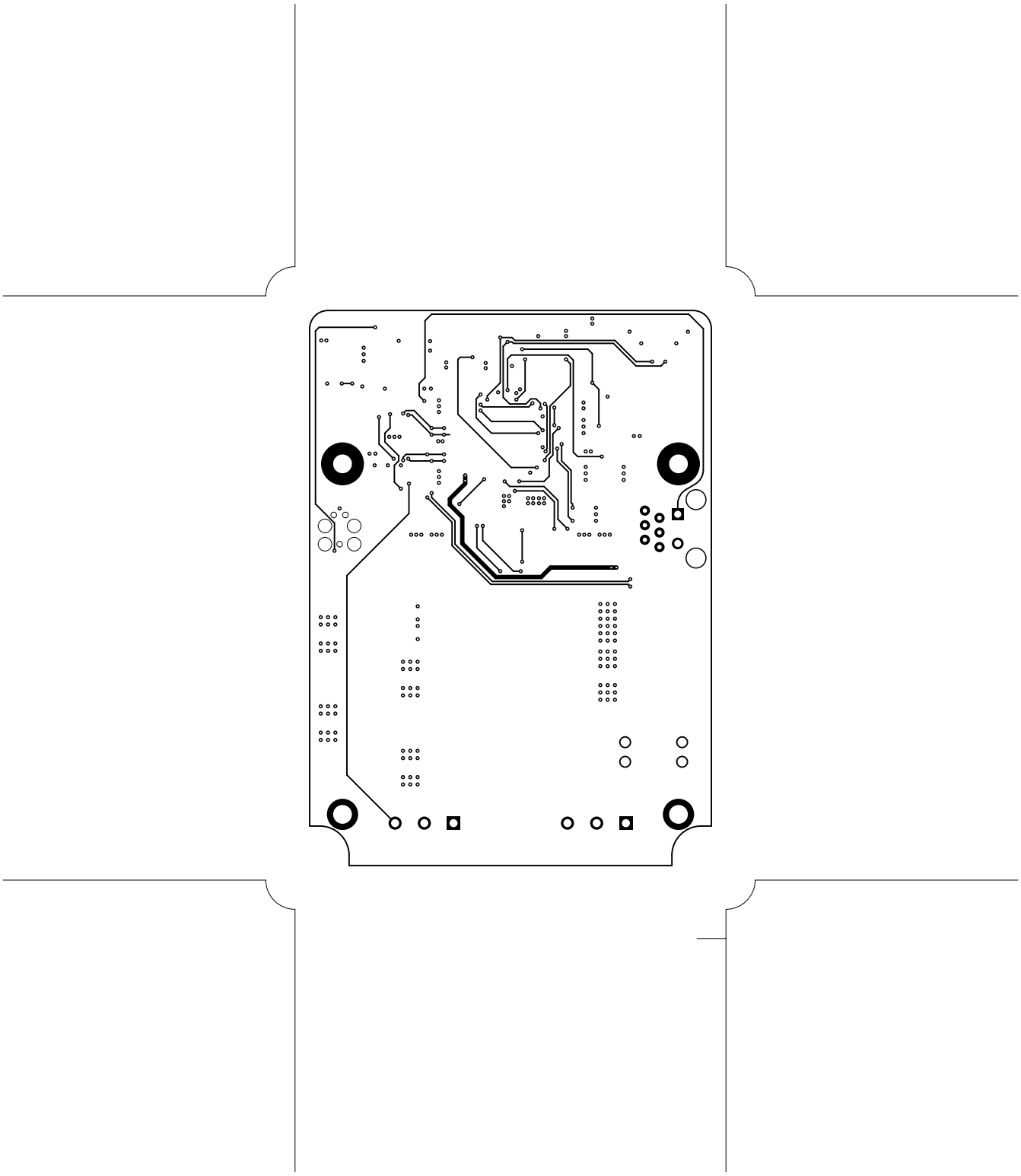
top, scale = 1:1.000
ckt-sbc.pcb



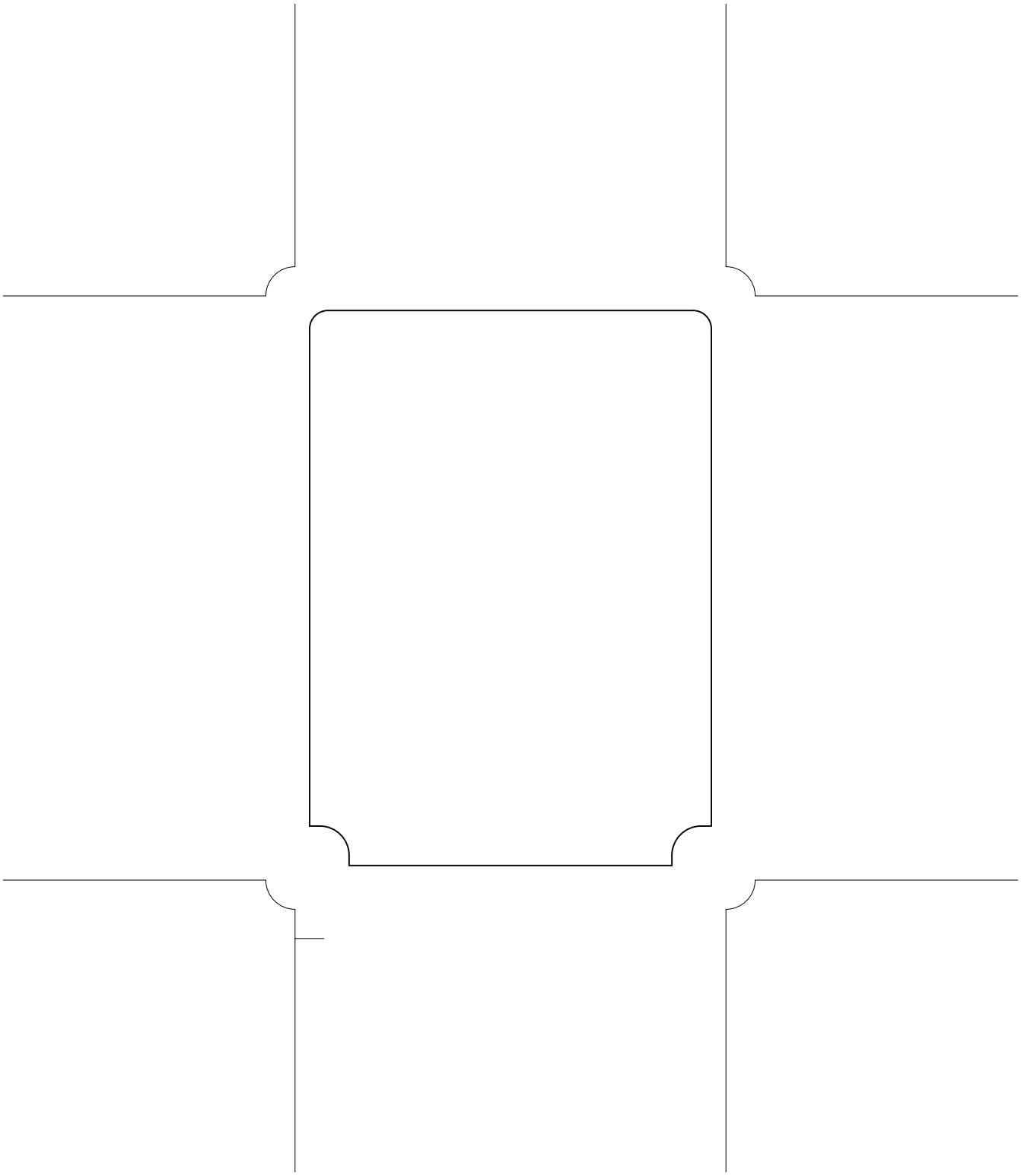
group1, scale = 1:1.000
ckt-sbc.pcb



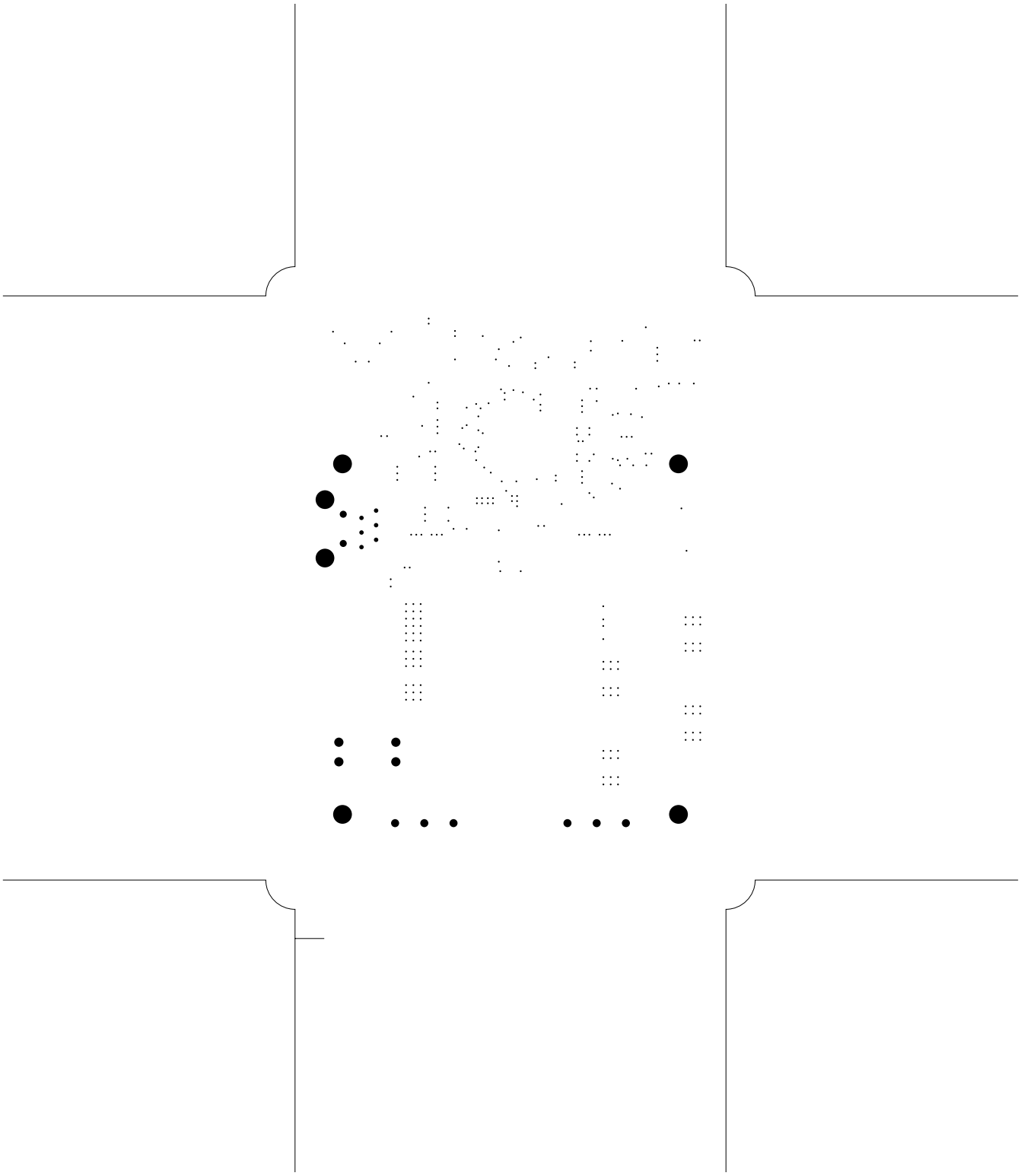
group2, scale = 1:1.000
ckt-sbc.pcb



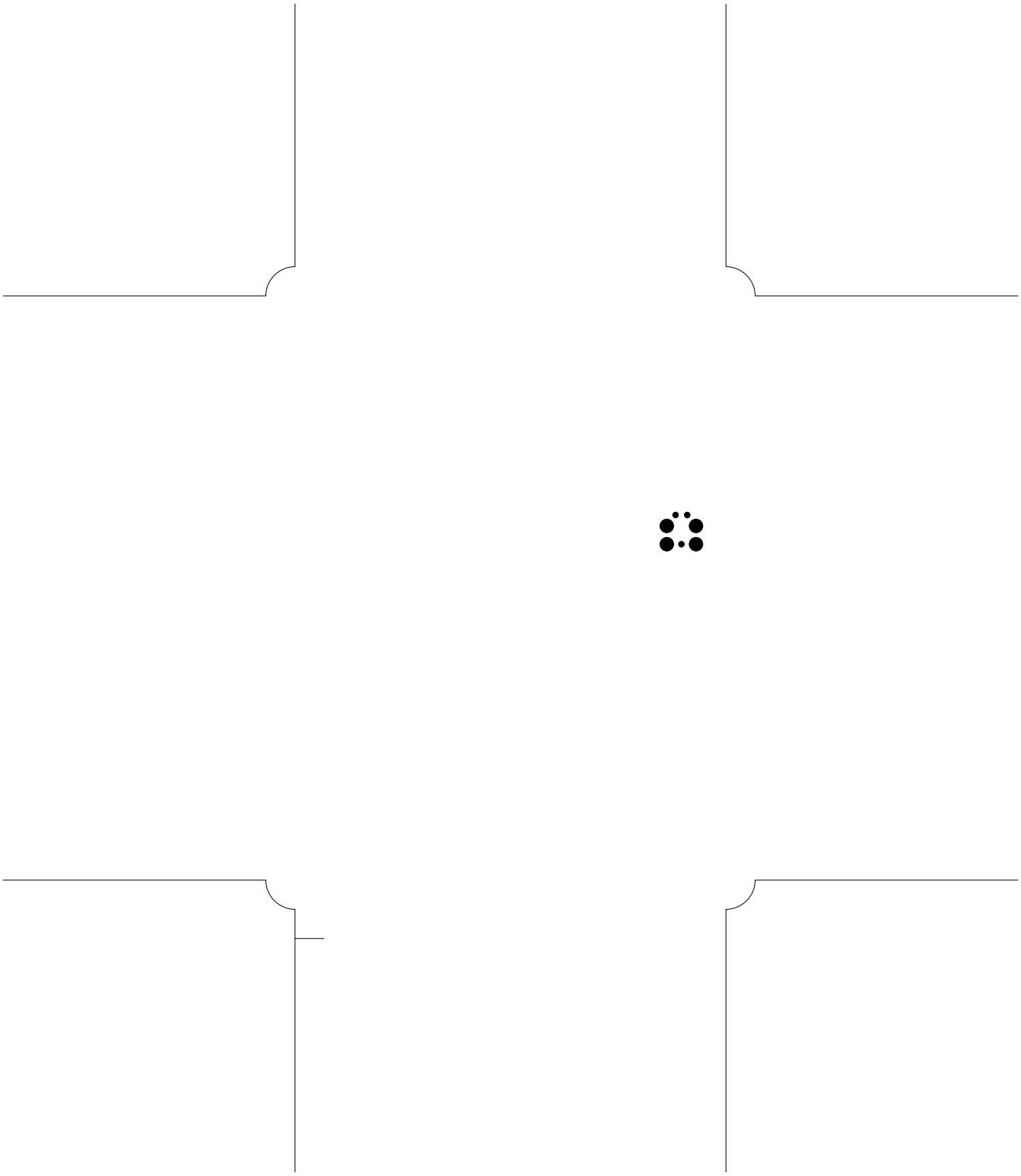
bottom (mirrored), scale = 1:1.000
ckt-sbc.pcb



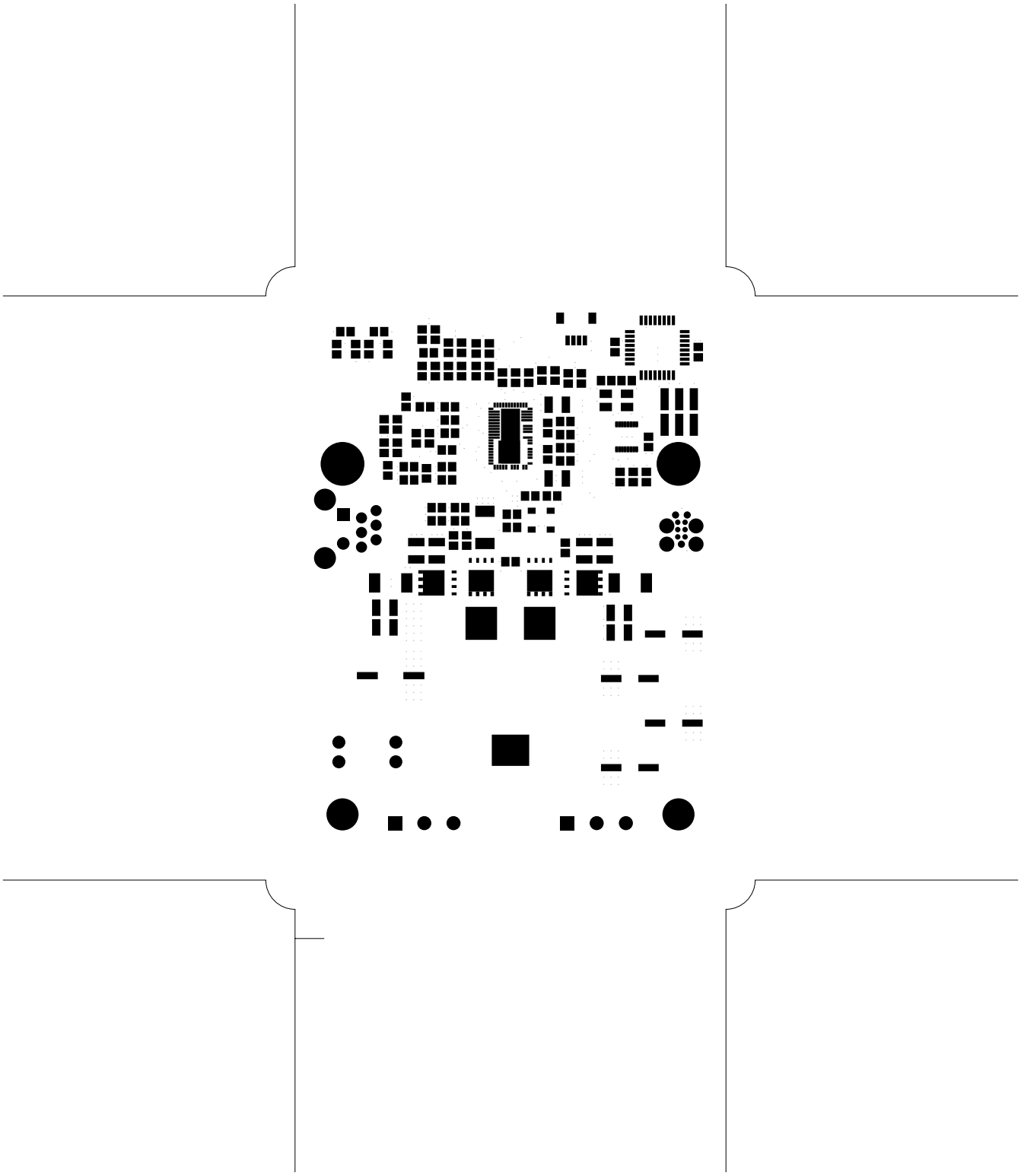
outline, scale = 1:1.000
ckt-sbc.pcb



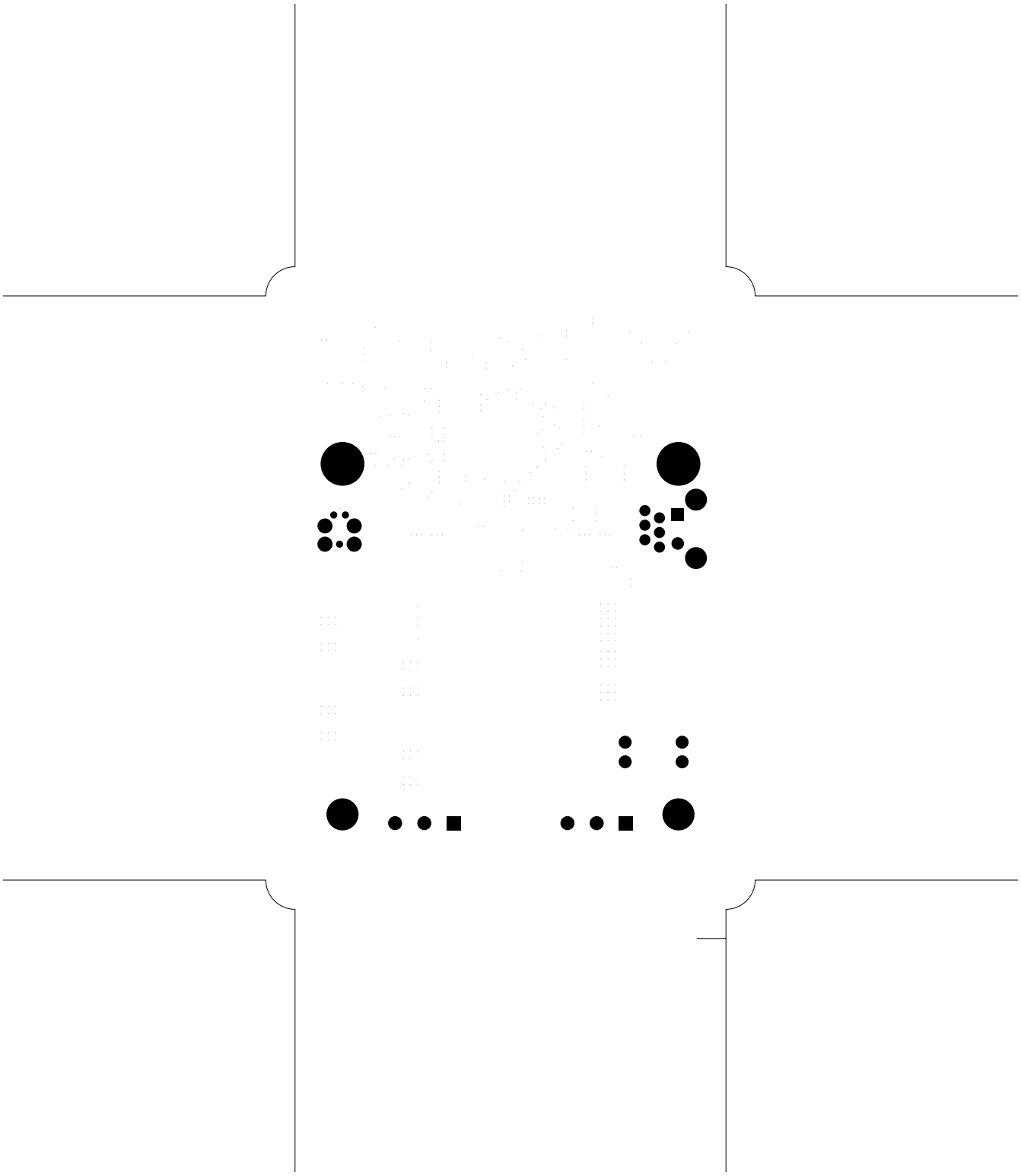
plated-drill, scale = 1:1.000
ckt-sbc.pcb



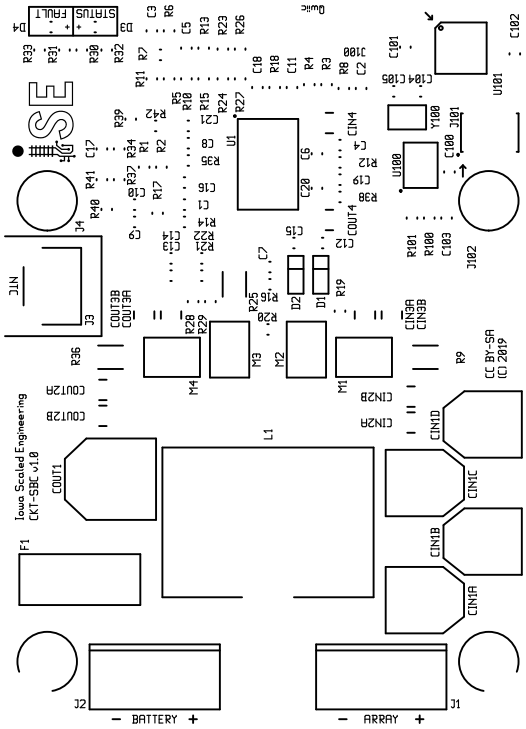
unplated-drill, scale = 1:1.000
ckt-sbc.pcb

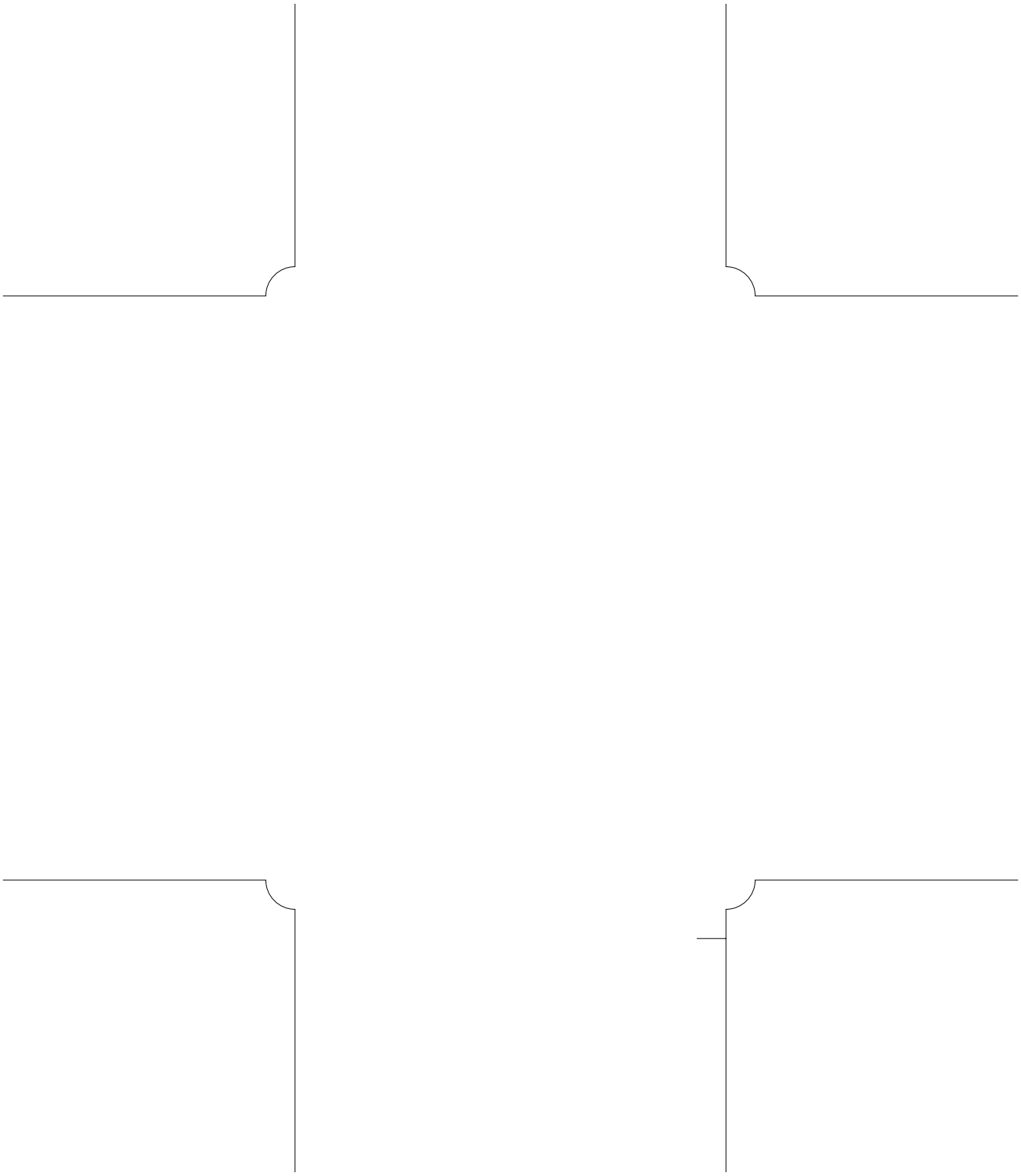


topmask, scale = 1:1.000
ckt-sbc.pcb

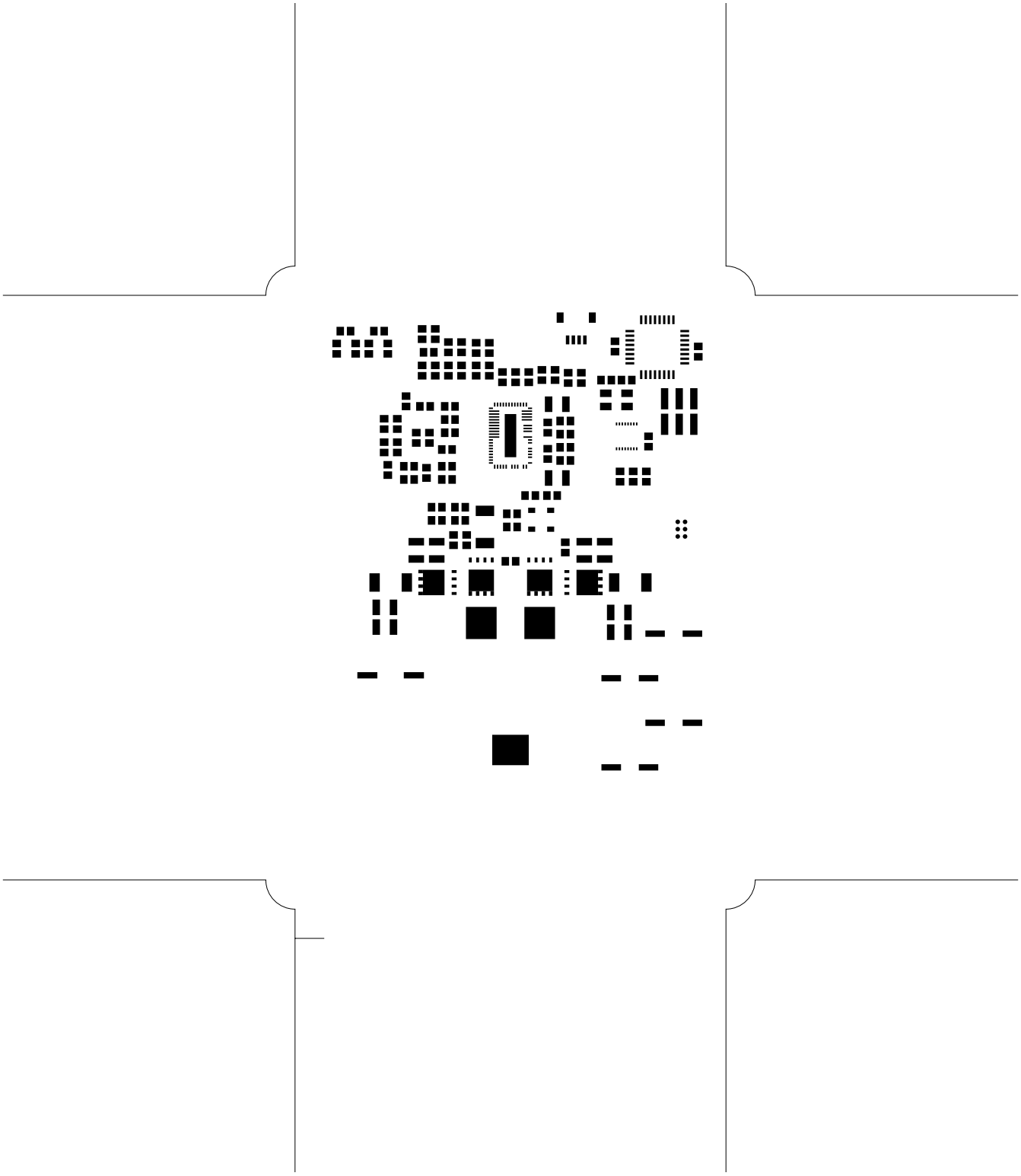


bottommask (mirrored), scale = 1:1.000
ckt-sbc.pcb

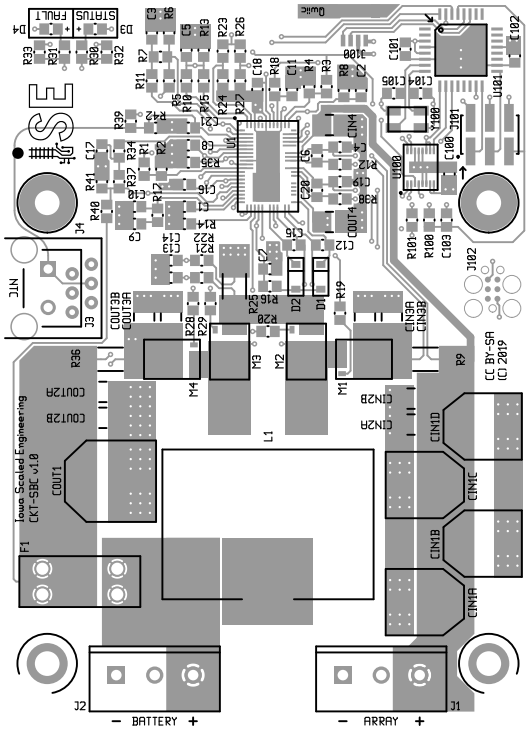

Iowa Scaled Engineering
CKT-SBC v1.0
CC BY-SA
(C) 2019
J1
J2
J3
J4
F1
L1
U1
U100
U101
Y100
J100
J101
J102
J103
J104
J105
J106
J107
J108
J109
J110
J111
J112
J113
J114
J115
J116
J117
J118
J119
J120
J121
J122
J123
J124
J125
J126
J127
J128
J129
J130
J131
J132
J133
J134
J135
J136
J137
J138
J139



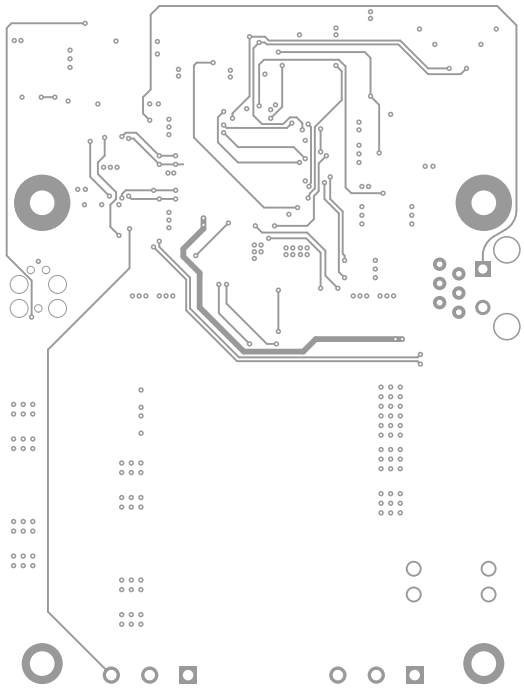
bottomsilk (mirrored), scale = 1:1.000
ckt-sbc.pcb



toppaste, scale = 1:1.000
ckt-sbc.pcb



topassembly, scale = 1:1.000
ckt-sbc.pcb



bottomassembly (mirrored), scale = 1:1.000
ckt-sbc.pcb

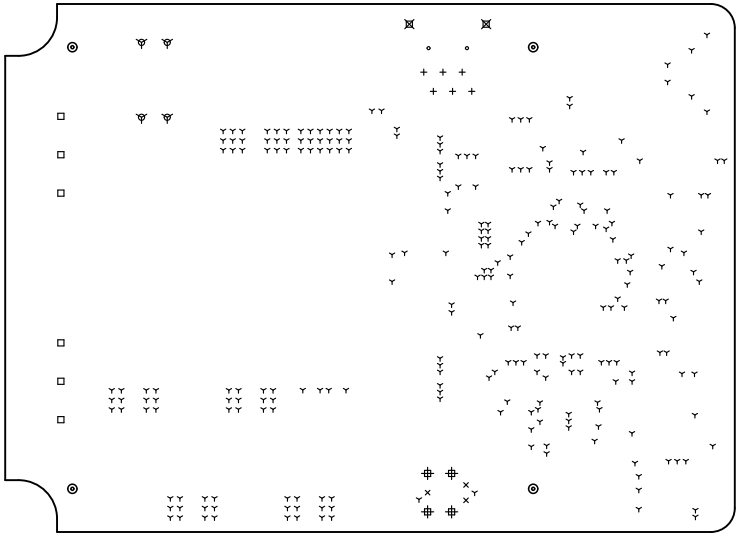
There are 9 different drill sizes used in this layout, 288 holes total

Symbol	Diam. (Inch)	Count	Plated?
⊤	0.012	257	YES
+	0.030	6	YES
×	0.039	3	NO
∘	0.048	2	YES
□	0.055	6	YES
⊕	0.063	4	YES
⌖	0.094	4	NO
⊠	0.128	2	YES
◎	0.129	4	YES

Title: (unknown) - Fabrication Drawing

Author: Michael Petersen

Date: Sun 30 Dec 2018 07:41:08 PM GMT UTC



Board outline is the centerline of this path

fab, scale = 1:1.000
ckt-sbc.pcb