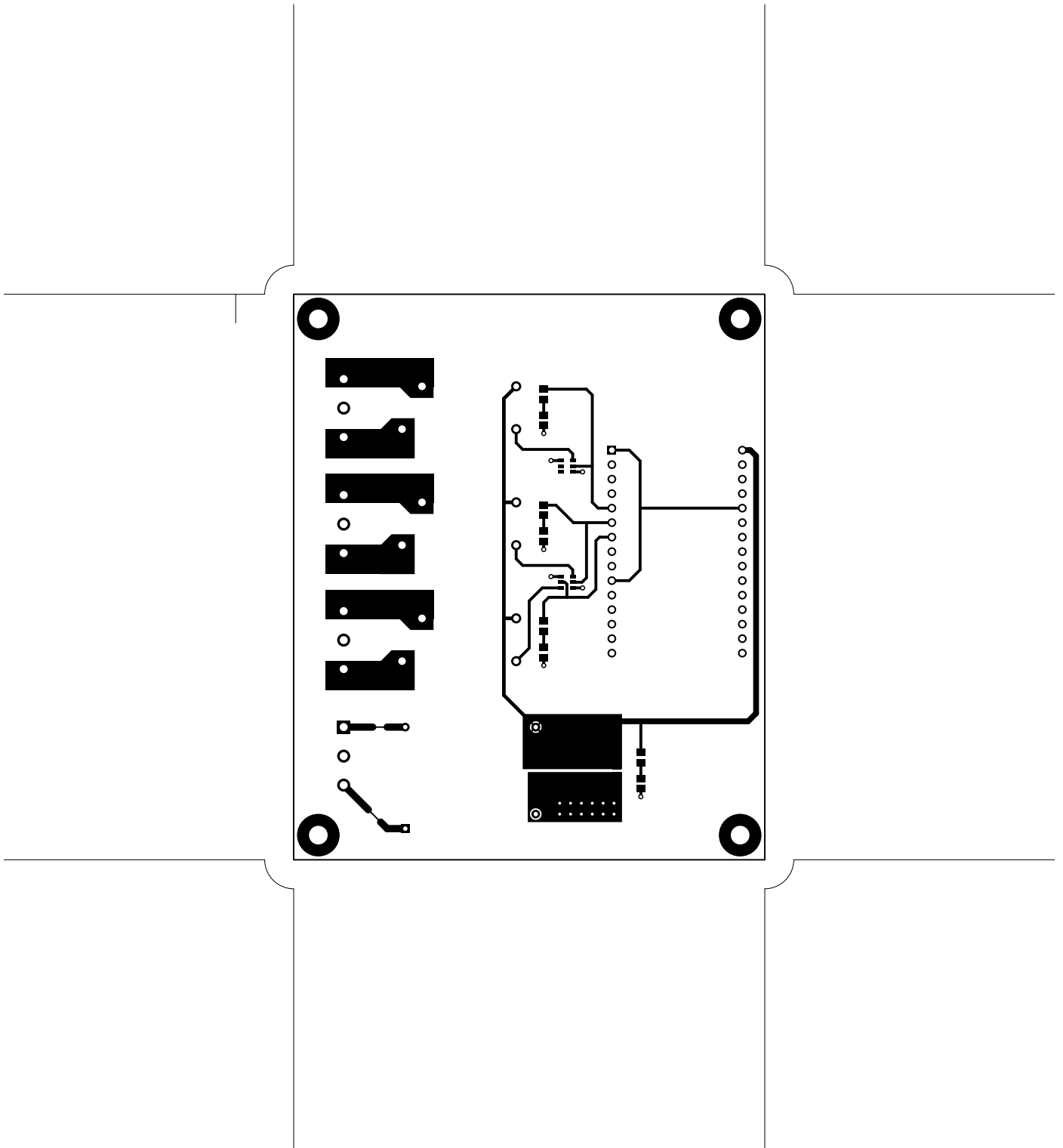
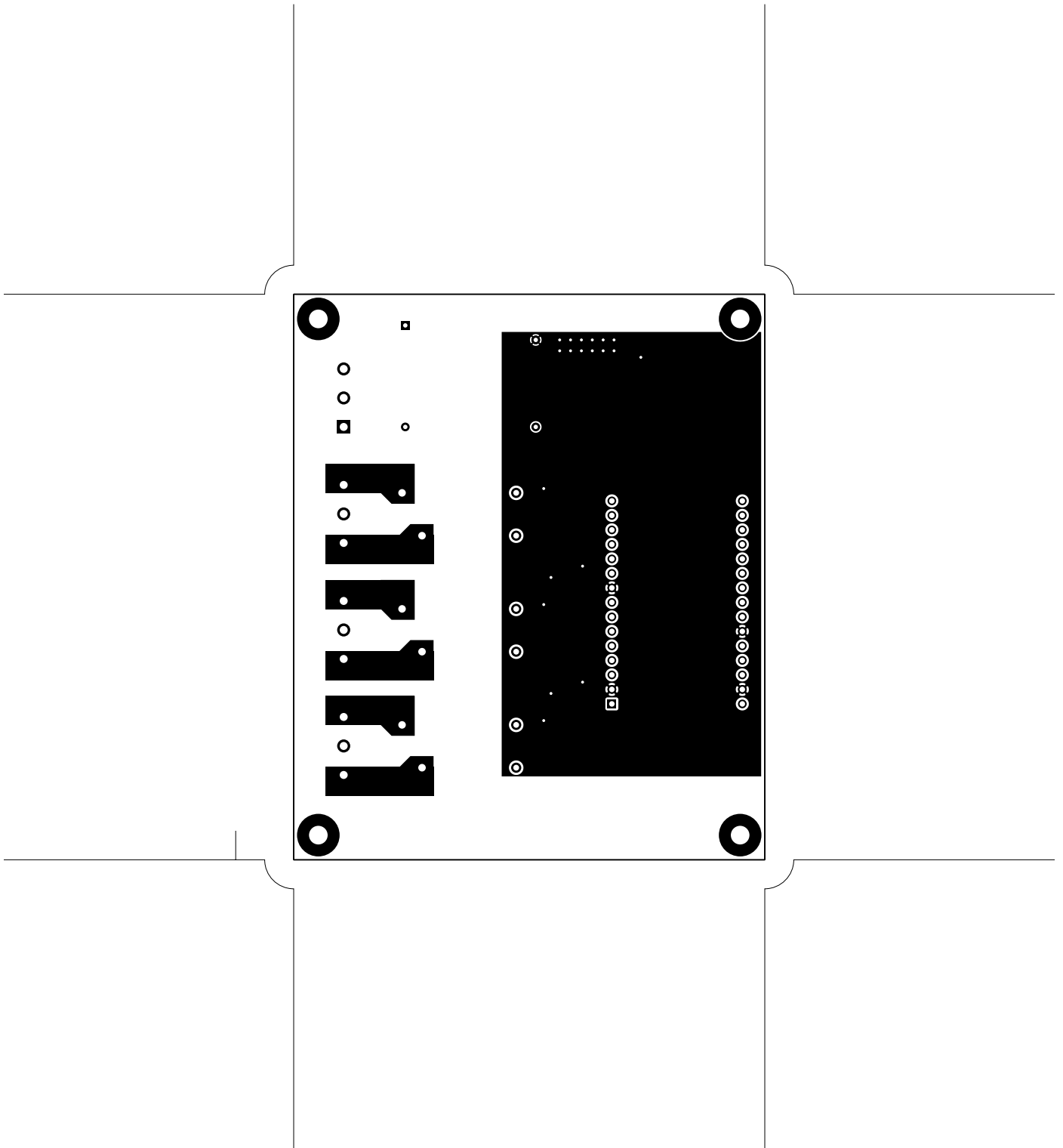


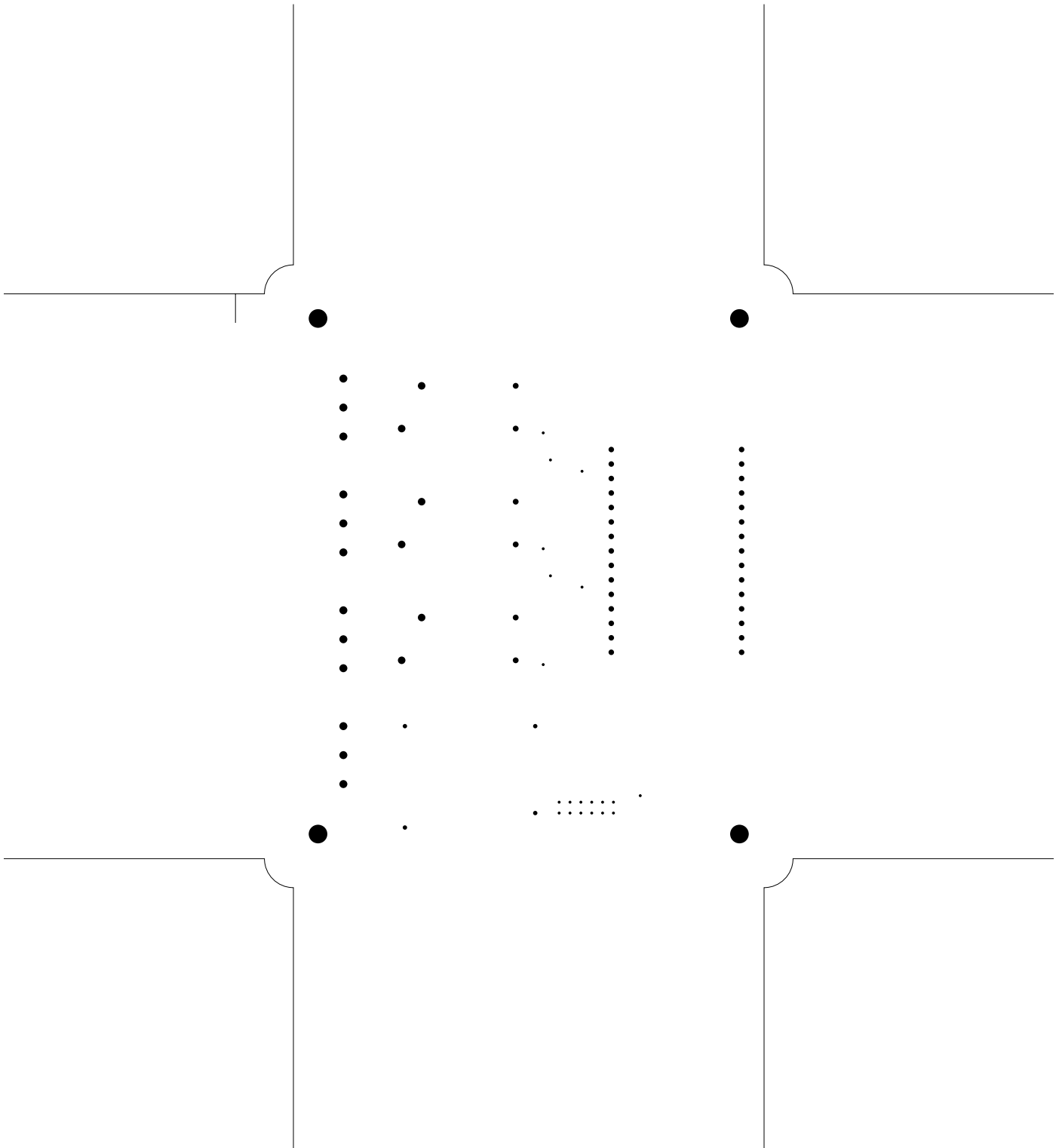
1. Table of Contents (This Page)
Created on Thu Dec 31 09:53:57 2020

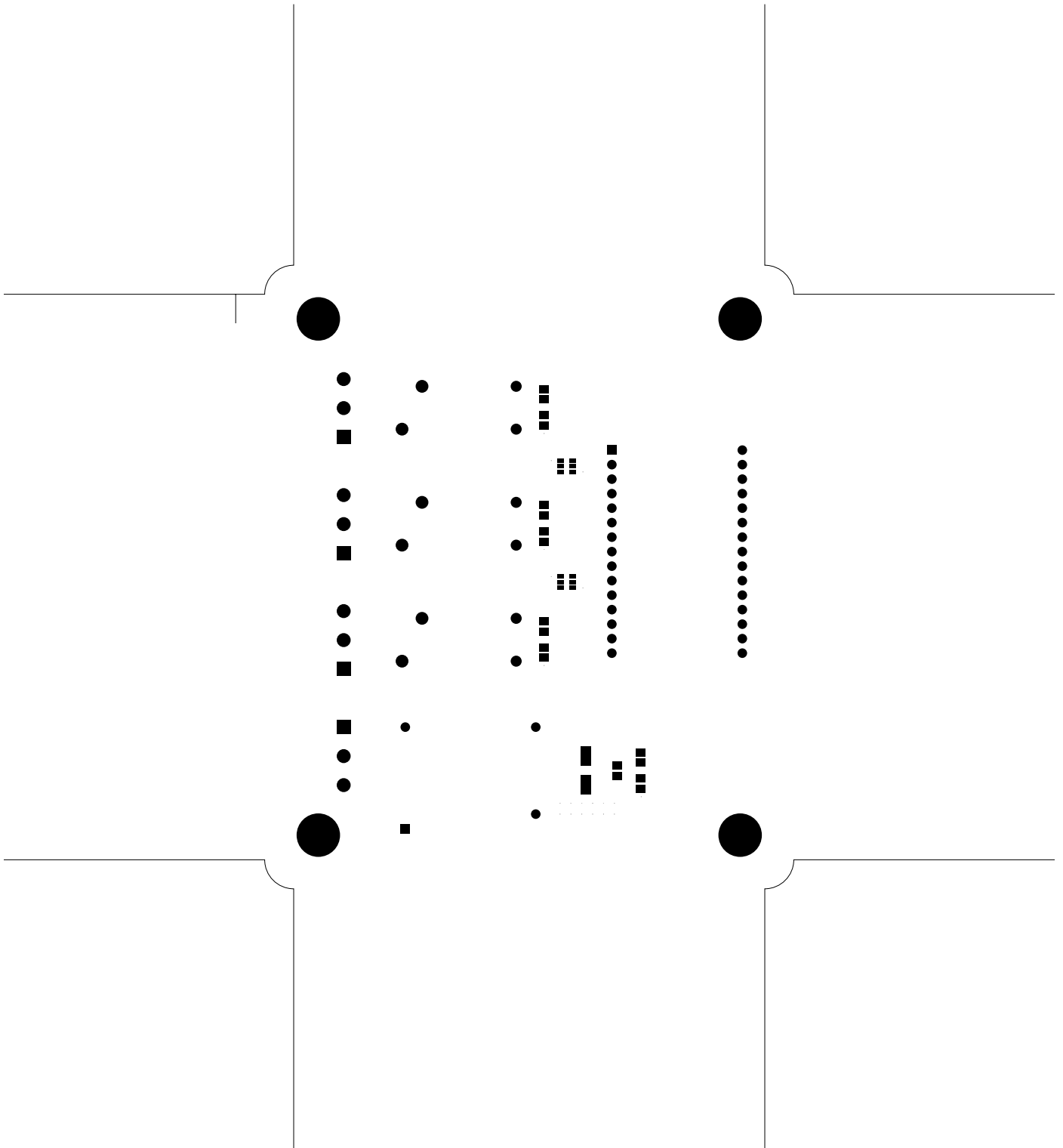
2. top
3. bottom
4. plated-drill
5. componentmask
6. soldermask
7. topsilk
8. bottomsilk
9. toppaste
10. topassembly
11. bottomassembly
12. fab

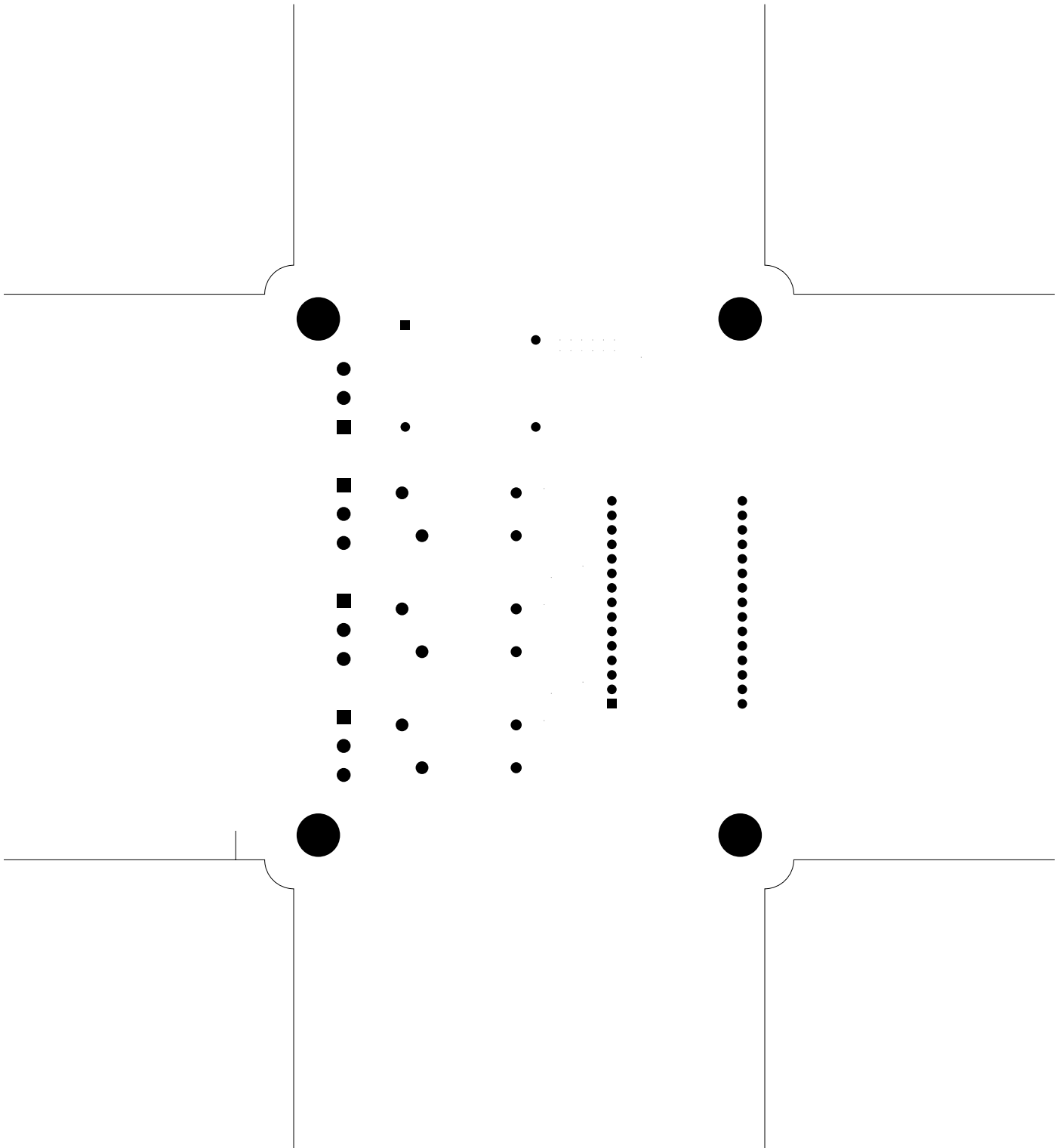




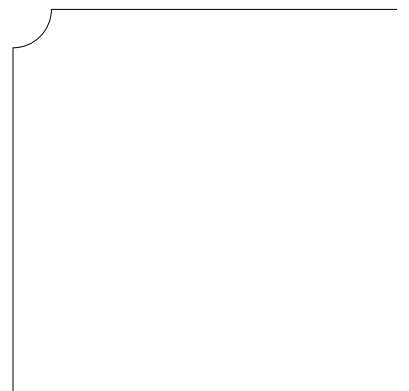
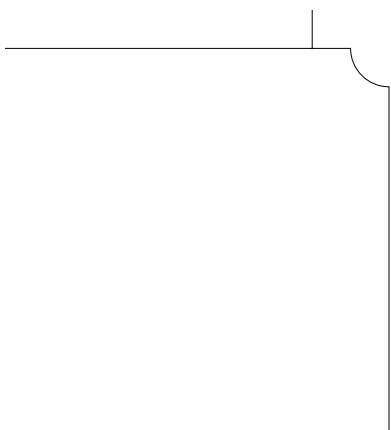
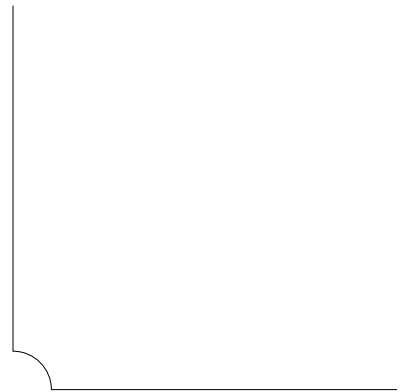
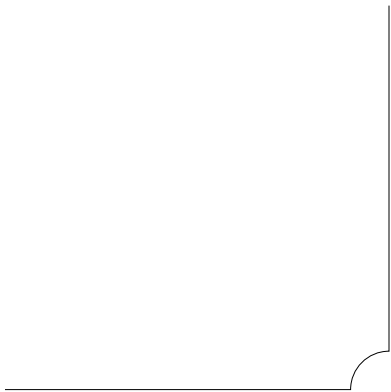
bottom (mirrored), scale = 1:1.000
esp-acsw3.pcb

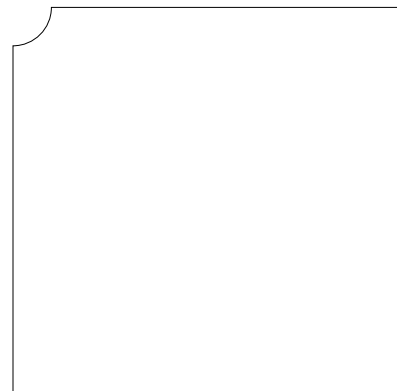
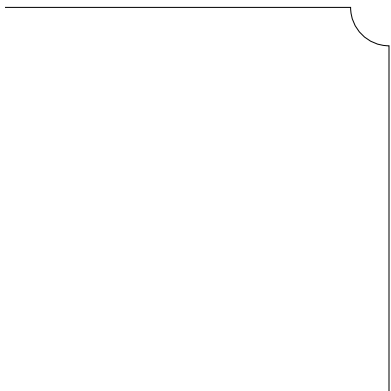
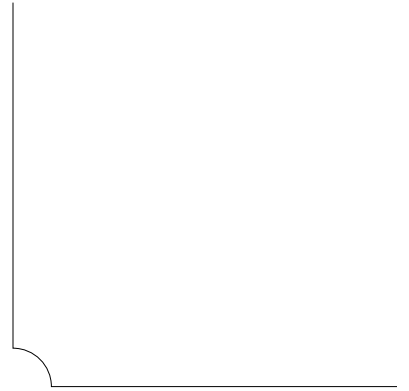
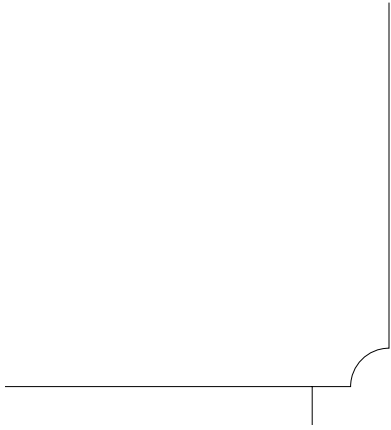


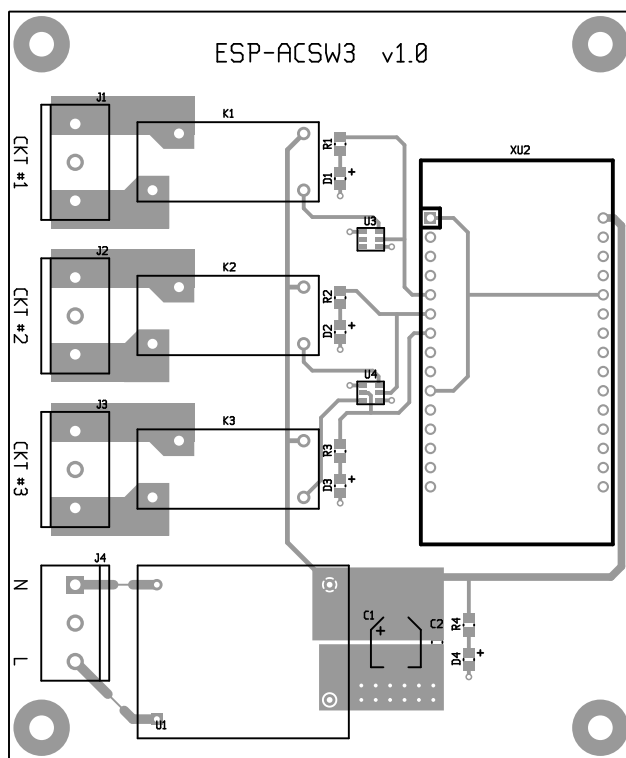


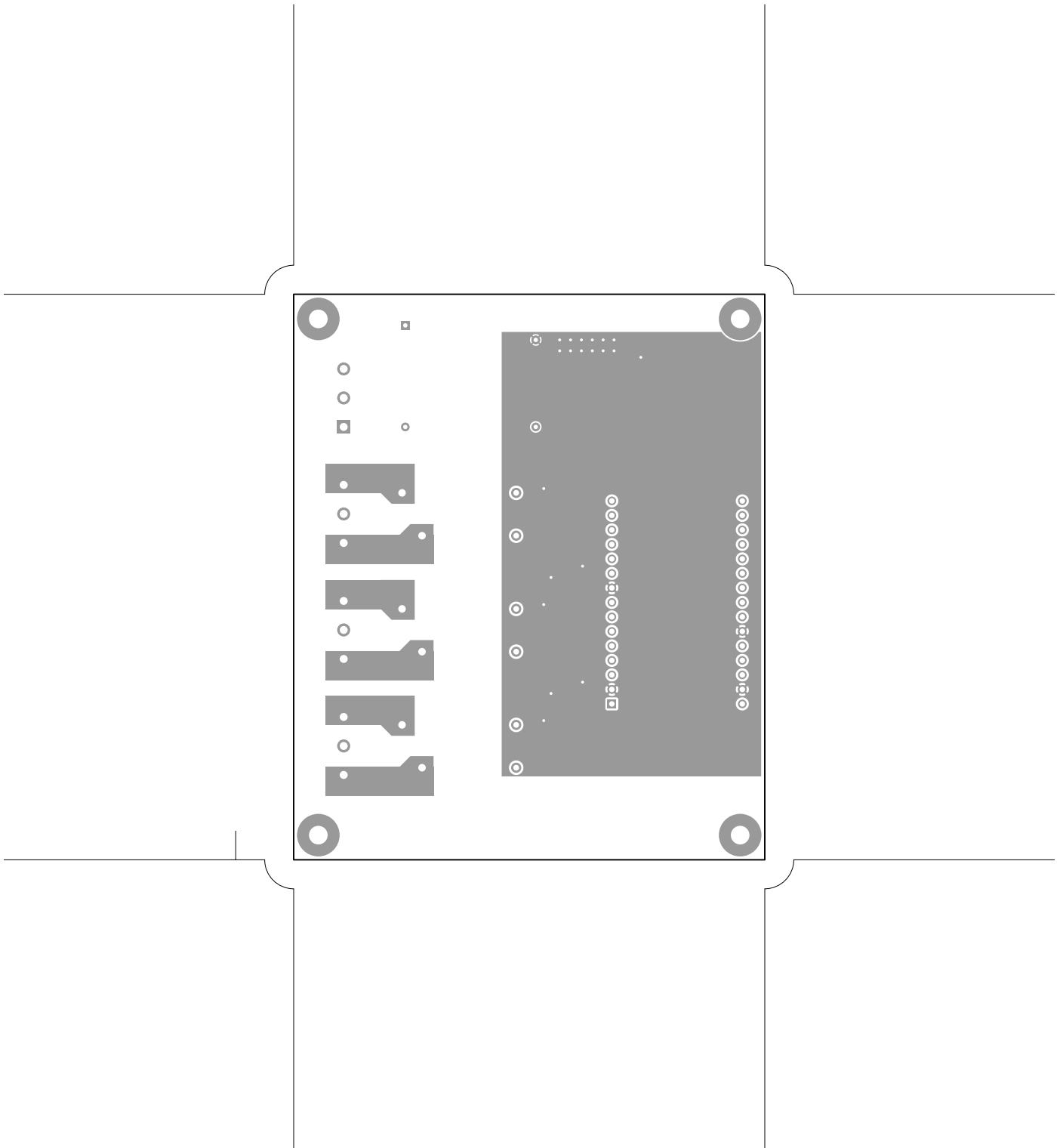


The diagram illustrates the proposed system architecture. It features a large rectangular block labeled 'XU2' on the right side. To its left, there are three smaller rectangular blocks labeled 'K1', 'K2', and 'K3' stacked vertically. To the left of these are three more rectangular blocks labeled 'J1', 'J2', and 'J3' stacked vertically. To the left of these are three more rectangular blocks labeled 'CKT #1', 'CKT #2', and 'CKT #3' stacked vertically. To the left of these are three more rectangular blocks labeled 'N', 'L', and 'U1' stacked vertically. The diagram also includes various labels like 'R1', 'D1', 'U3', 'R2', 'D2', 'U4', 'R3', 'D3', 'C1', 'C2', 'R4', 'D4', and 'U1' with associated symbols like '+' and '-'.









bottomassembly (mirrored), scale = 1:1.000
esp-acsw3.pcb

There are 7 different drill sizes used in this layout, 82 holes total

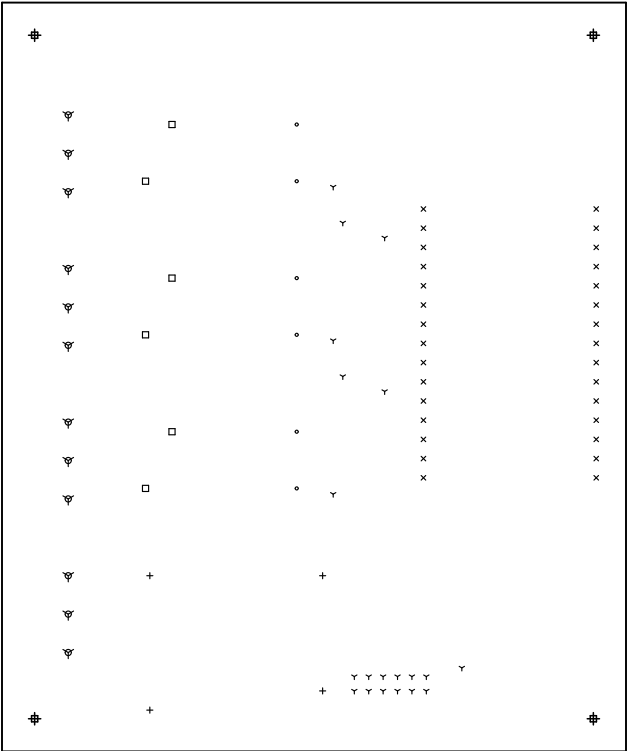
Symbol	Diam. (Inch)	Count	Plated?
γ	0.020	20	YES
+	0.030	4	YES
×	0.038	30	YES
•	0.040	6	YES
□	0.052	6	YES
▽	0.055	12	YES
⊕	0.129	4	YES

Title: (unknown) - Fabrication Drawing

Author: Michael Petersen

Date: Thu Dec 31 16:53:57 2020 UTC

Maximum Dimensions: 3250.000000 mils wide, 3900.000000 mils high



Board outline is the centerline of this 8.000000 mil rectangle - 0,0 to 3250.000000,3900.000000 mils