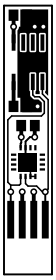
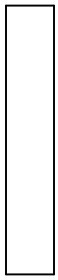
1. Table of Contents (This Page)
   Created on Fri Jan 29 20:30:32 2016
2. top
3. bottom
4. outline
5. plated-drill
6. componentmask
7. soldermask
8. topsilk
9. bottomsilk
10. toppaste
11. topassembly
12. bottomassembly
13. fab



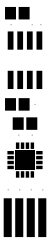
top, scale = 1:1.000
i2c-wxsns.pcb

bottom (mirrored), scale = 1:1.000
i2c-wxsns.pcb



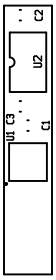
outline, scale = 1:1.000
i2c-wxsns.pcb

plated-drill, scale = 1:1.000
i2c-wxsns.pcb



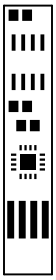
topmask, scale = 1:1.000
i2c-wxsns.pcb

bottommask (mirrored), scale = 1:1.000
i2c-wxsns.pcb

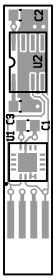


topsilk, scale = 1:1.000
i2c-wxsns.pcb

bottomsilk (mirrored), scale = 1:1.000
i2c-wxsns.pcb



toppaste, scale = 1:1.000
i2c-wxsns.pcb



topassembly, scale = 1:1.000
i2c-wxsns.pcb

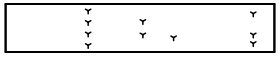
bottomassembly (mirrored), scale = 1:1.000
i2c-wxsns.pcb

fab, scale = 1:1.000
i2c-wxsns.pcb

There are 1 different drill sizes used in this layout, 10 holes total

Symbol	Diam. (Inch)	Count	Plated?
+	0.020	10	YES

Title: (unknown) - Fabrication Drawing
Author: Michael Petersen
Date: Sat 30 Jan 2016 03:30:32 AM GMT UTC



Board outline is the centerline of this path