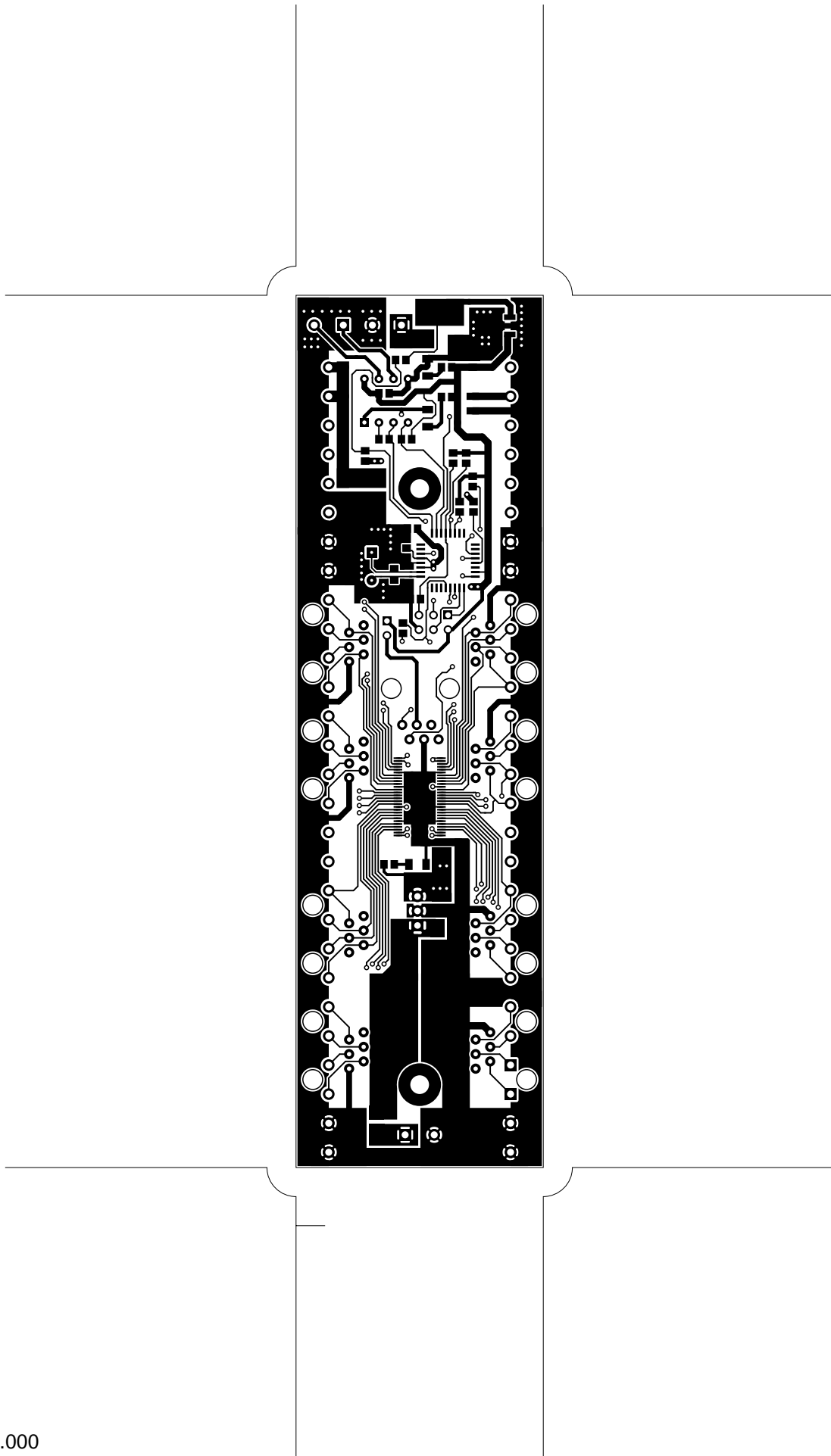
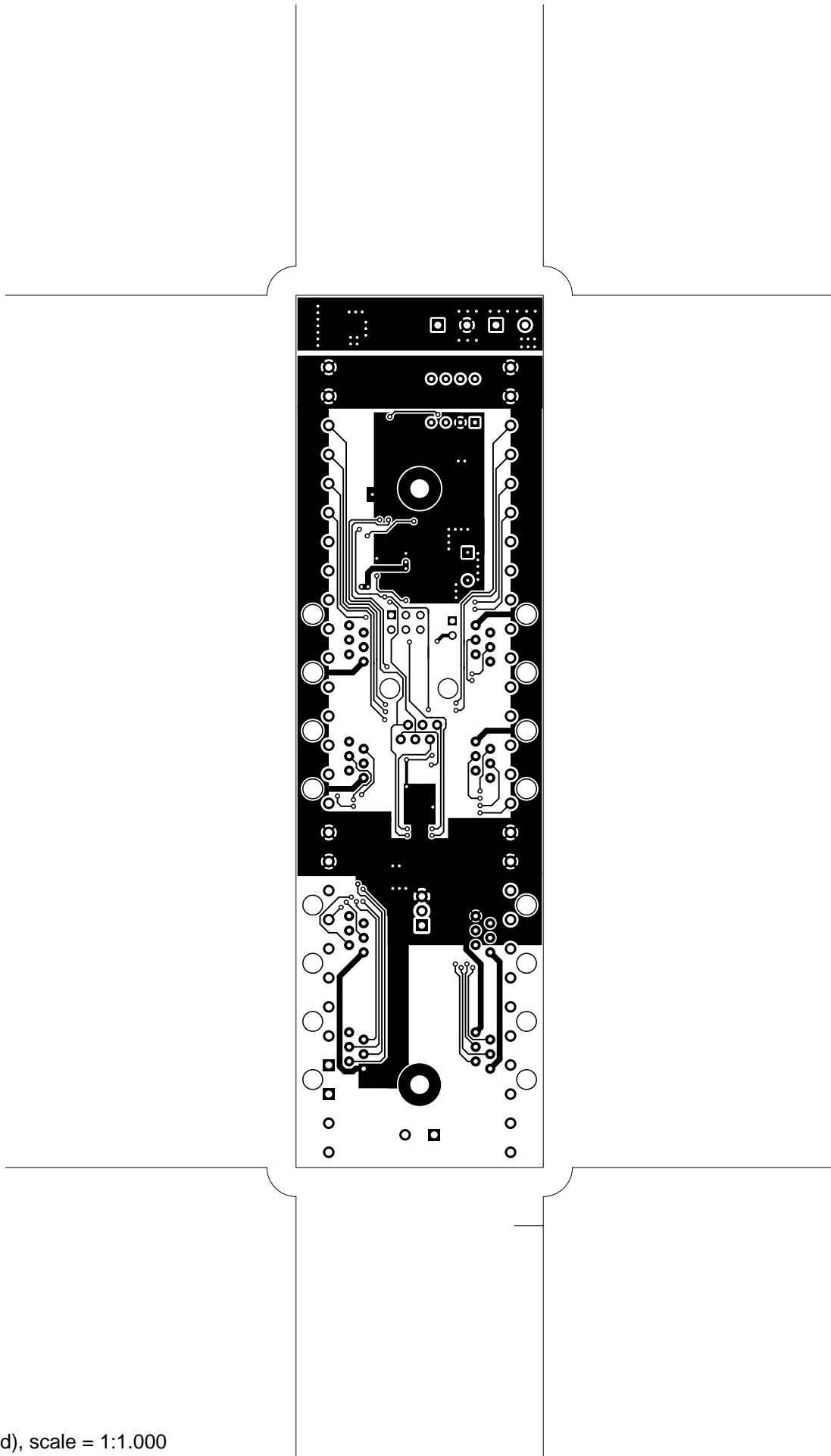


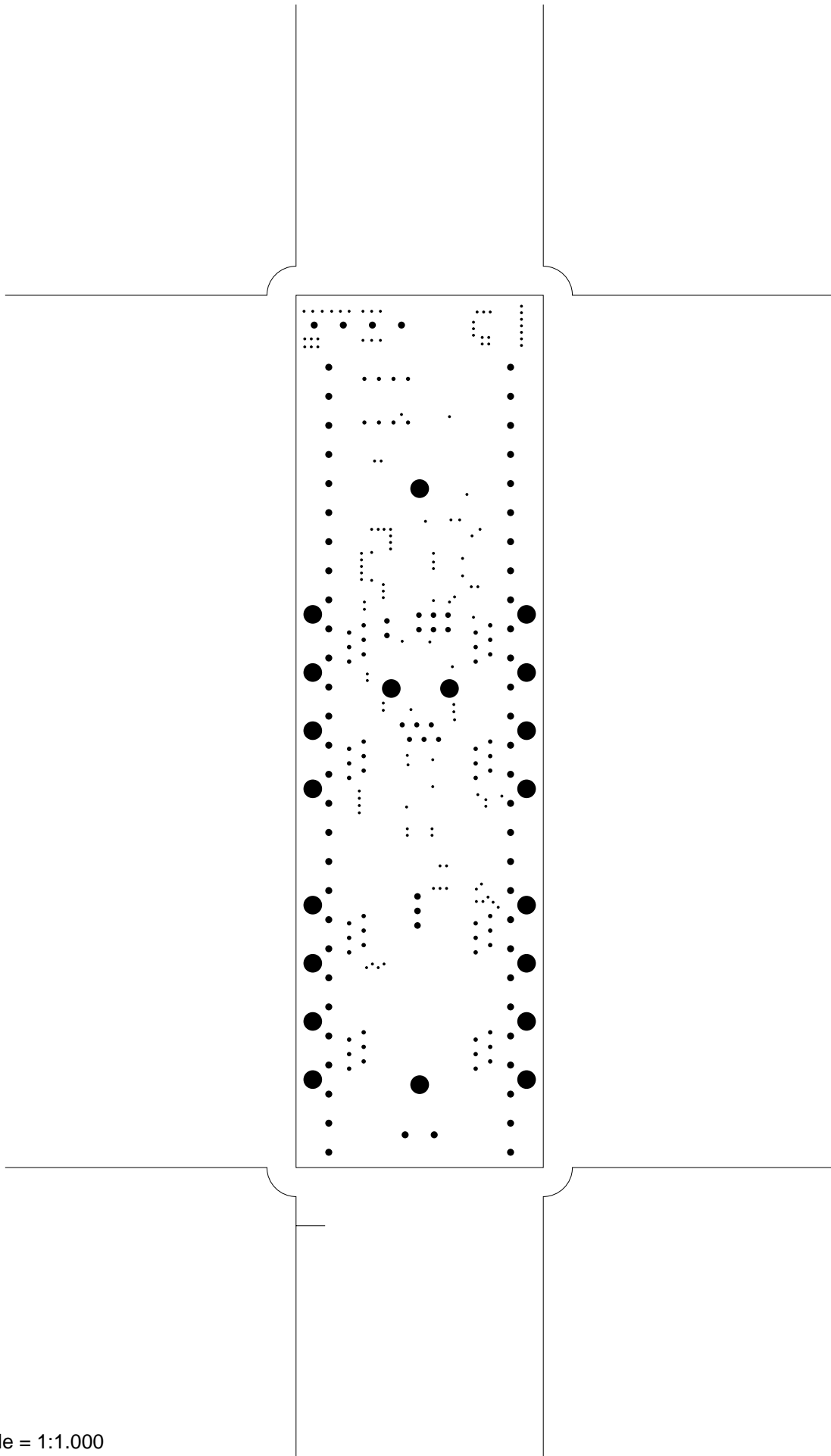
1. Table of Contents (This Page)
Created on Fri Jan 9 14:25:45 2015

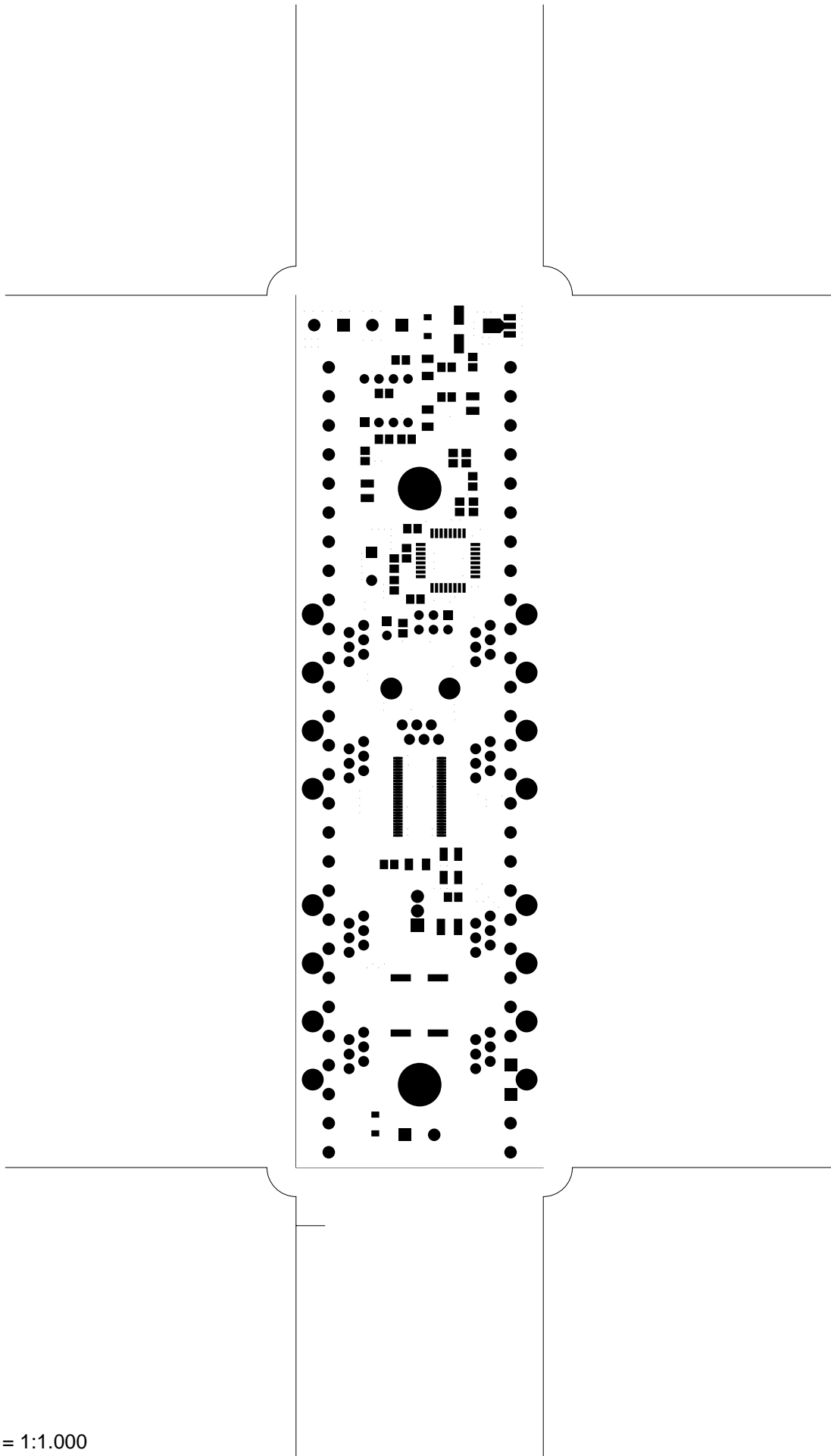
- 2. component
- 3. solder
- 4. plated-drill
- 5. componentmask
- 6. soldermask
- 7. topsilk
- 8. bottomsilk
- 9. toppaste
- 10. topassembly
- 11. bottomassembly
- 12. fab

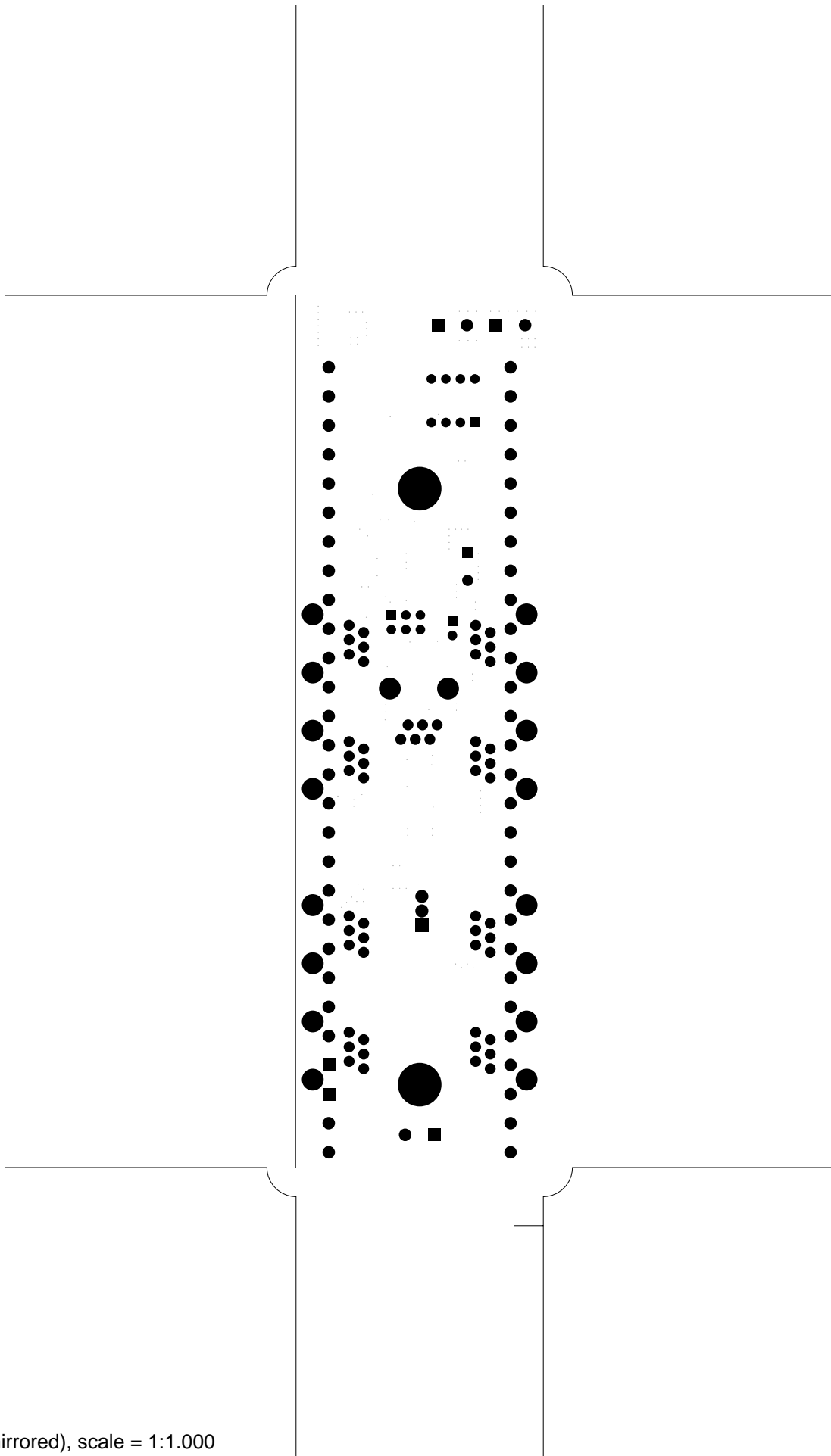




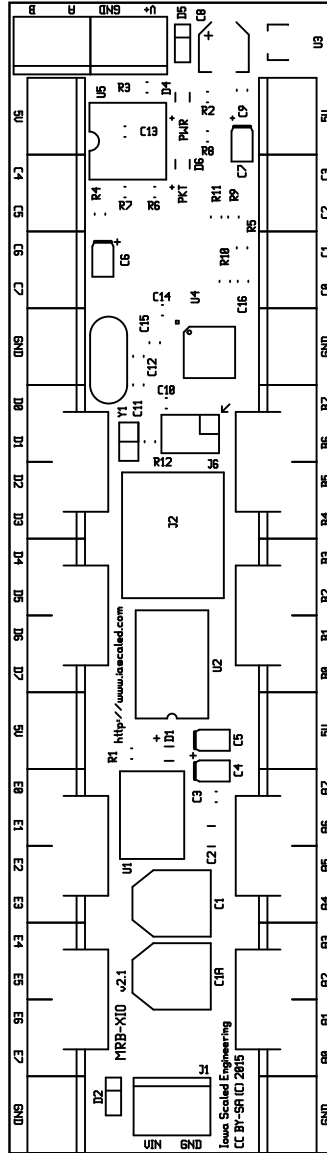
bottom (mirrored), scale = 1:1.000
mrb-xio.pcb

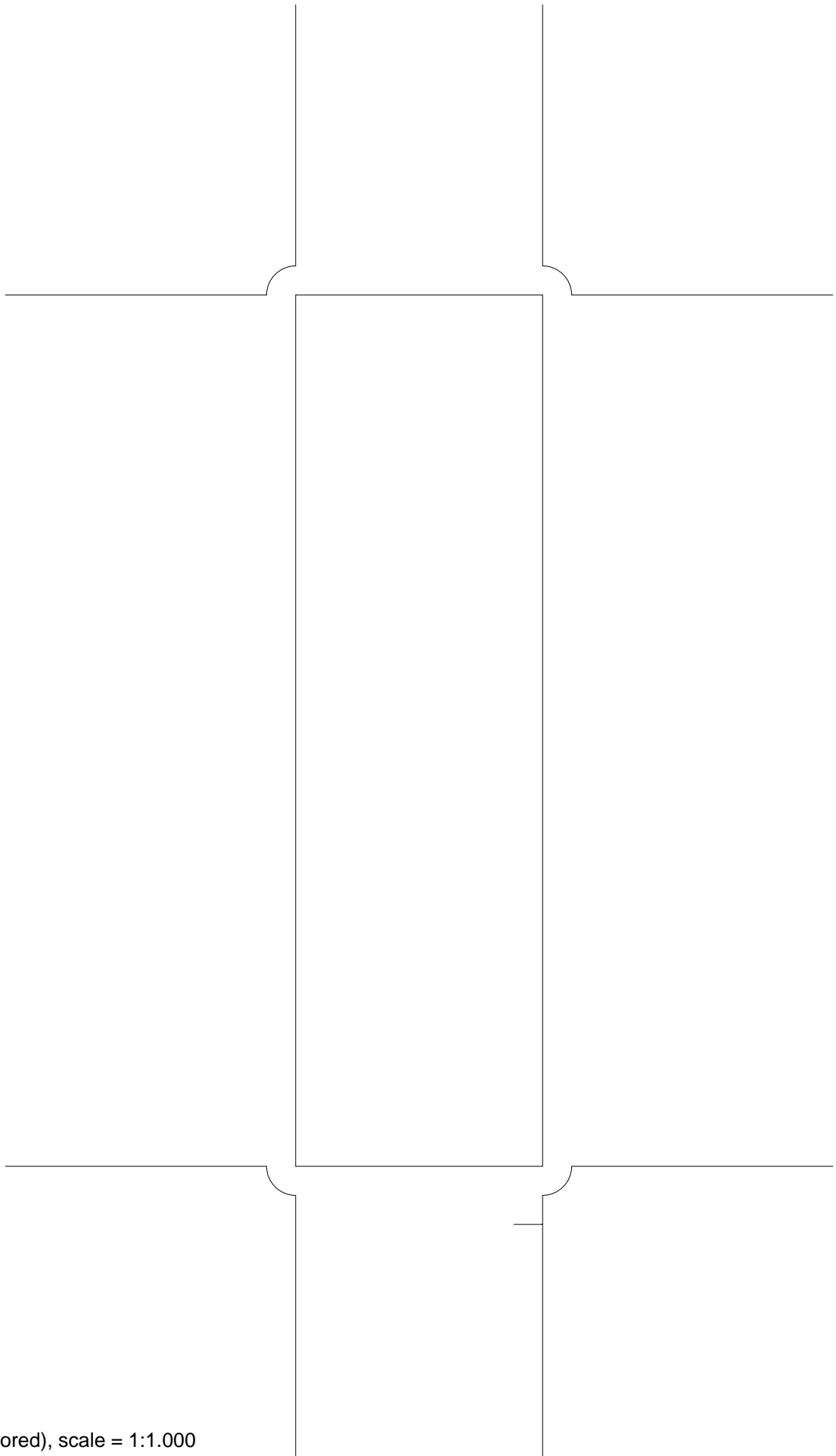




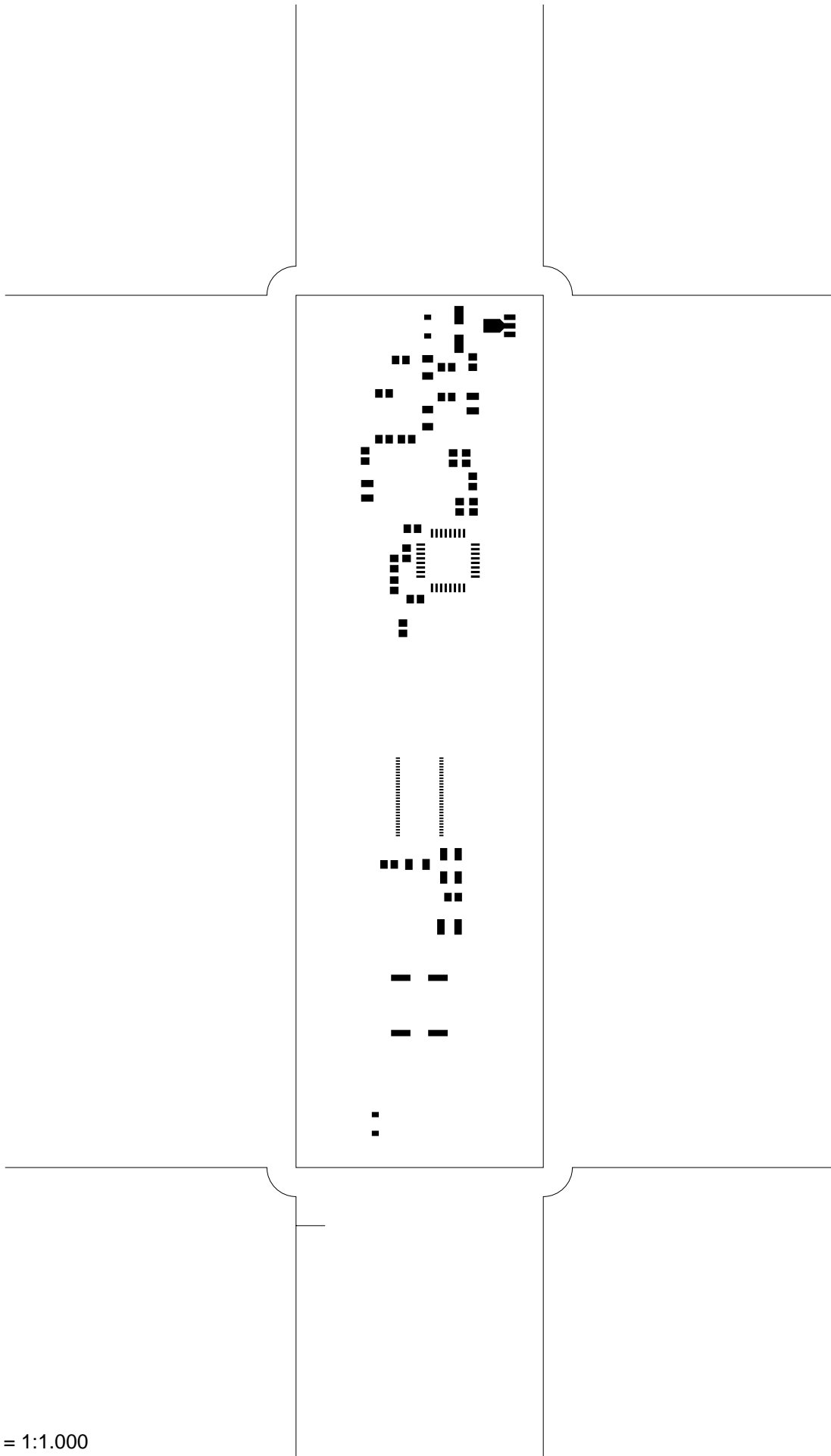


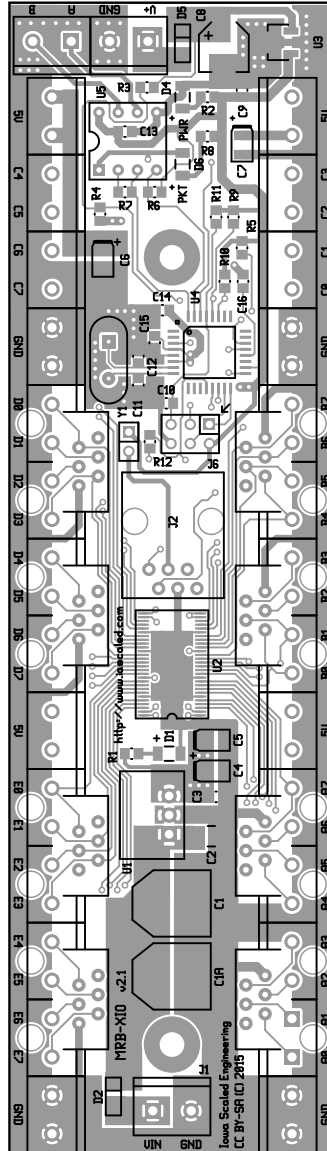
bottommask (mirrored), scale = 1:1.000
mrb-xio.pcb

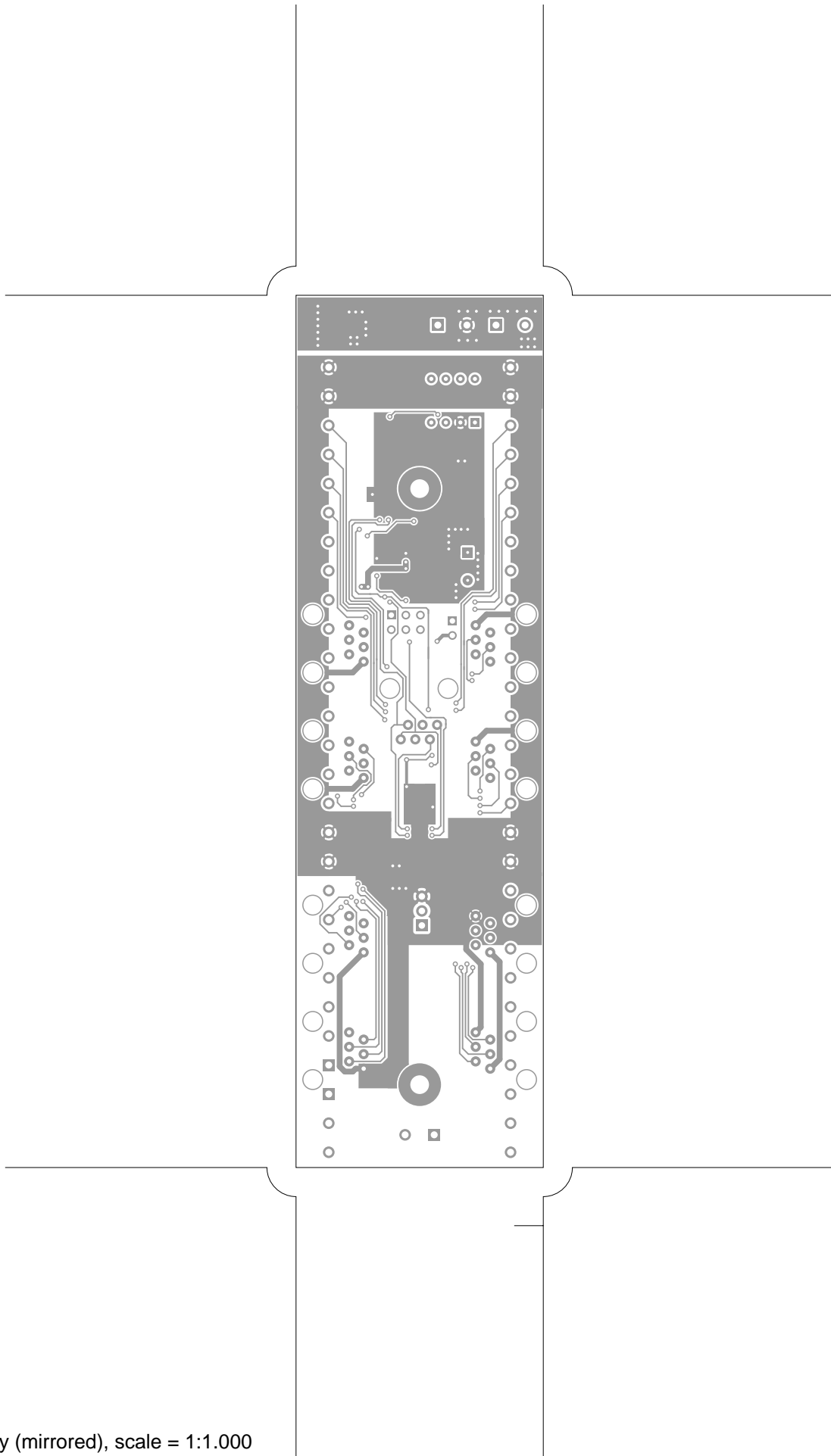




bottomsilk (mirrored), scale = 1:1.000
mrb-xio.pcb





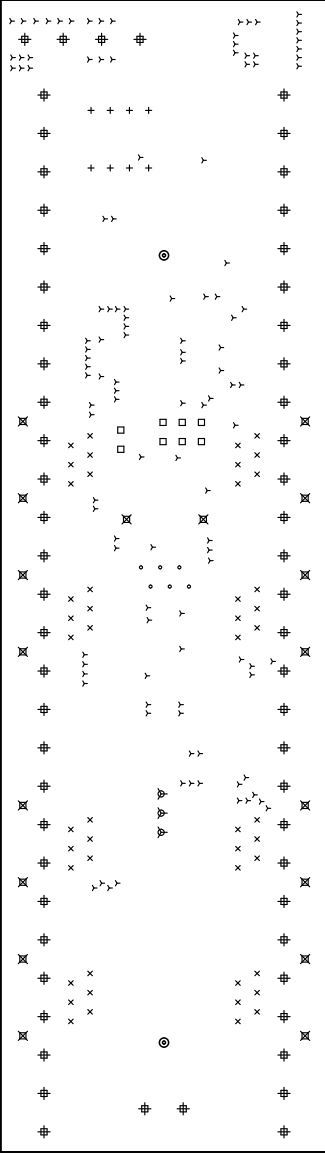


bottomassembly (mirrored), scale = 1:1.000
mrb-xio.pcb

There are 9 different drill sizes used in this layout, 274 holes total

Symbol	Diam. (Inch)	Count	Plated?
Y	0.020	119	YES
+	0.028	8	YES
x	0.030	48	YES
o	0.035	6	YES
□	0.038	8	YES
ψ	0.045	3	YES
#	0.048	52	YES
⊗	0.128	18	YES
⊙	0.129	2	YES

Title: (unknown) - Fabrication Drawing
Author: Nathan D. Holmes
Date: Fri 09 Jan 2015 09:25:45 PM GMT UTC
Maximum Dimensions: 6000.000000 mils wide, 1700.000000 mils high



Board outline is the centerline of this 8.000000 mil rectangle - 0.0 to 6000.000000, 1700.000000 mils