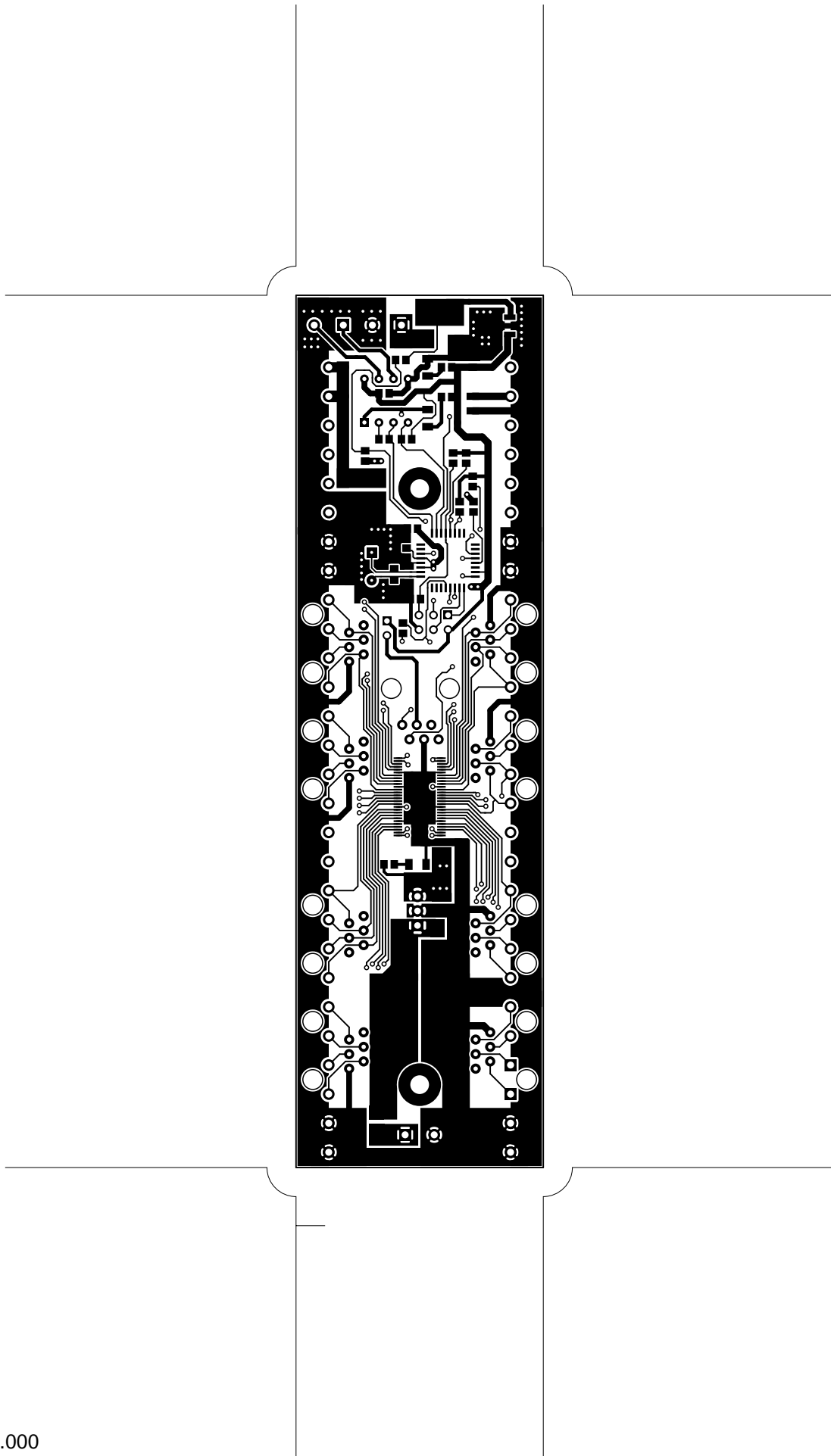
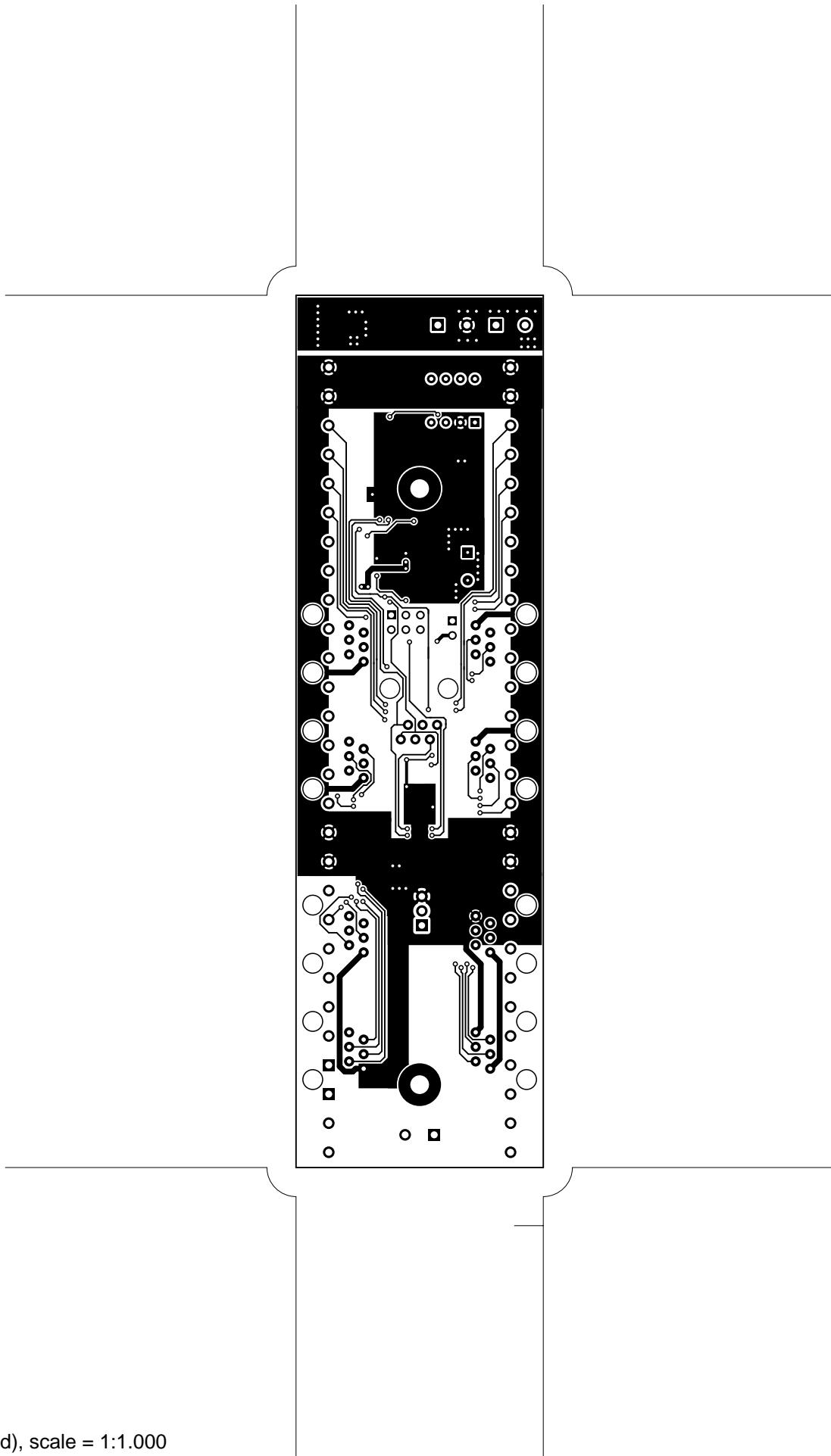


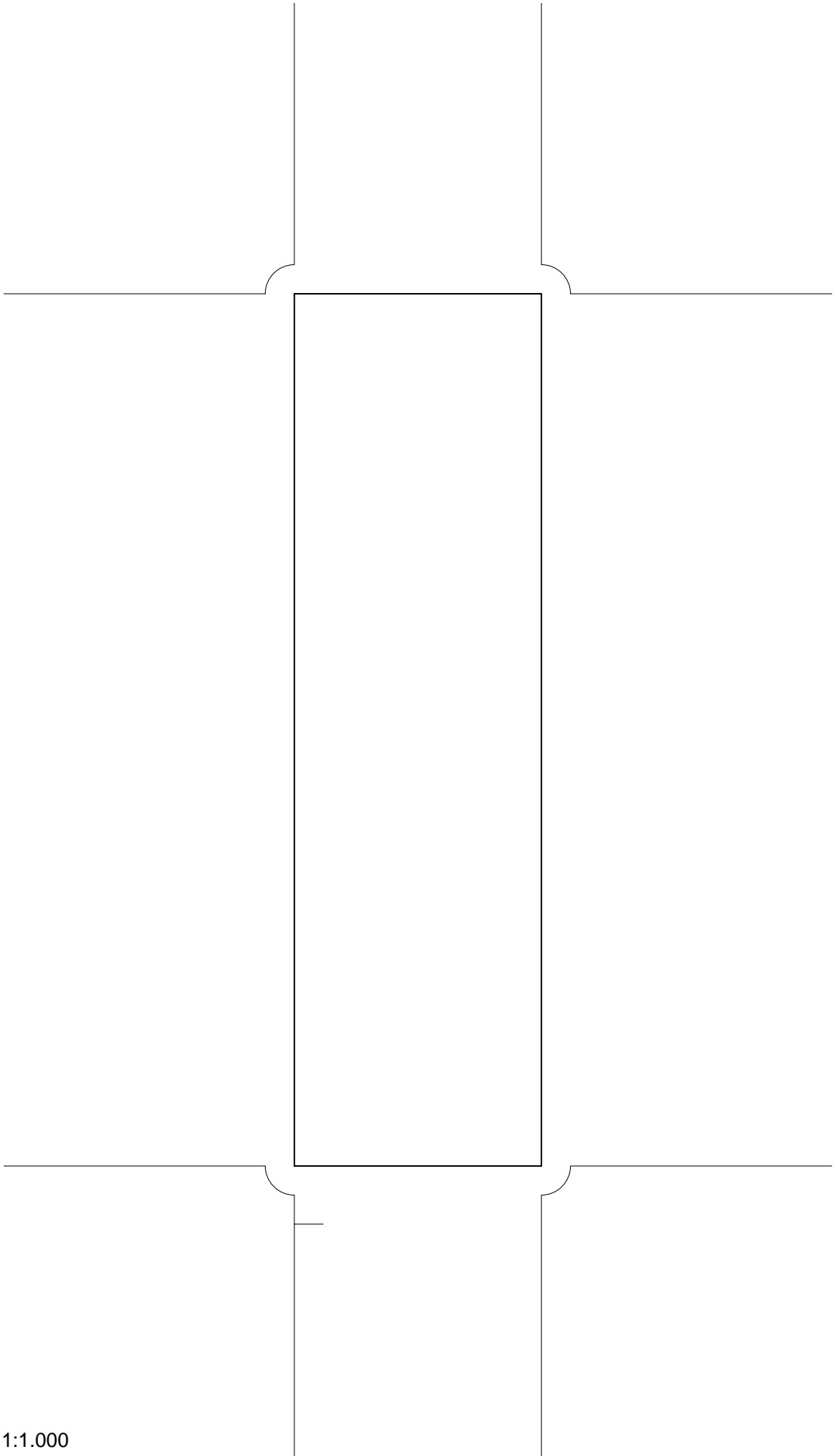
1. Table of Contents (This Page)
Created on Fri Jan 9 15:12:05 2015

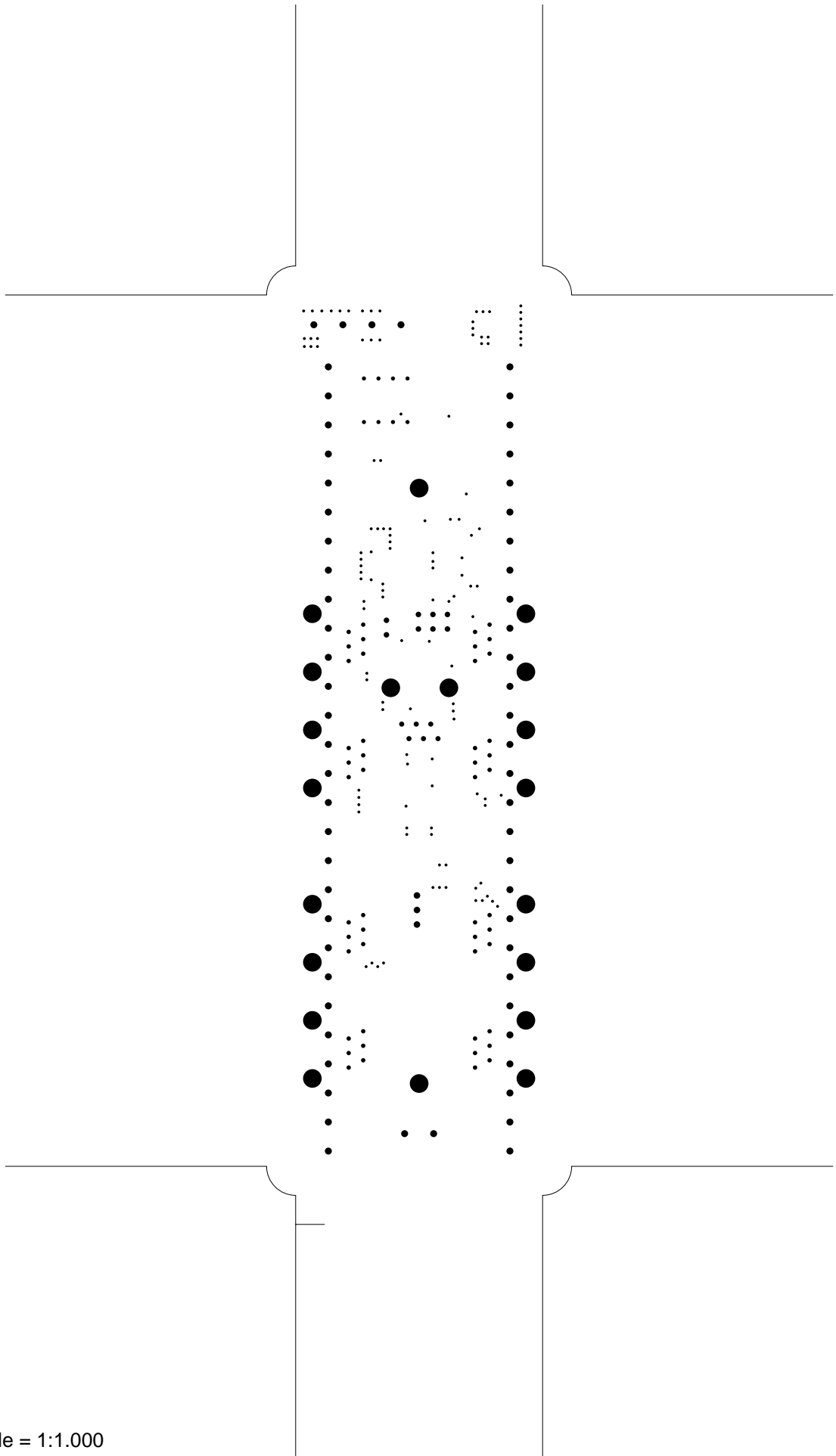
- 2. component
- 3. solder
- 4. outline
- 5. plated-drill
- 6. componentmask
- 7. soldermask
- 8. topsilk
- 9. bottomsilk
- 10. toppaste
- 11. topassembly
- 12. bottomassembly
- 13. fab

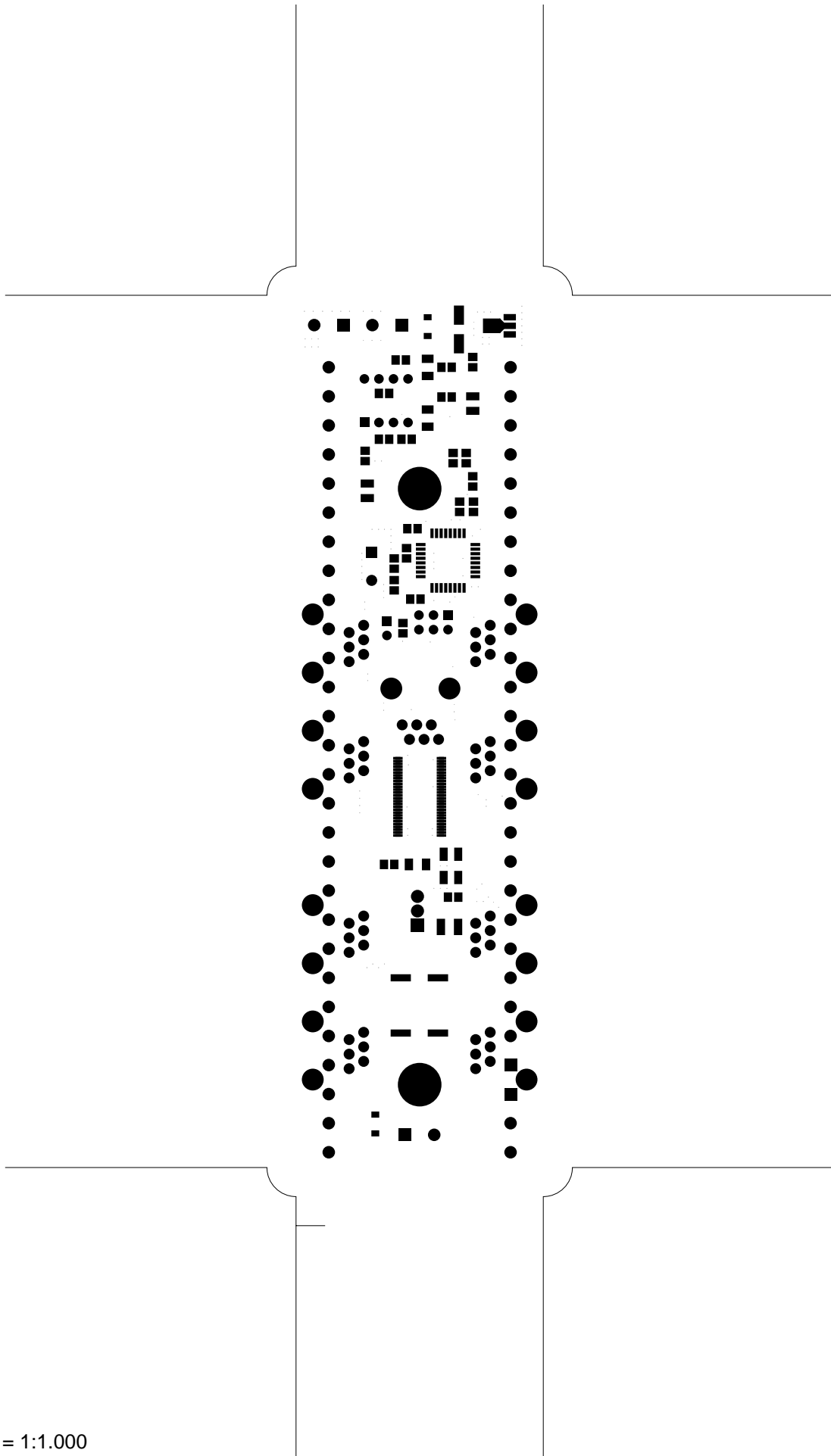


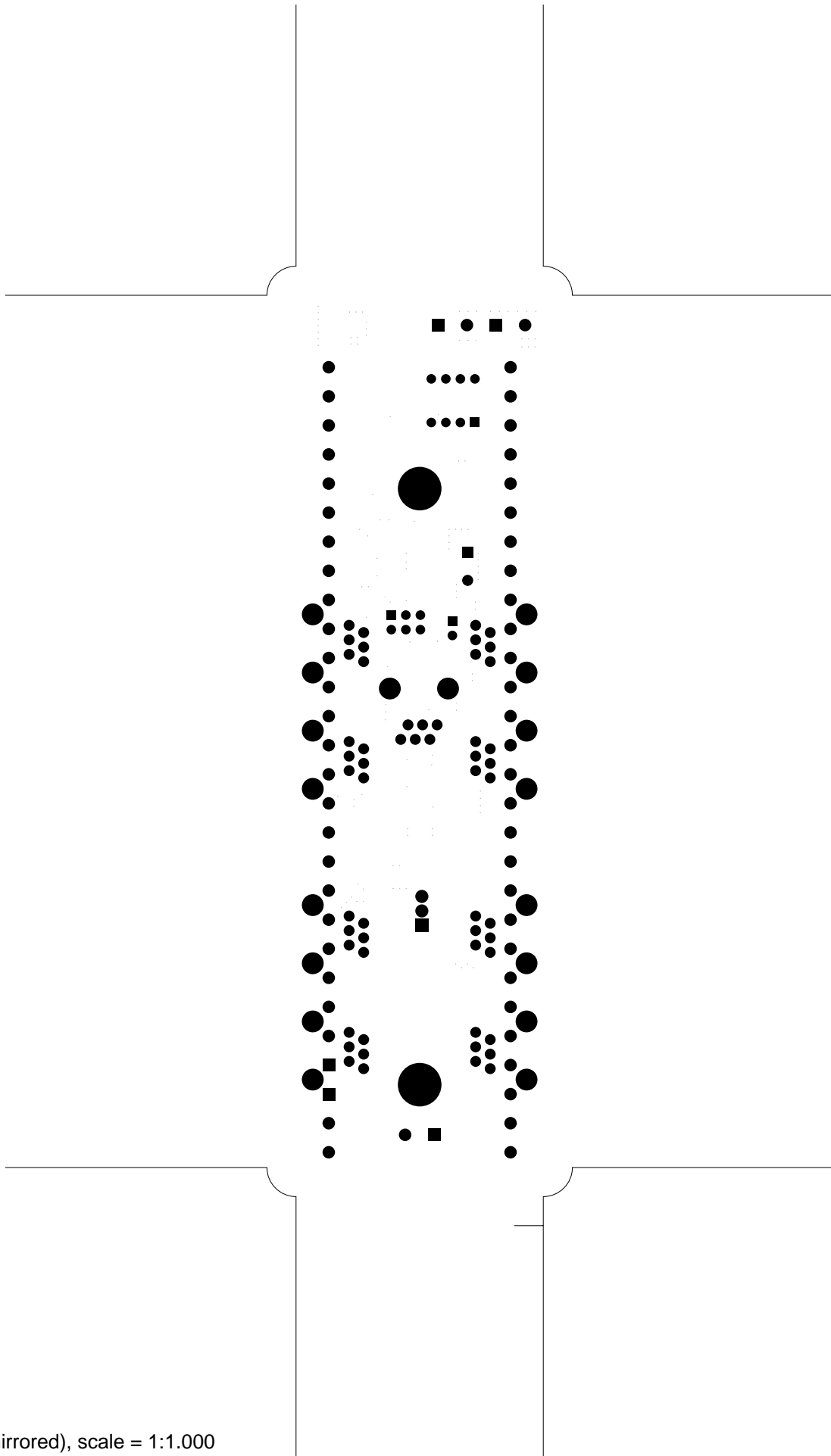


bottom (mirrored), scale = 1:1.000
mrb-xio.pcb

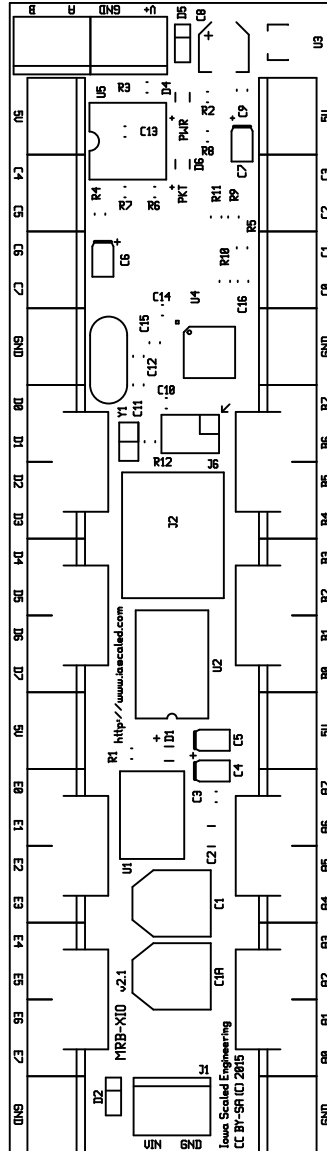


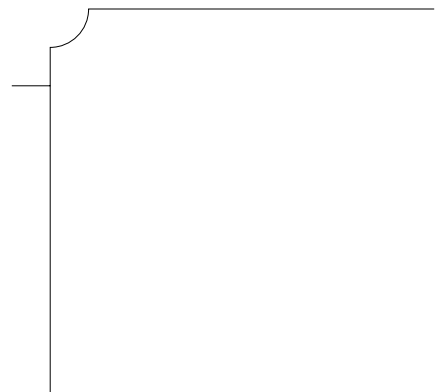
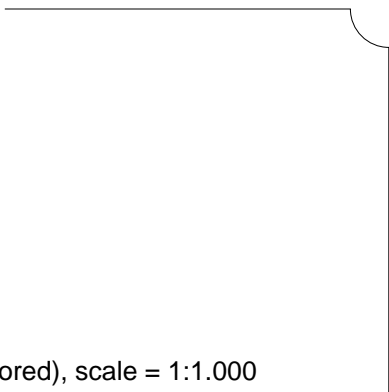
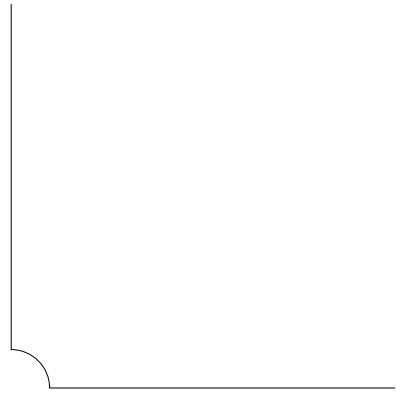
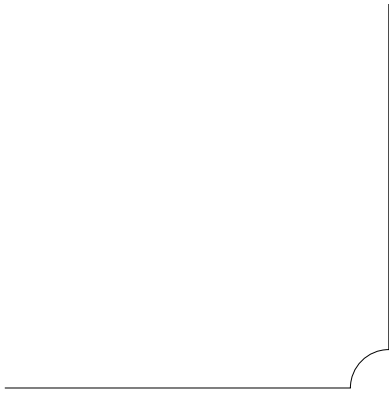




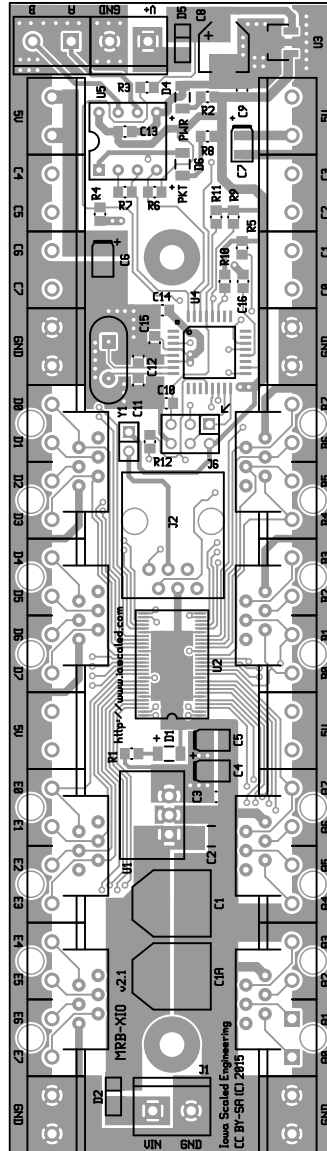


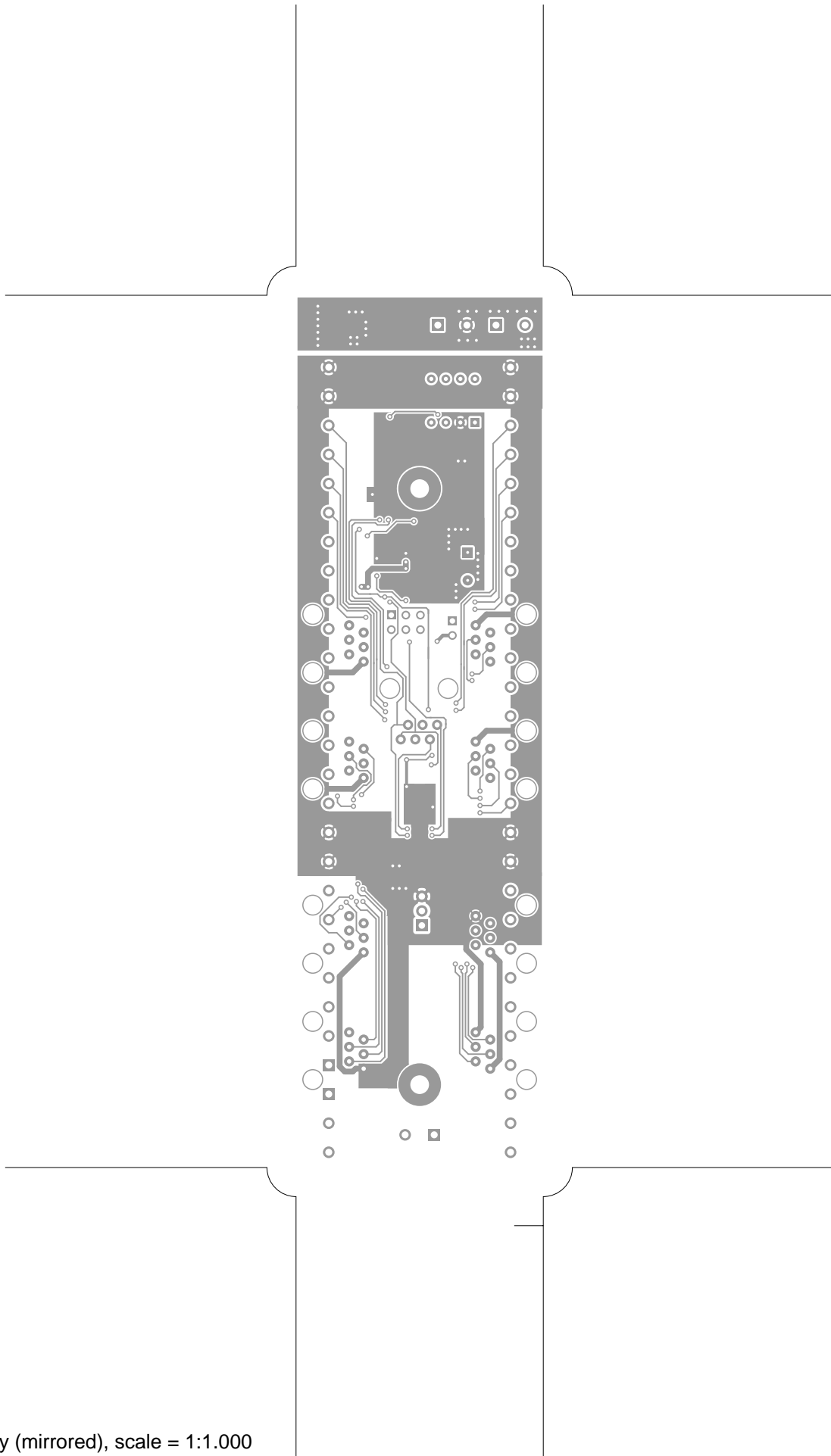
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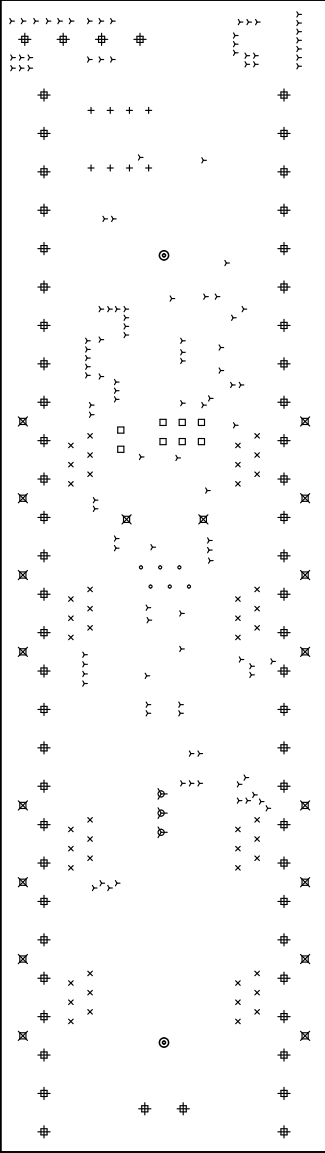


bottomassembly (mirrored), scale = 1:1.000
mrb-xio.pcb

There are 9 different drill sizes used in this layout, 274 holes total

Symbol	Diam. (Inch)	Count	Plated?
γ	0.020	119	YES
+	0.028	8	YES
x	0.030	48	YES
°	0.035	6	YES
□	0.038	8	YES
ψ	0.045	3	YES
#	0.048	52	YES
⊗	0.128	18	YES
⊙	0.129	2	YES

Title: (unknown) - Fabrication Drawing
Author: Nathan D. Holmes
Date: Fri 09 Jan 2015 10:12:06 PM GMT UTC



Board outline is the centerline of this path