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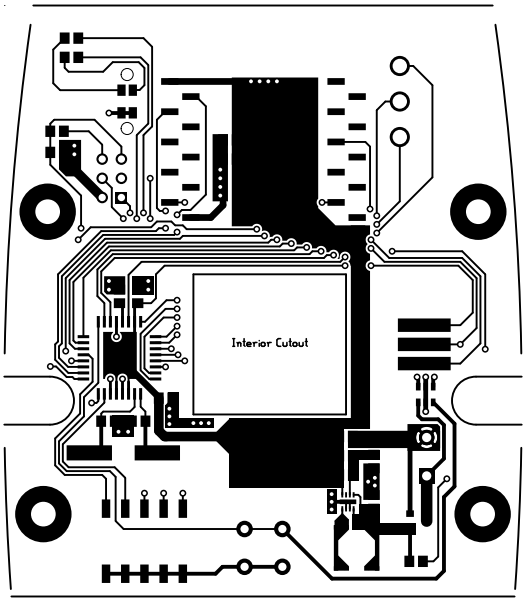
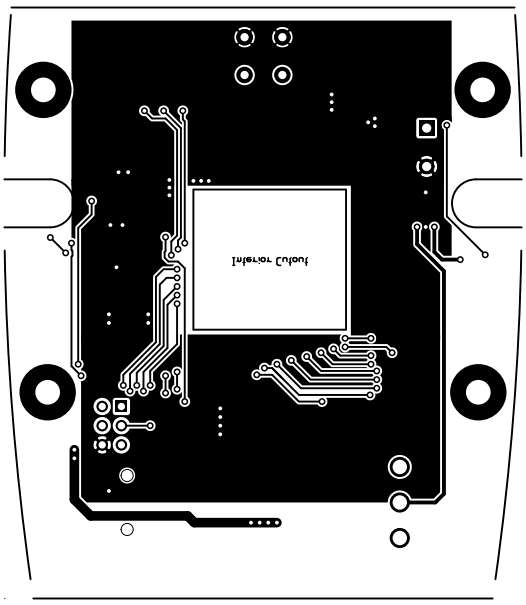
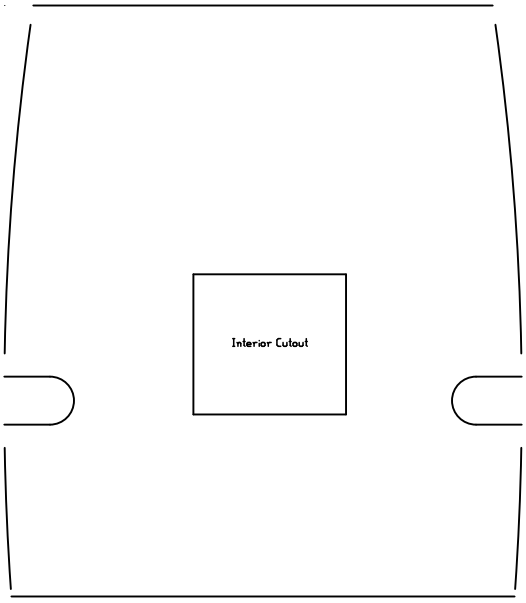


top, scale = 1:1.000
mrbw-sat.pcb



bottom (mirrored), scale = 1:1.000
mrbw-sat.pcb



outline, scale = 1:1.000
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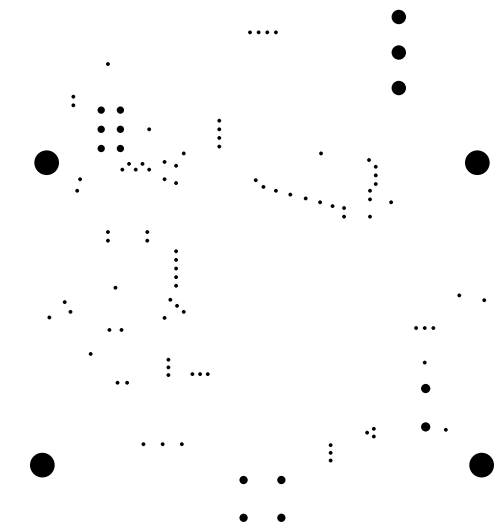
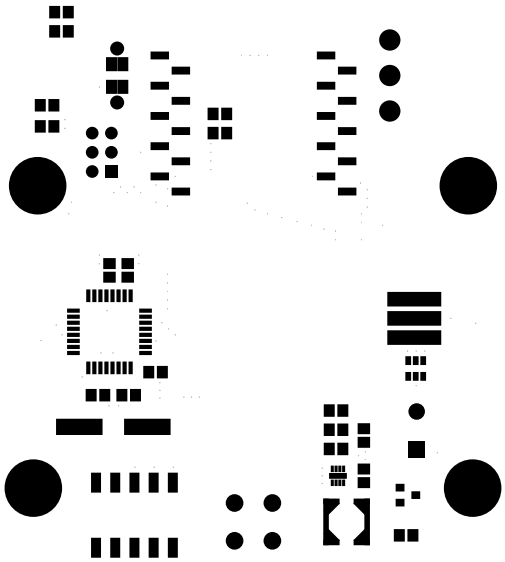
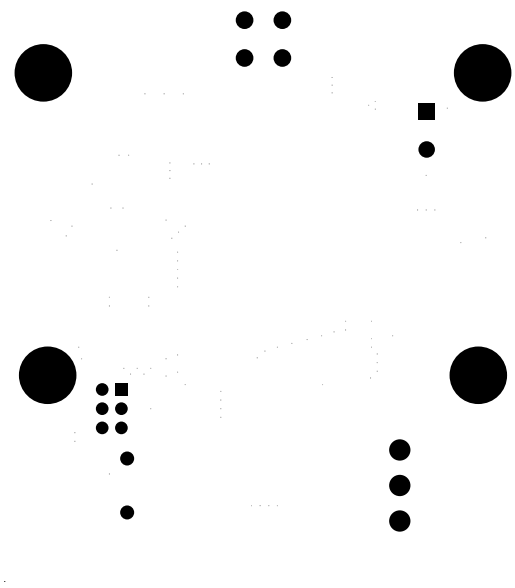


plated-drill, scale = 1:1.000
mrbw-sat.pcb

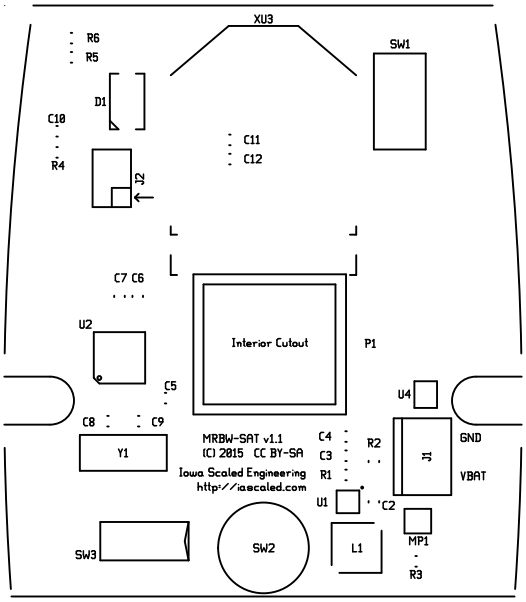
unplated-drill, scale = 1:1.000
mrbw-sat.pcb



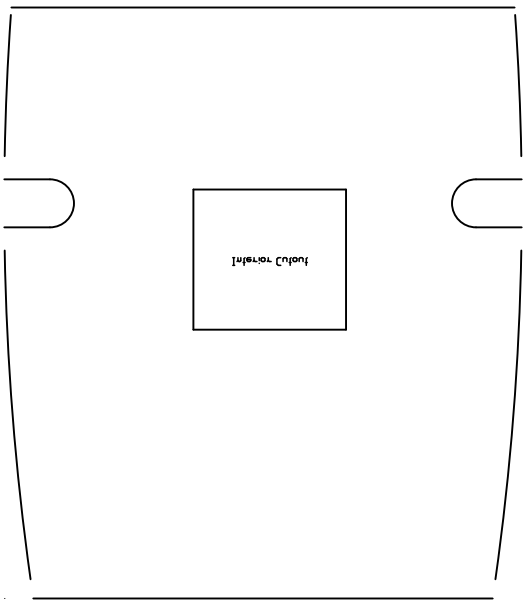
topmask, scale = 1:1.000
mrbw-sat.pcb



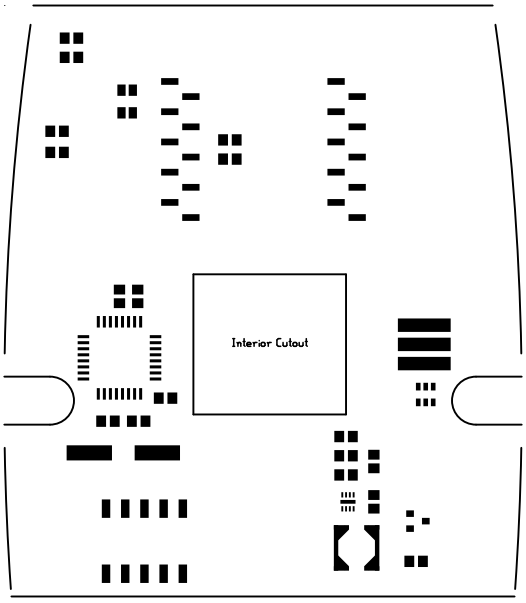
bottommask (mirrored), scale = 1:1.000
mrbw-sat.pcb



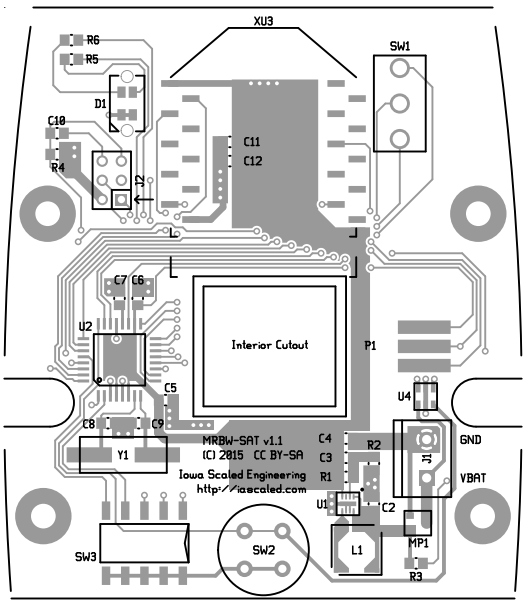
topsilk, scale = 1:1.000
mrbw-sat.pcb



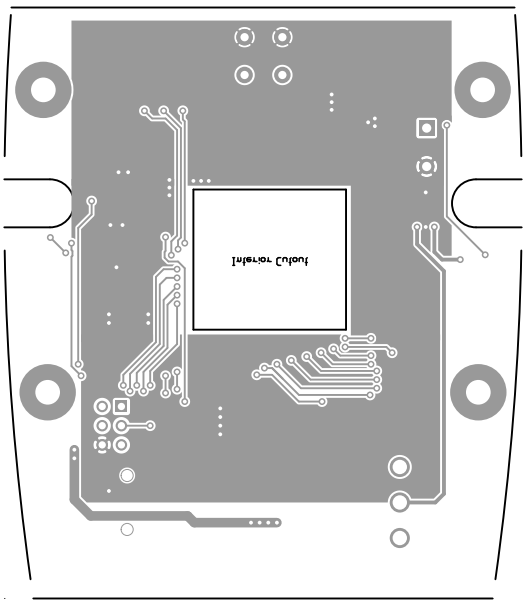
bottomsilk (mirrored), scale = 1:1.000
mrbw-sat.pcb



toppaste, scale = 1:1.000
mrbw-sat.pcb



topassembly, scale = 1:1.000
mrbw-sat.pcb

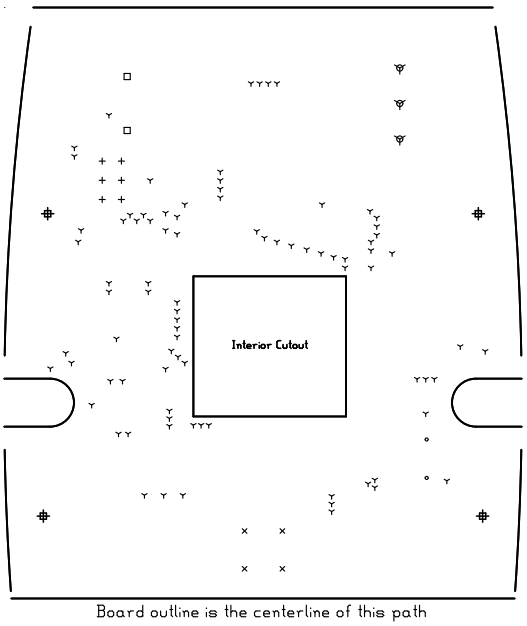


bottomassembly (mirrored), scale = 1:1.000
mrbw-sat.pcb

There are 7 different drill sizes used in this layout, 107 holes total

Symbol	Diam. (Inch)	Count	Plated?
˟	0.020	86	YES
+	0.038	6	YES
×	0.043	4	YES
◦	0.048	2	YES
□	0.063	2	NO
⊛	0.075	3	YES
⌖	0.129	4	YES

Title: (unknown) - Fabrication Drawing
Author: Michael Petersen
Date: Thu 09 Jul 2015 02:06:34 AM GMT UTC



Board outline is the centerline of this path

fab, scale = 1:1.000
mrbw-sat.pcb