HYGROCHIP
DIGITAL HUMIDITY SENSOR
PROTOCOL DESCRIPTION I^{2}C
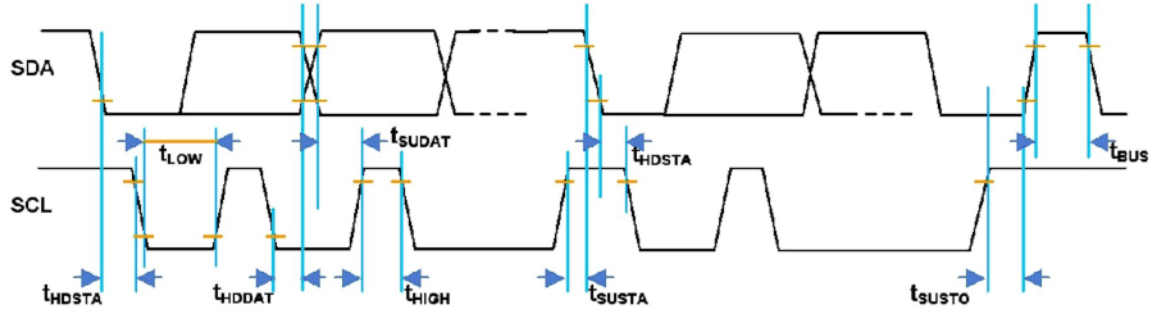

Figure – I^{2}C Timing Diagram

I^{2}C Interface and Timing

For integration with the micro-controller, the Humidity module has a I^{2}C-compatible interface which supports both 100 kHz and 400 kHz bit rate. The I^{2}C slave address is programmed by default on 0x28 and can be adjusted in the entire address range of (0x00 to 0x7F). Hence, up to 126 Humidity modules can be operated on a single I^{2}C-Bus.

PARAMETER	SYMBOL	MIN	MAX	UNIT
SCL clock frequency	fSCL	100	400	kHz
Start condition hold time relative to SCL edge	tHDSTA	0.1		µs
Minimum SCL clock low width 1	tLOW	0.6		µs
Minimum SCL clock high width 1	tHIGH	0.6		µs
Start condition setup time relative to SCL edge	tSUSTA	0.1		µs
Data hold time on SDA relative to SCL edge	tHDDAT	0		µs
Data setup time on SDA relative to SCL edge	tSUDAT	0.1		µs
Stop condition setup time on SCL	tSUSTO	0.1		µs
Bus free time between stop condition and start condition	tBUS	1		µs

There are two I^{2}C commands for the user to access the humidity module:
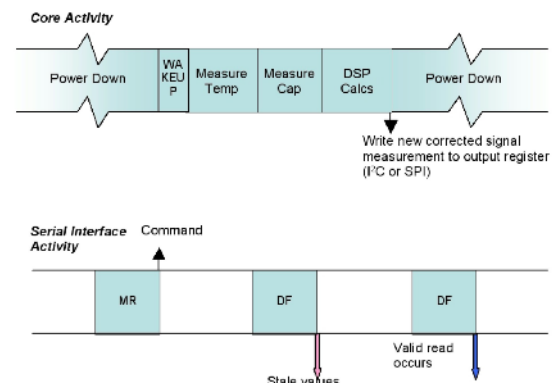
Command	Description
'Data Fetch' (DF)	Fetch the last measured value of Humidity / Temperature
'Measuring Request' (MR)	Start a measuring cycle

In the initial condition, the humidity module is in the Sleep mode to minimise on the current consumption. A new measurement is carried out only after the command measuring request (MR) is received.

Access to the status bits and measured values is made by the data fetch command.

After the measuring cycle has been completely processed, the ready status bit is set and the current measured values are available. To determine if the measuring cycle has been already finished, the output registers may be cyclically polled.

If the access to the measured values takes place too early, then the measured values of the previous measuring cycle are transferred and the stale status bit is set.

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MR (Measurement Requests)

By a measurement request command, the sleep mode is terminated and the humidity module executes a measurement cycle. The measuring cycle begins with the temperature measurement, followed by humidity measurement, digital signal processing (linearizing, temperature compensation) and finally writing the processed measured values into the output register.

The MR command consists of the address of the humidity module, with which the R/W bit is transferred as 0 (= write). After the humidity module is answered with ACK (= measurement started), the master finalized the transfer with NACK (=stop condition).

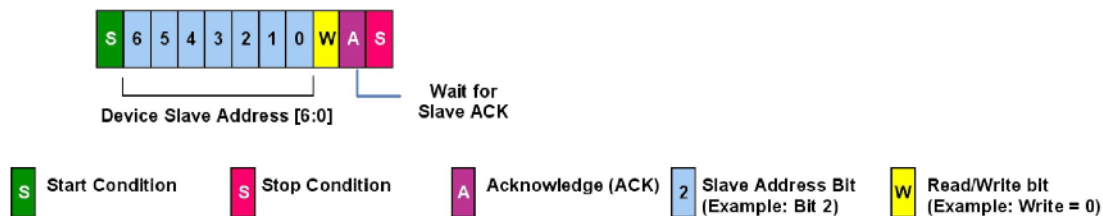I^{2}C MR– Measurement Request: Slave starts a measurement cycle

Figure – I^{2}C MR

DF (Data Fetch)

The data fetch command serves to finish reading the output register. The DF command is sent by the master to the Humidity module (Slave) and begins with the 7 Bit slave 8. bit as 1 (= read). The Humidity module sends back an acknowledgement (ACK) in case of incorrect addressing

The number of bits, that the humidity module sends back, is distinguished when the master sends a NACK (= stop condition). The first two bytes of measurement data contain the two status bits as MSB, then followed by the humidity value with 14 bits.

If the temperature data is also needed, then these can be read after the humidity value. The most significant 8 bits of the temperature value will be transferred as third byte. Then the least significant 6 bits of the temperature value can be read as the fourth byte. The last two bits are not used and should be masked away.
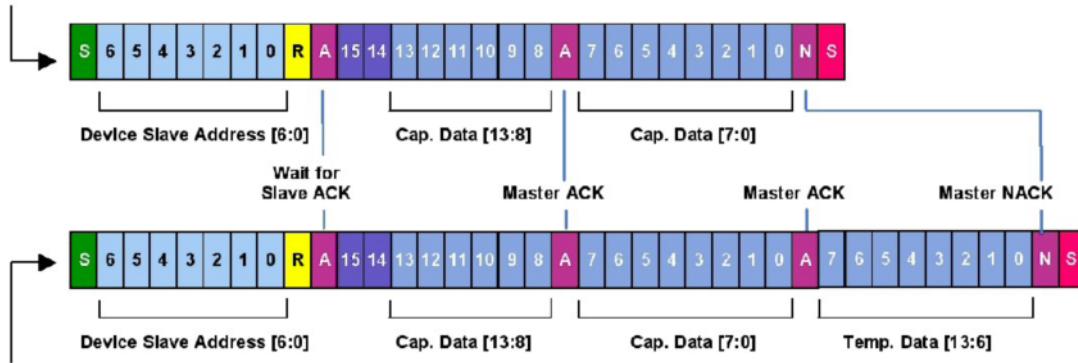
The master has the possibility to terminate the reading after each read byte through an NACK. Hence, it is possible to finish reading even after the first byte and evaluate only the status/stale bit and the master can terminate the transfer without completing the whole cycle. If only the upper 8-bits of the temperature value are to be transferred (8 bit resolution), the transfer can be aborted after the third byte by a NACK.

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I^{2}C DF –2 Bytes: Slave returns only capacitance data to the master in 2 bytes

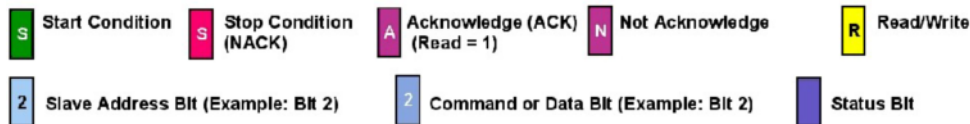I^{2}C DF – 3 Bytes: Slave returns 2 capacitance data bytes & temperature high byte (T[13:6]) to master

S Start Condition **S** Stop Condition (NACK) **A** Acknowledge (ACK) (Read = 1) **N** Not Acknowledge **R** Read/Write

2 Slave Address Bit (Example: Bit 2) **2** Command or Data Bit (Example: Bit 2) **2** Status Bit

Figure – I^{2}C Measurement Packet Reads

Scaling of measurement values

T_{raw} and rH_{raw} are the digital 16 bit values submitted by the sensor.

The first top bits are status bits with following relevance:

Bit 15: CMode Bit, if 1 – element is in command mode

Bit 14: Stale bit, if 1 – no new value has been created since the last reading.

To mask the 2 top status bits in a 16 bit value, it will be linked logically with 3FFF and AND. The remaining 14 bit represents the measured value.

The masked value data now have to be scaled into physical measurement units:

T [°C] = (165 / 2^14) * T_{raw} - 40

Example:
0x0 complies with -40°C
0x3FFF complies with +125°C
T_{raw} = 0x0000….0x3FFF (Hex) or **0…..16383 (Dec)**

Humidity Values will be calculated as follows:

rH [%] = (100 / 2^14) * rH_{raw}

Example:
0x0 complies with - 0 %rH
0x3FFF complies with 100 %rH
rH_{raw} = 0x0000….0x3FFF(Hex) or **0…..16383 (Dec)**

C-Code Examples are available upon request.

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