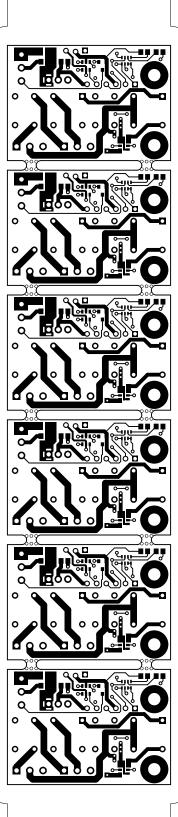
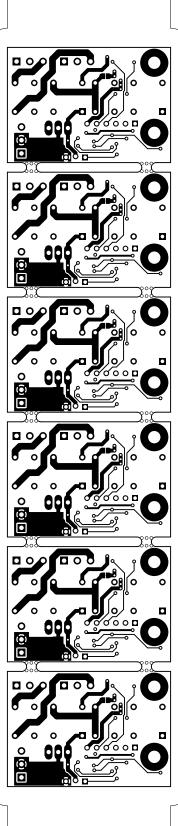
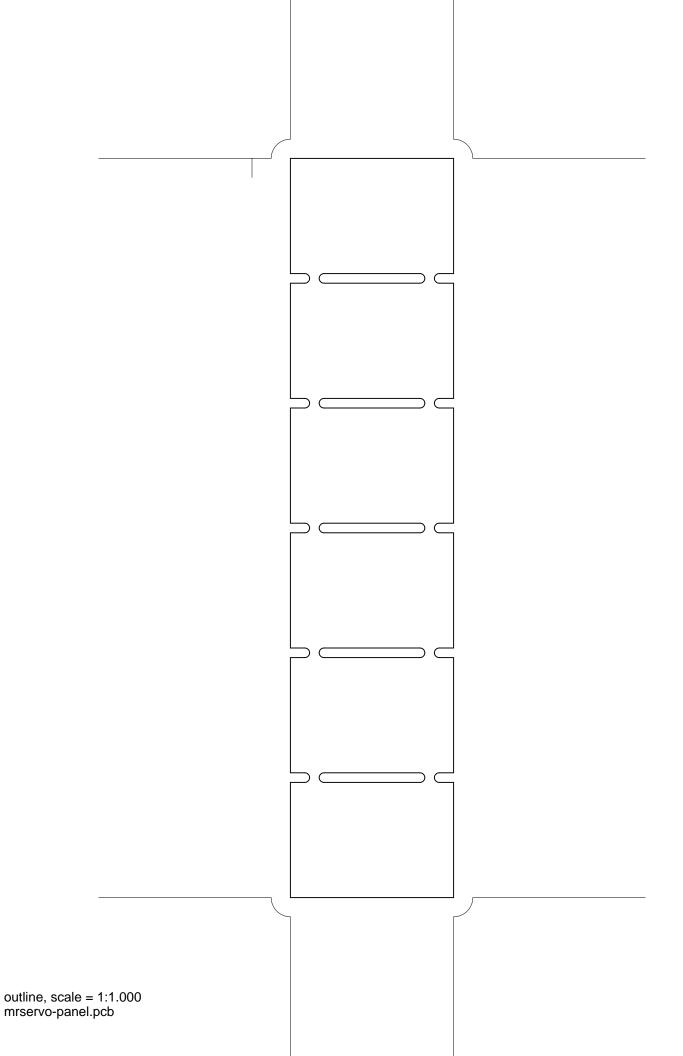
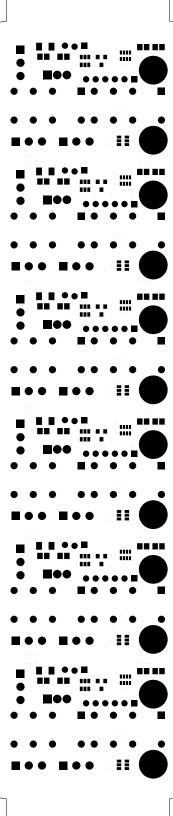
- 1. Table of Contents (This Page) Created on Sun Jul 22 20:23:10 2018
- 2. top
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- 8. soldermask
- 9. topsilk
- 10. bottomsilk
- 11. toppaste
- 12. bottompaste
- 13. topassembly
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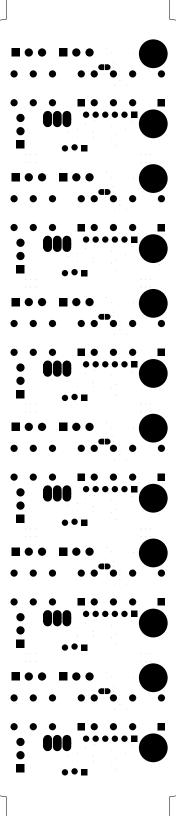


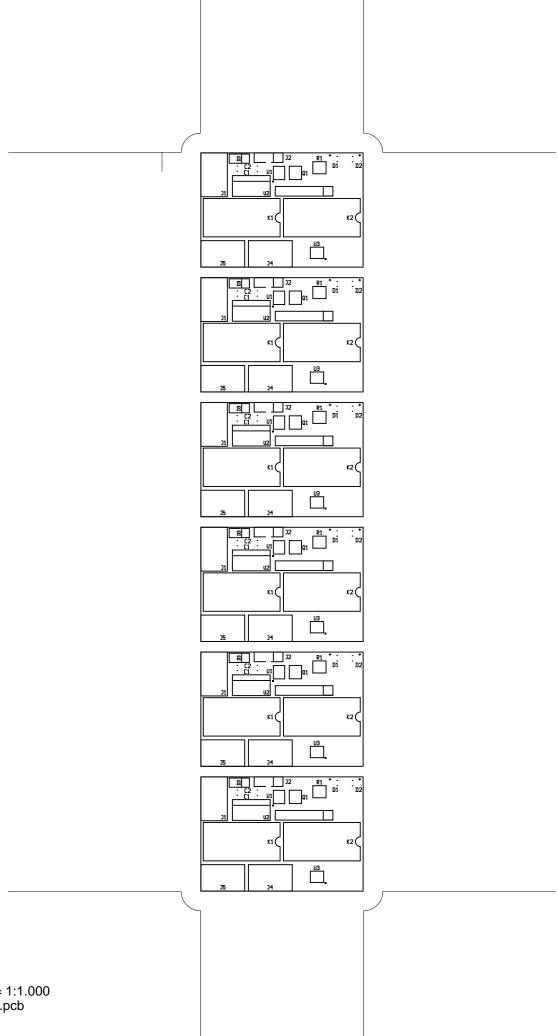




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unplated-drill, scale = 1:1.000 mrservo-panel.pcb		







NOR COM REV NOR COM REV OUTPUT +1 JP1 OUTPUT #2 UDD GND CNTL NOR COM REV NOR COM REV OUTPUT #1 JP1 OUSSERUO

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P.S.v. NOR COM REU NOR COM REU OUTPUT #1 JP1 OUTPUT *2 GND CNTL http://iaecaled.com (C) 2015 Iowa Scaled Engin NOR COM REV NOR COM REV OUTPUT #2 OUTPUT #1 JP1 GND CNTL MRSERUO MOS. Secoled Engineering Iowa Scaled Engineering A.S. (2) A.S. (2) NOR COM REV NOR COM REV OUTPUT *2 OUTPUT *1 JP1 UDD GND CNTL http://icaecoled.com | Iowa Scaled Engineering US) 2015 | Owa Scaled Engineering US) 424 MESERO NOR COM REV NOR COM REV OUTPUT *2 OUTPUT *1 JP1 UDD GND CNTL MRSERUO

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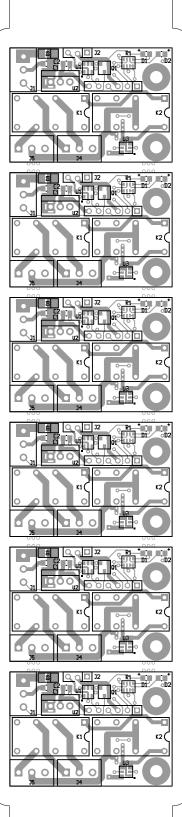
bottomsilk (mirrored), scale = 1:1.000 mrservo-panel.pcb

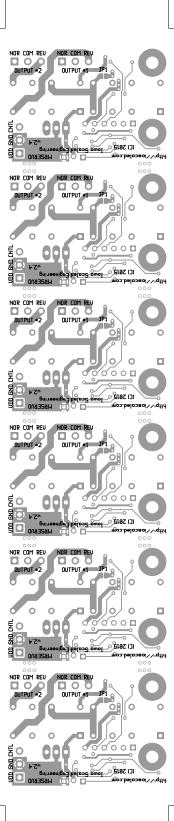
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		J
bottompaste (mirrored), scale = 1:1.000 mrservo-panel.pcb		





There are 6 different drill sizes used in this layout, 438 holes total

Symbol	Diam. (Inch)	Count	Plated?
۲	0.020	144	YES
+	0.031	60	N0
×	0.038	150	YES
٠	0.045	18	YES
	0.047	54	YES
\$	0.129	12	YES

Title: (unknown) - Fabrication Drawing Author: Michael Petersen

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Board outline is the centerline of this path