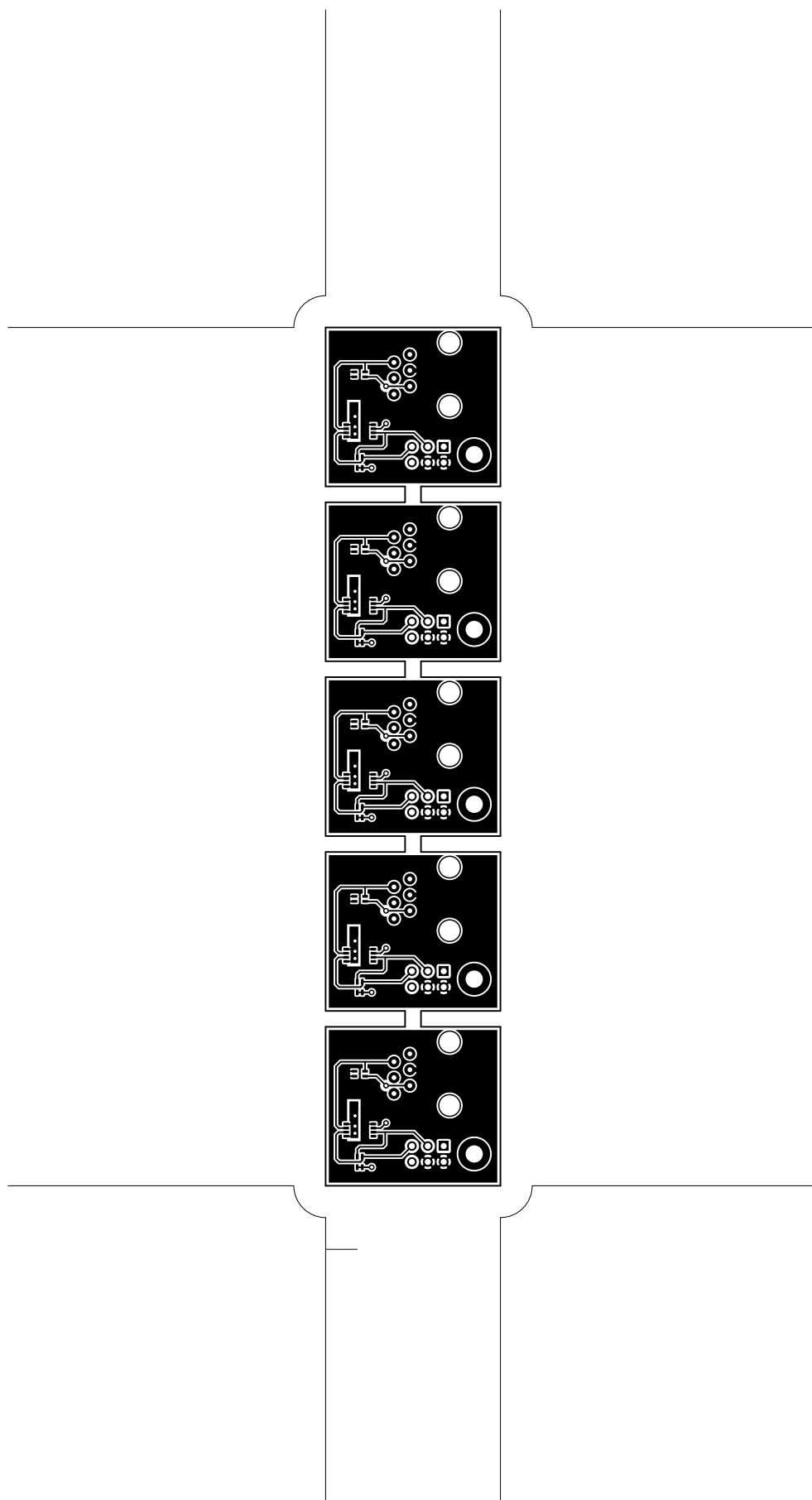
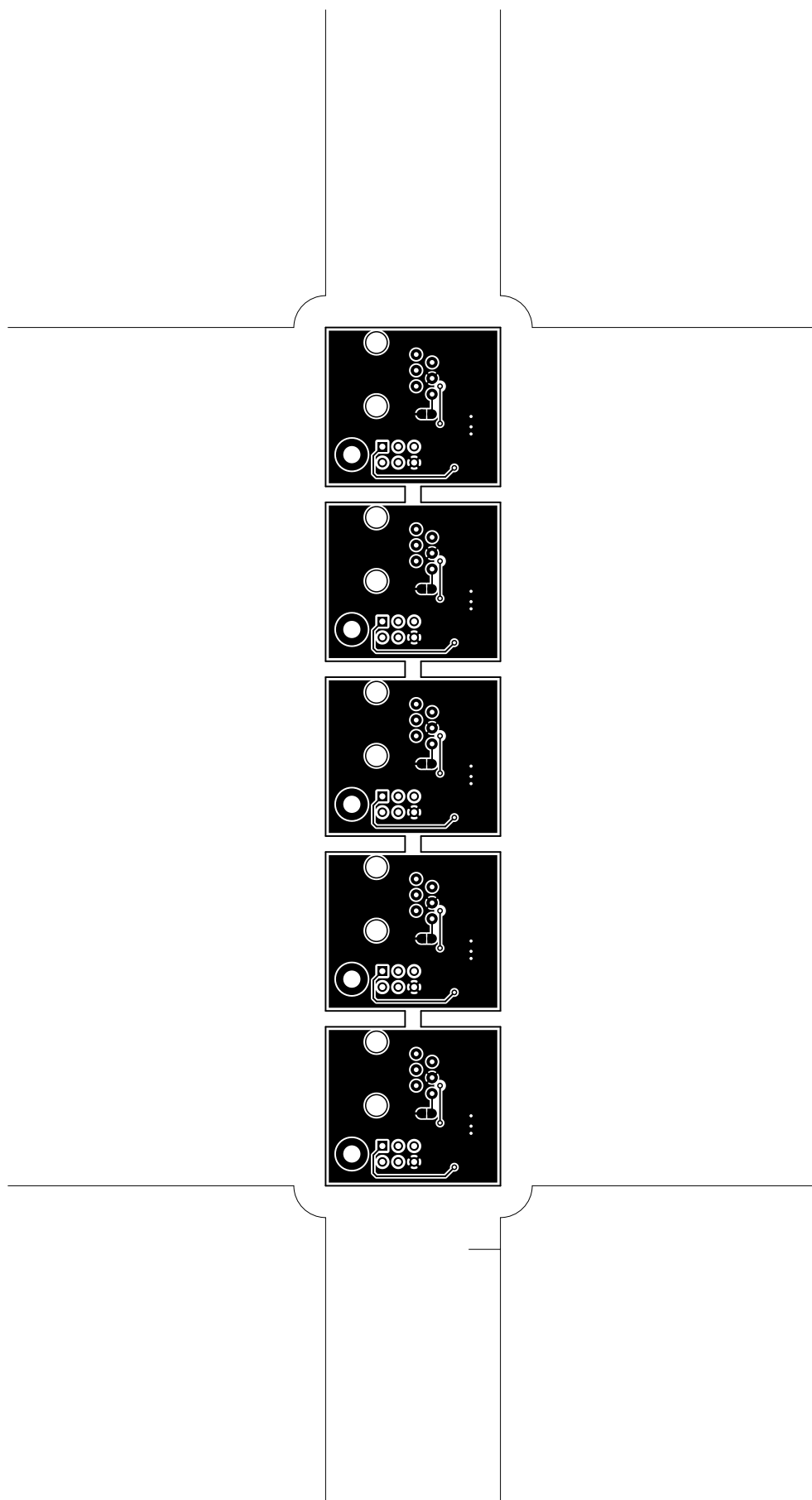


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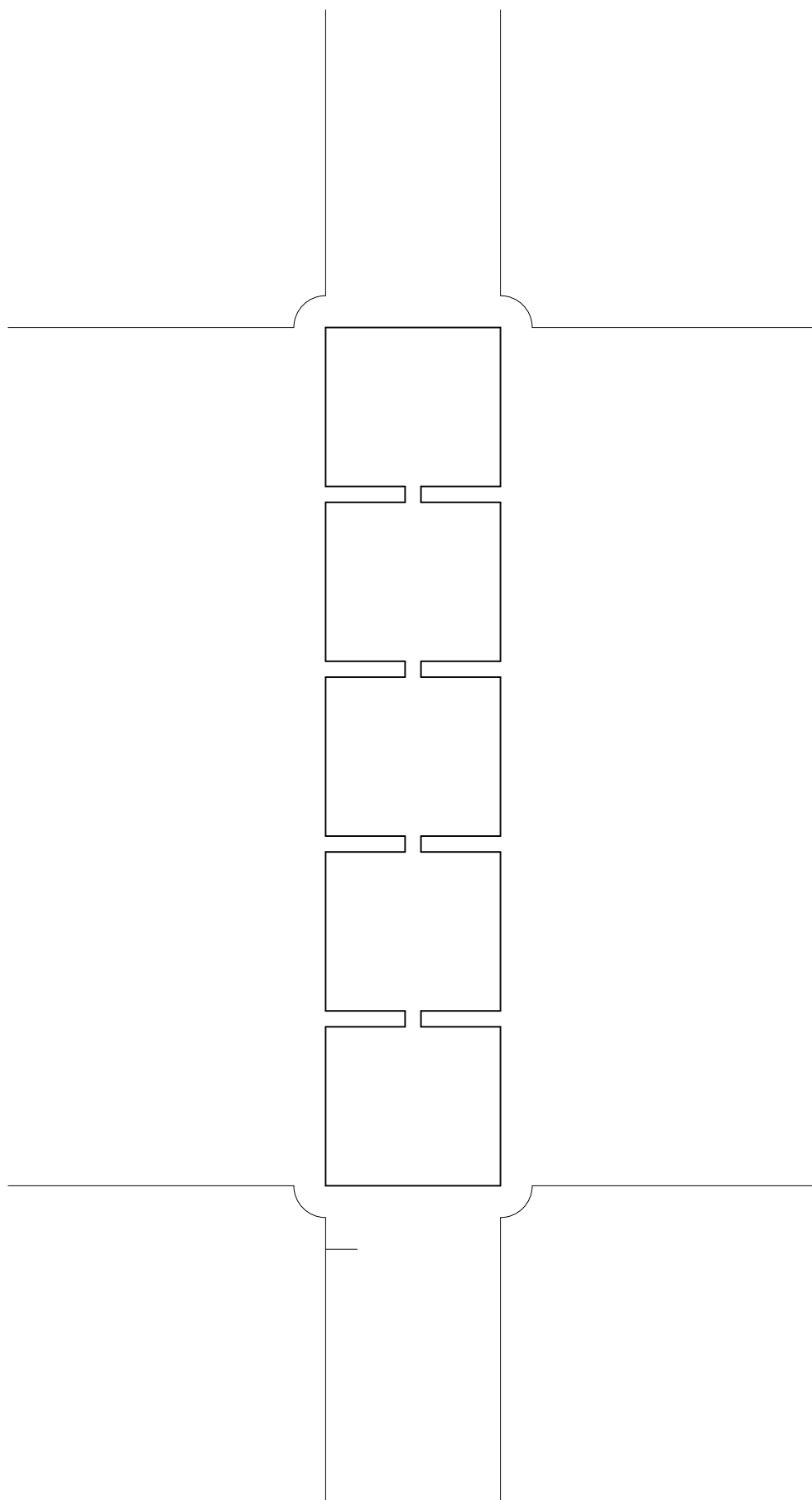
Created on Thu Oct 25 10:49:50 2018

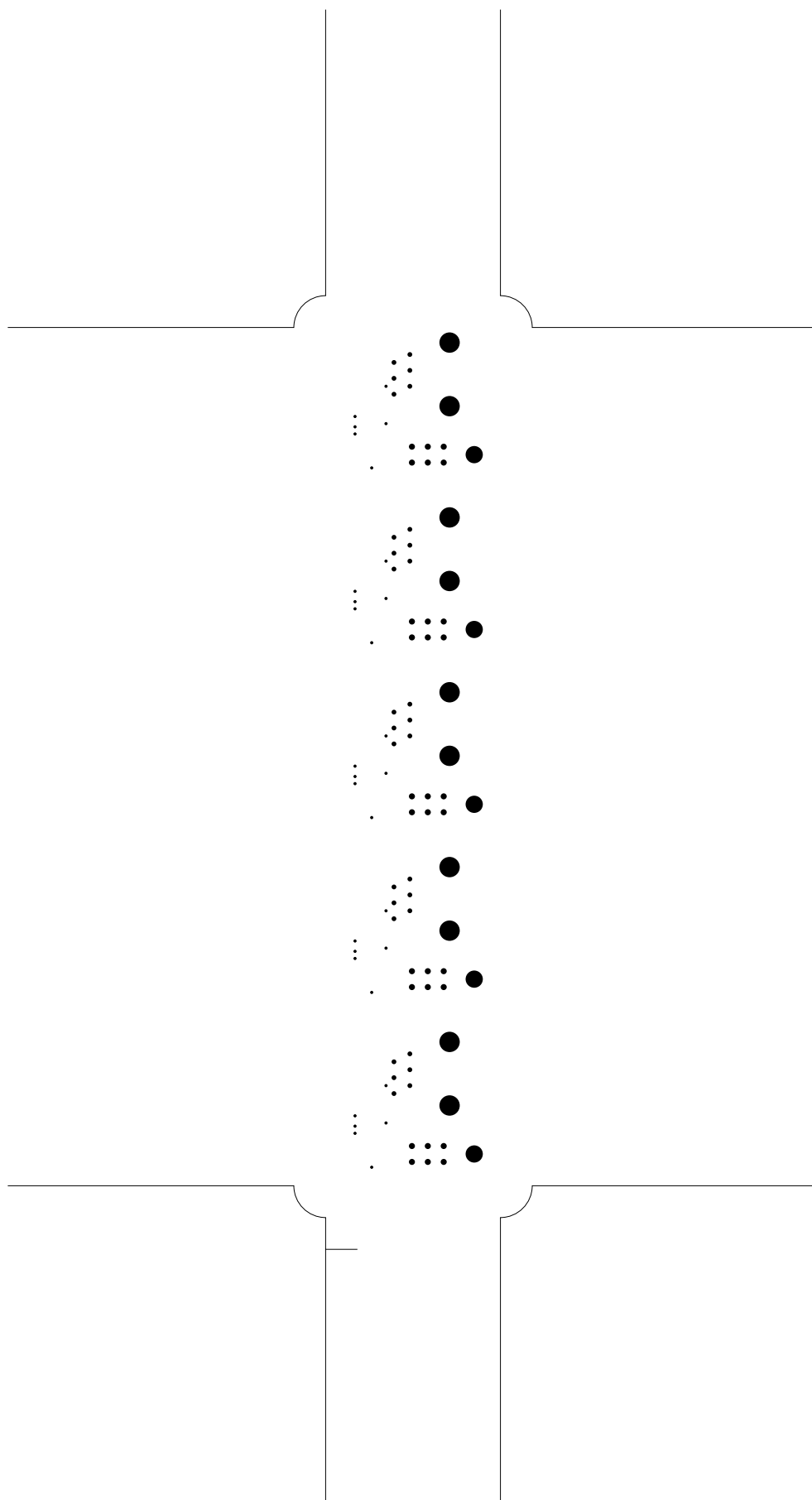
- 2. top
- 3. bottom
- 4. outline
- 5. plated-drill
- 6. componentmask
- 7. soldermask
- 8. topsilk
- 9. bottomsilk
- 10. toppaste
- 11. bottompaste
- 12. topassembly
- 13. bottomassembly
- 14. fab

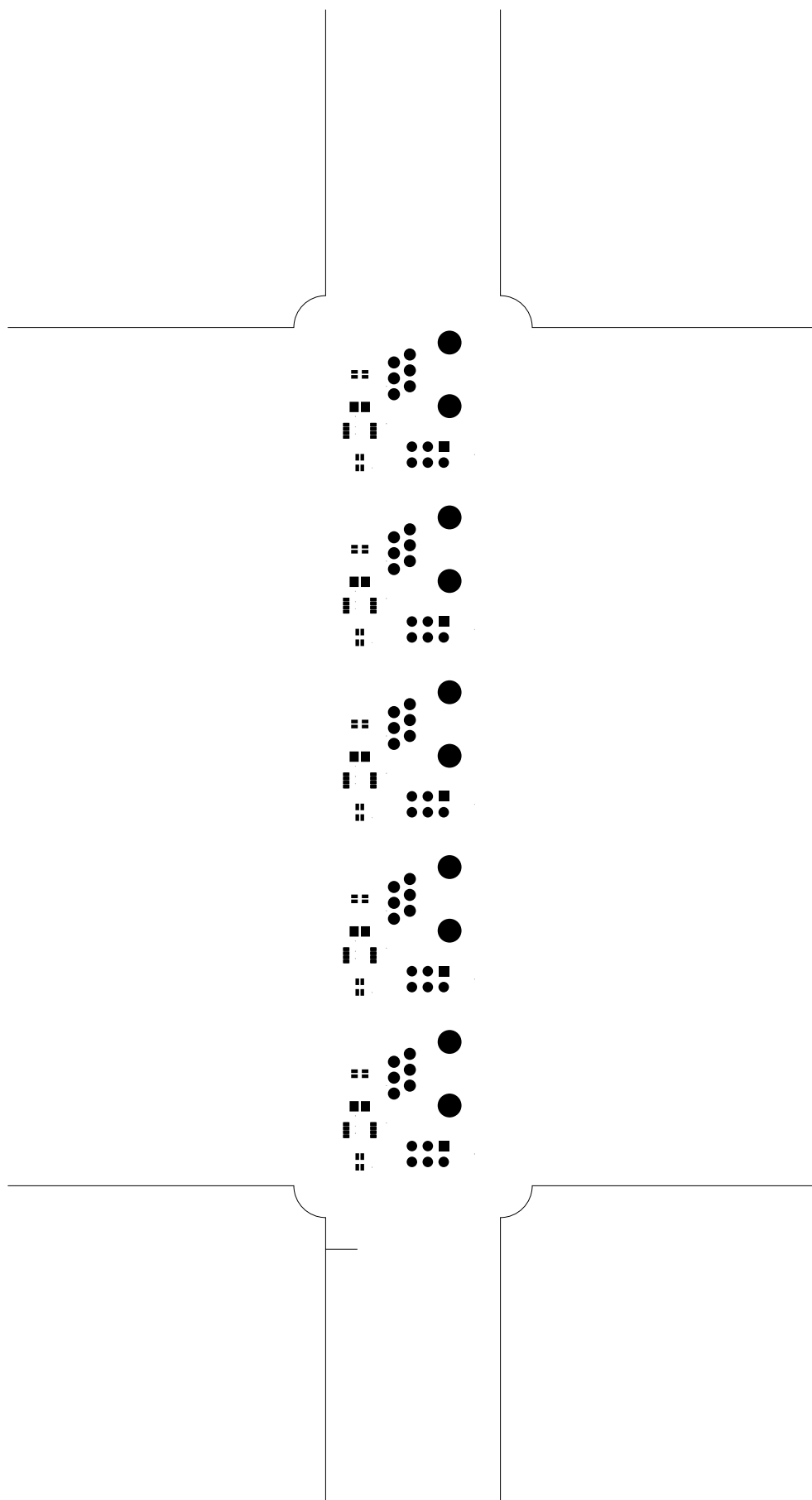


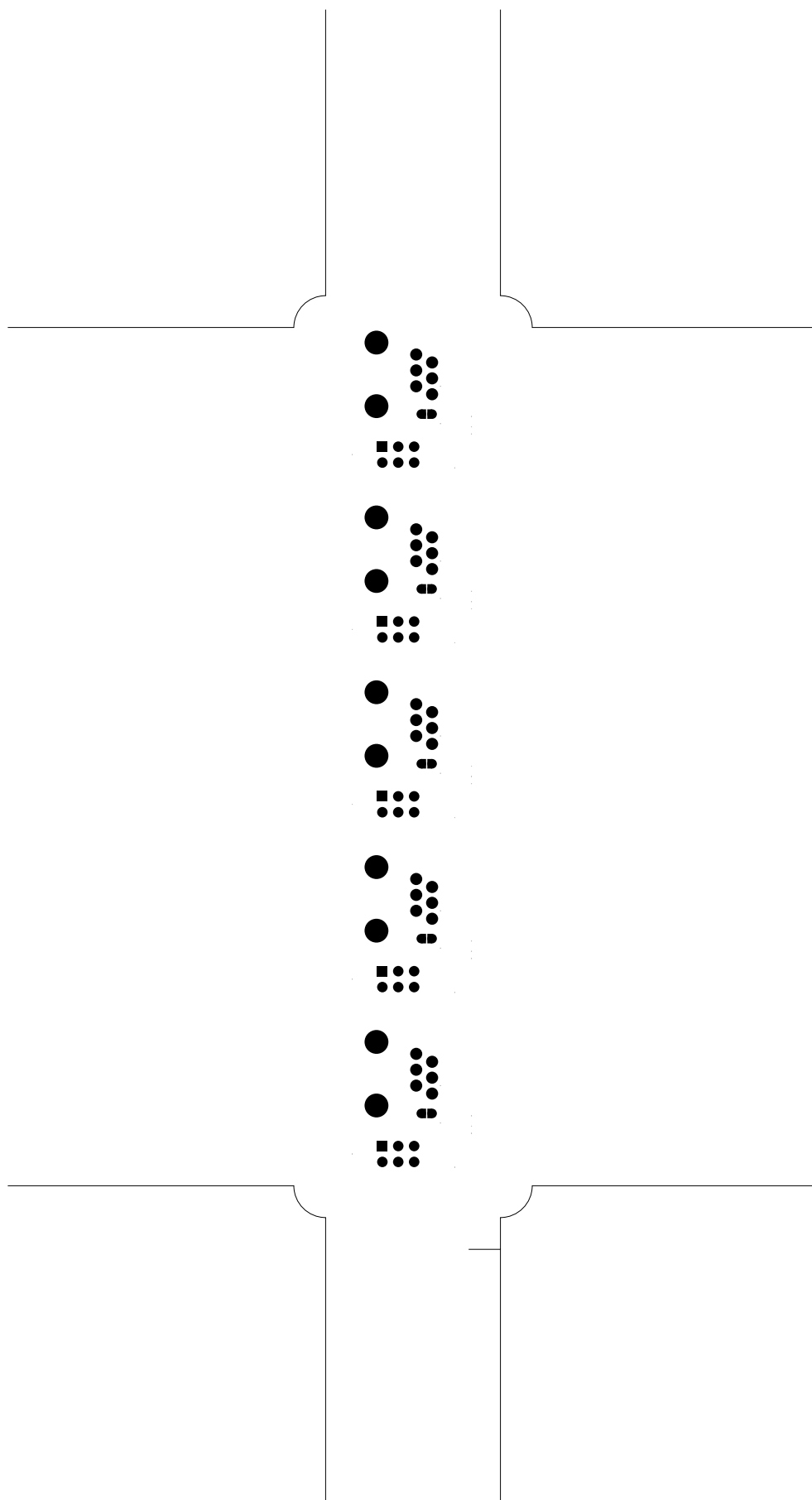


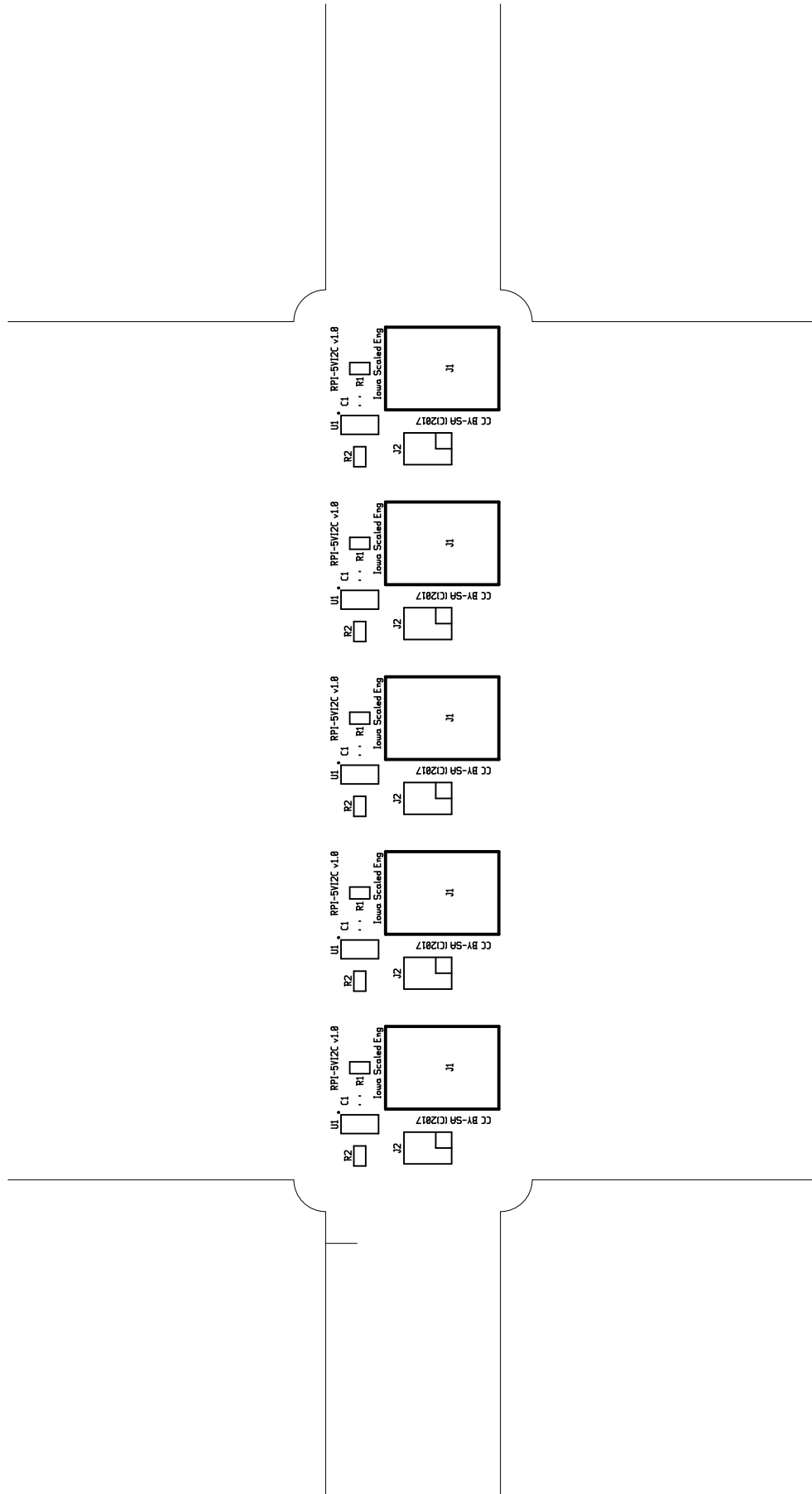
bottom (mirrored), scale = 1:1.000
rpi-5vi2c-panel.pcb

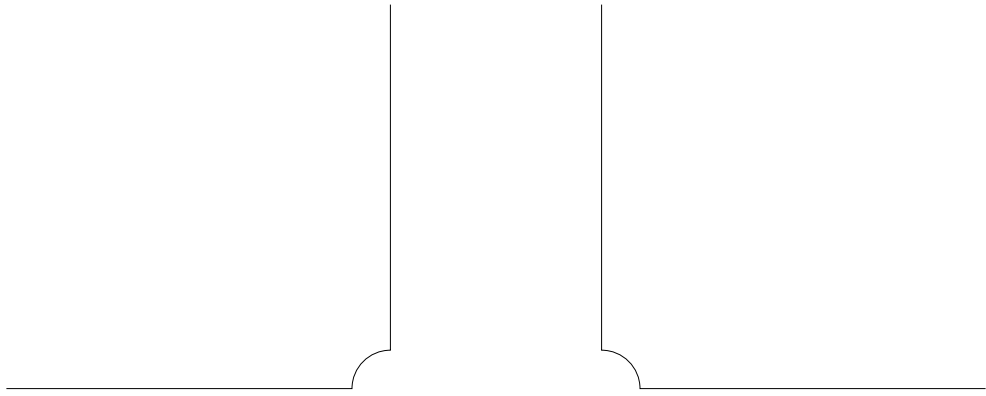












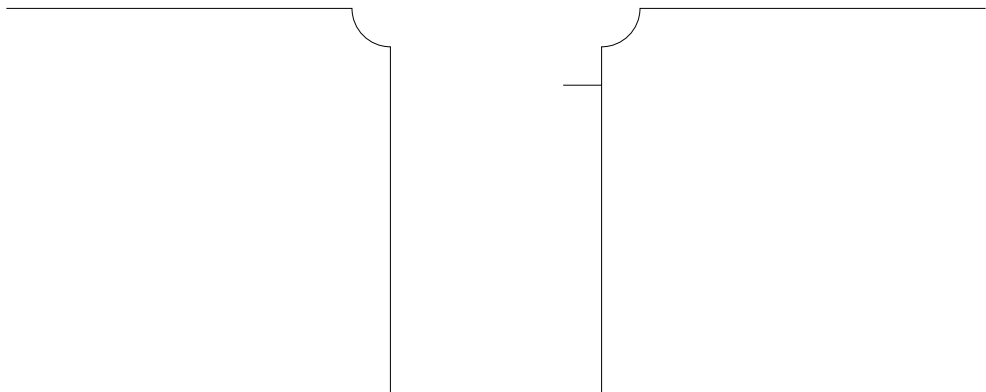
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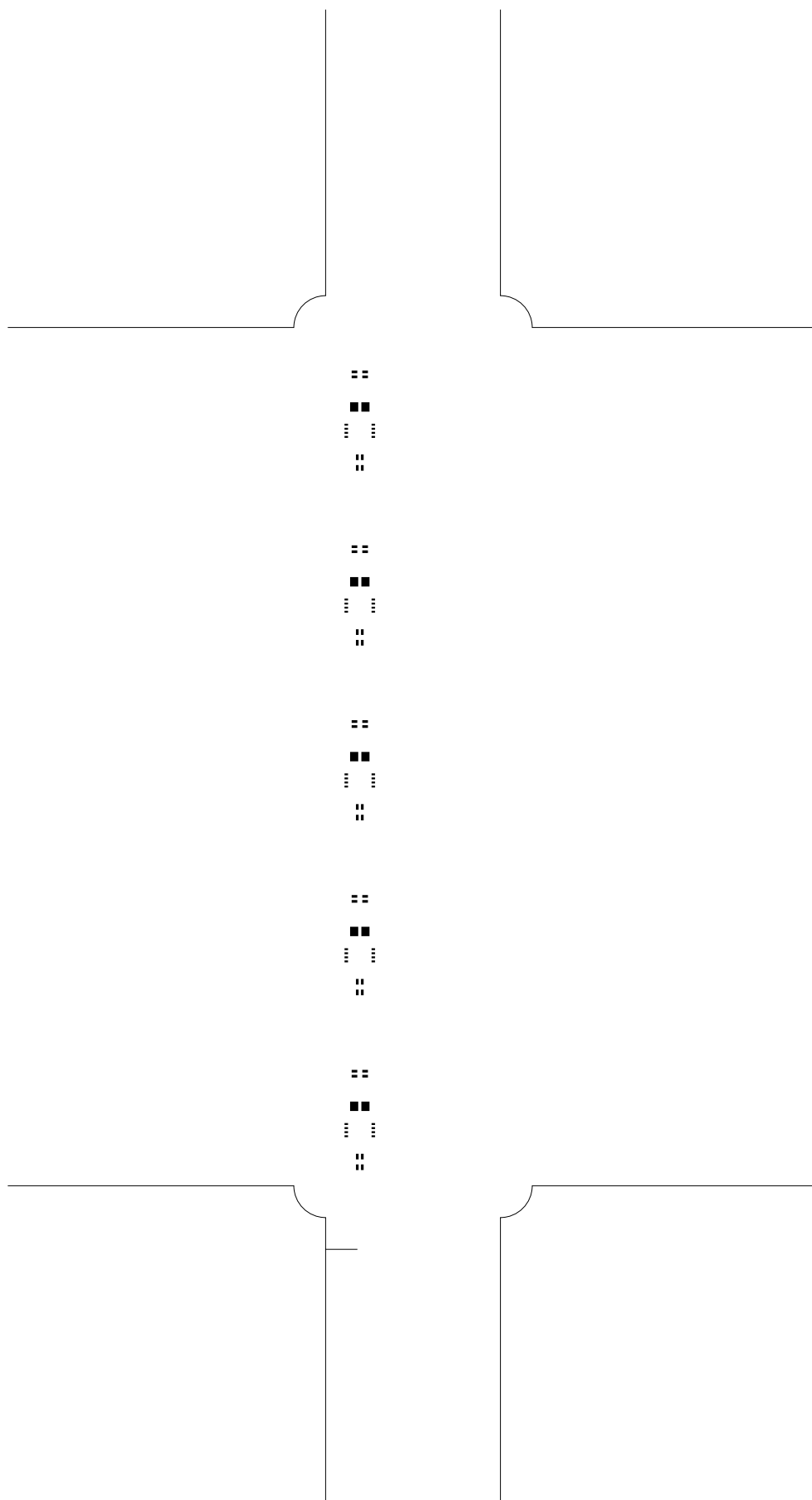
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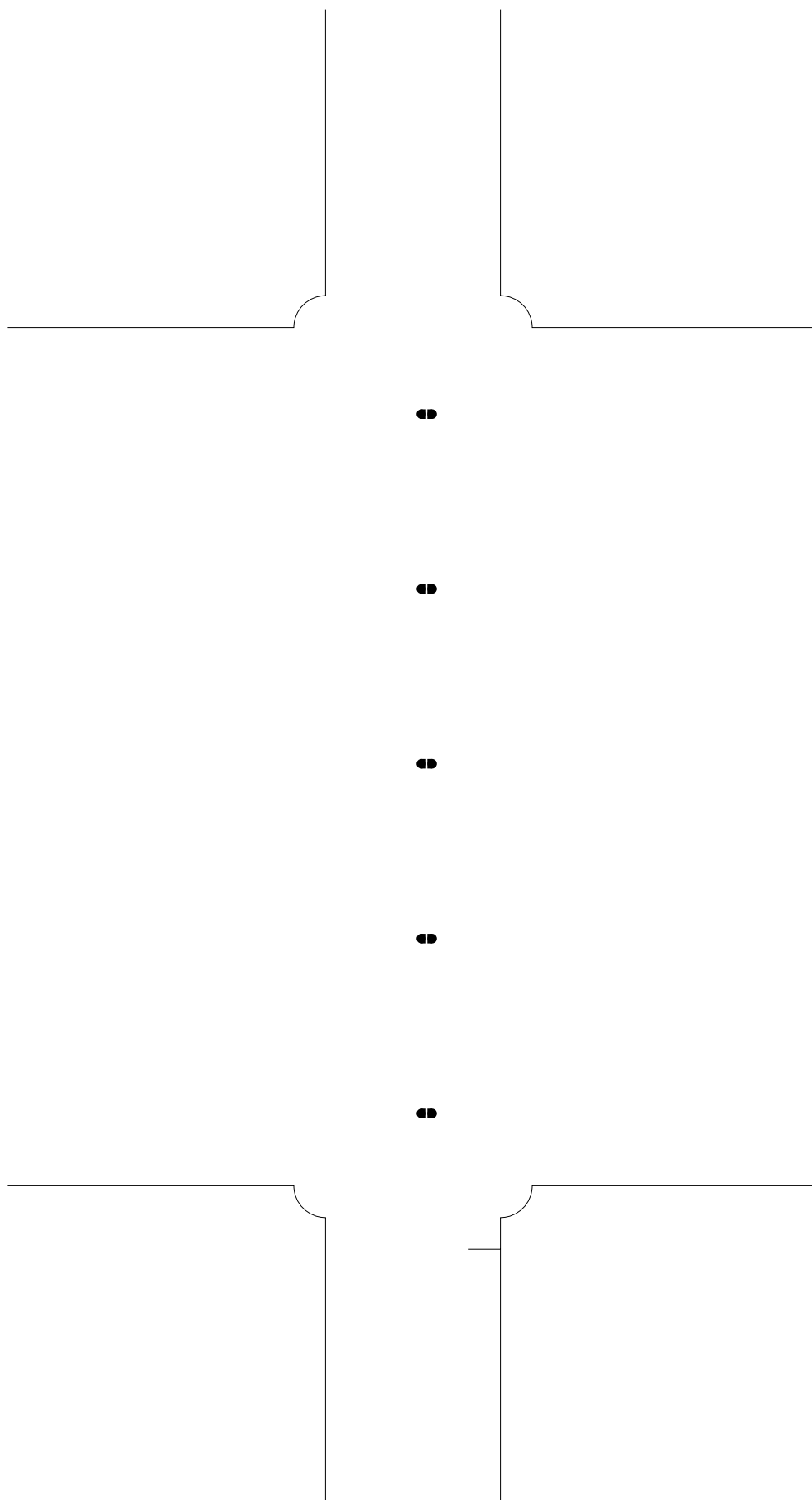
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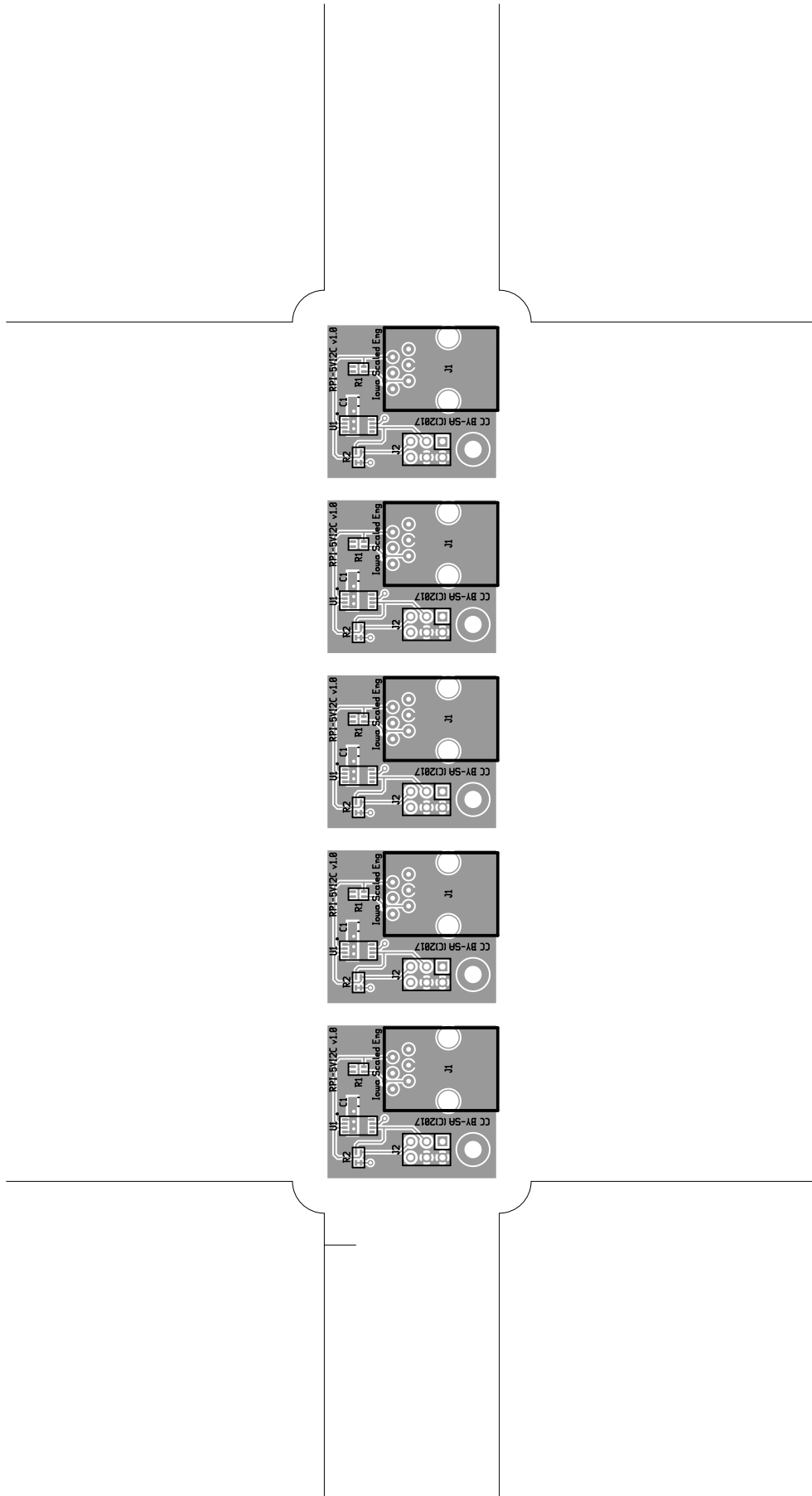
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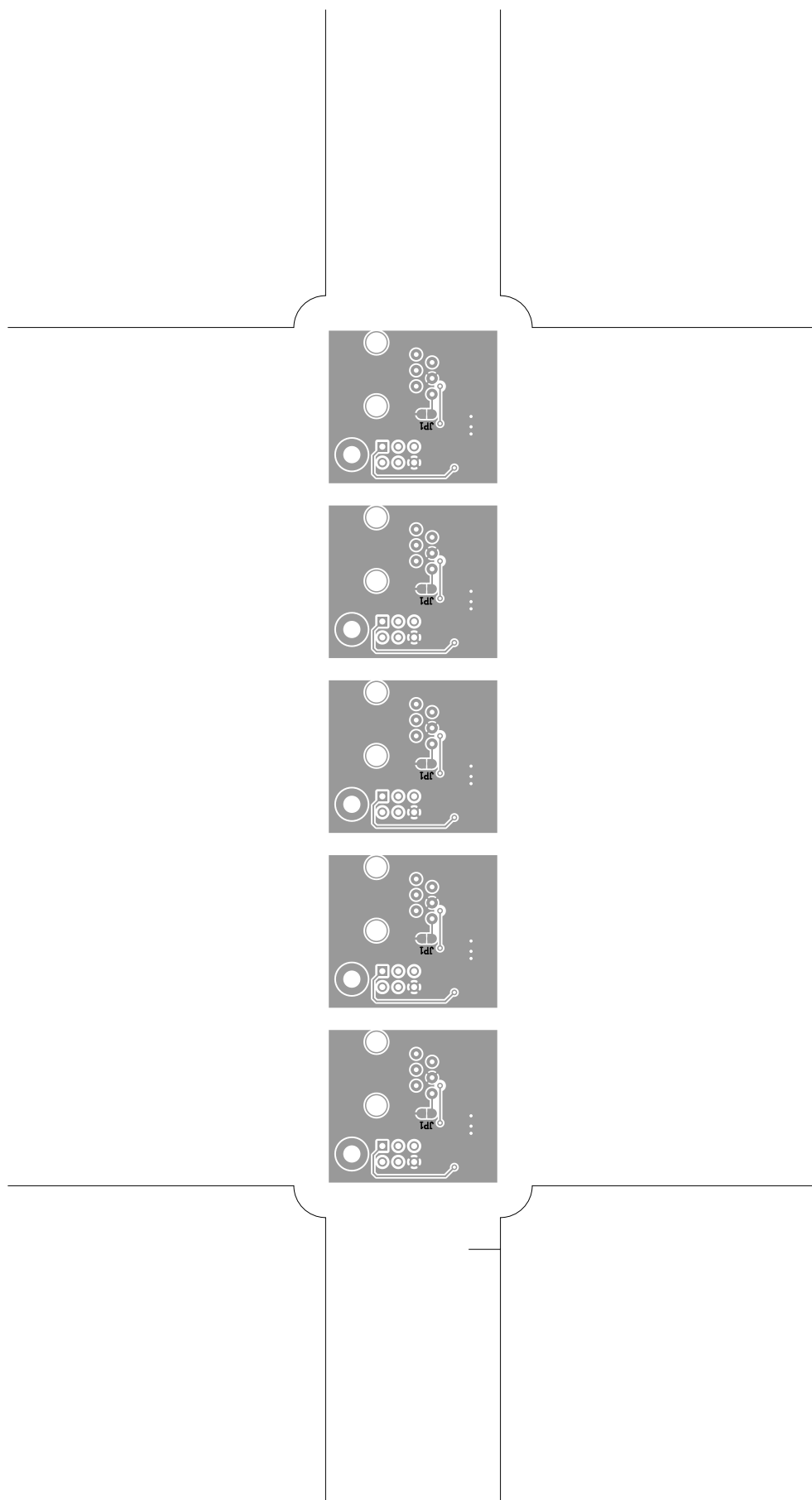
1d0











bottomassembly (mirrored), scale = 1:1.000
rpi-5vi2c-panel.pcb

There are 5 different drill sizes used in this layout, 105 holes total

Symbol	Diam. (Inch)	Count	Plated?
y	0.020	30	YES
+	0.030	30	YES
x	0.038	30	YES
o	0.108	5	YES
□	0.128	10	YES

Title: (unknown) - Fabrication Drawing

Author: Michael Petersen

Date: Thu 25 Oct 2018 04:49:50 PM GMT UTC

