

1		2		3		4		5	6		7	8
Dimension	Tolerance							REV.	DATE	DESIGN	APPROVED	DESCRIPTION
0-5mm	±0.1mm											
5-10mm	±0.15mm											
10-100mm	±0.2mm											
100mm-	±0.3%											
Angle	±0.5°											
R	±1											

A

B

C

D

E

F

A

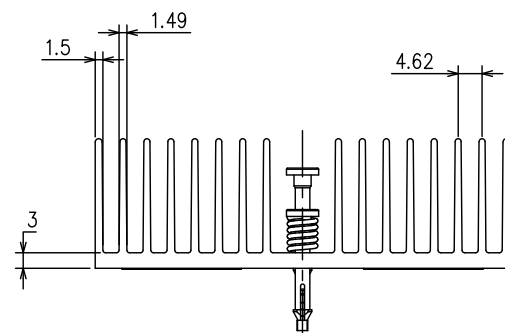
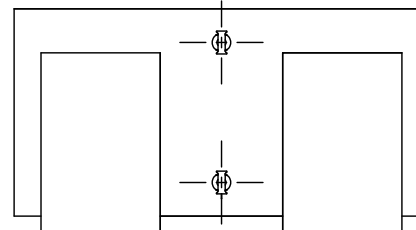
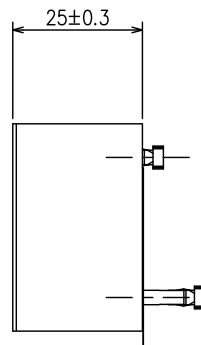
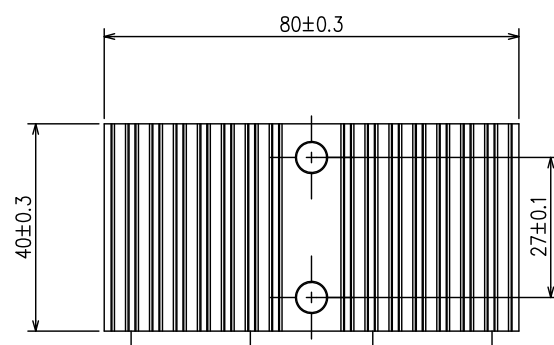
B

C

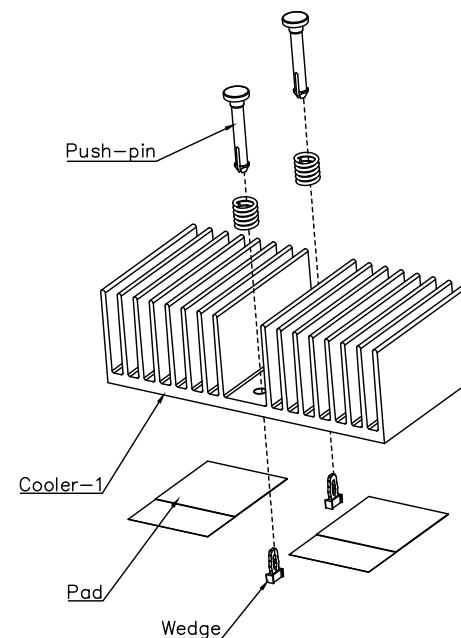
D

E

F



Each chipset has compressive force applied 5 lbs, chipset heigh 4.5 mm, M/B height 3mm



- NOTE:
1. All burrs max 0.05mm break sharp edges 0.03 mm max.
 2. All materials must meet the spec. of RoHS.
 3. The surfaces of the product can't have any contamin,scraches, and inger marks.
 4. All materials must halogen-free .
 5. Finish: clean (nataural color)

4	Wedge		2	
3	Push-pin	Copper	2	
2	Pad	PCS-TC-11	2	23x23mm
1	Cooler-1	A6063	1	clean
No.	PART NAME	MATERIAL	PCS	REMARKS

DRAWING	DESIGN	CHECKED	APPROVED
20160811	20160811	20160811	20160811
NICK			
SCALE	TITLE		
1/1	Zaius-VTM-1		
UNIT	Part Number		
mm	Drawing No.	HS855360-C	
FURUKAWA ELECTRIC CO.,LTD		PAGE	1/1