EmbSys2 LaunchPad EK_TM4C1294XL

			Setting 2	Setting 3	Setting 6	Setting 7	Setting 11	Setting 13
		I/0	Pin Port <i>Zusatz</i>	Pin Port <i>Zusatz</i>	Pin Port <i>Zusatz</i>	Pin Port <i>Zusatz</i>	Pin Port <i>Zusatz</i>	
Digital	Analyzer -	DIO 0		3 PP.0 UART6_RX	3 PP.0 esp_dbgOutSig (3)	3 PP.0 esp_dbgOutSig (3)		
		DIO 1	4 PP.1 UART6_TX	4 PP.1 UART6_TX	40 PG.1 esp_dbgOutSig (4)	40 PG.1 esp_dbgOutSig (4)		
		DIO 2	14 PQ.3 <i>SPI3_MISO</i>	14 PQ.3 <i>SPI3_MISO</i>	28 PK.3 esp_dbgOutSig (7)	28 PK.3 esp_dbgOutSig (7)		
		DIO 3	15 PQ.2 SPI3_MOSI	15 PQ.2 SPI3_MOSI	29 PA.4 esp_dbgOutSig (0)	29 PA.4 esp_dbgOutSig (0)		
		DIO 4	7 PQ.0 SPI3_CLK	7 PQ.0 SPI3_CLK	30 PA.5 esp_dbgOutSig (1)	30 PA.5 esp_dbgOutSig (1)		
		DIO 5	12 PQ.1 <i>SPI_CS</i>	12 PQ.1 <i>SPI_CS</i>	31 PK.7 esp_dbgOutSig (11)	31 PK.7 esp_dbgOutSig (11)		
		DIO 6	13 PP.3 -	13 PP.3 -	34 PH.0 esp_dbgOutSig (5)	34 PH.0 esp_dbgOutSig (5)		
		DIO 7	18 PP.5 -	18 PP.5 -	35 PM.2 esp_dbgOutSig (14)	35 PM.2 esp_dbgOutSig (14)		
		DIO 8	11 PM.6 PLATTFORM_CS	11 PM.6 PLATTFORM_CS	32 PK.6 esp_dbgOutSig (10)	32 PK.6 esp_dbgOutSig (10)		
		DIO 9	8 PP.4 PLATTFORM Interrupt	8 PP.4 PLATTFORM Interrupt	33 PH.1 esp_dbgOutSig (6)	33 PH.1 esp_dbgOutSig (6)		
		DIO 10	_	9 PN.5 <i>I2C2_SCL</i>	36 PM.1 esp_dbgOutSig (13)	36 PM.1 esp_dbgOutSig (13)		
			10 PN.4 <i>I2C2_SDA</i>	10 PN.4 <i>I2C2_SDA</i>	39 PK.4 esp_dbgOutSig (8)	39 PK.4 esp_dbgOutSig (8)		
		DIO 12	37 PM.0 esp_dbgOutSig (12)	37 PM.0 esp_dbgOutSig (12)	37 PM.0 esp_dbgOutSig (12)	37 PM.0 esp_dbgOutSig (12)		
		DIO 13	38 PK.5 esp_dbgOutSig (9)	38 PK.5 esp_dbgOutSig (9)	38 PK.5 esp_dbgOutSig (9)	38 PK.5 esp_dbgOutSig (9)		
			19 PM.7 esp_dbgOutSig (15)	19 PM.7 esp_dbgOutSig (15)	19 PM.7 <i>esp_dbgOutSig (15)</i>	19 PM.7 <i>esp_dbgOutSig (15)</i>		
			17 PA.7 esp_dbgOutSig (2)	17 PA.7 esp_dbgOutSig (2)	17 PA.7 esp_dbgOutSig (2)	17 PA.7 esp_dbgOutSig (2)		
	Patterns	DIO 0	same signals,	same signals,	same signals,	same signals,	same signals,	same signals,
		•••	use with carel	use with care!	use with care!	use with care!	use with care!	use with care!
		DIO 15						
Analog	Scope	C1		2 PD.2 <i>A13</i>	2 PD.2 A13	2 PD.2 <i>A13</i>	29 PA.4 -	29 PA.4 -
		C2	6 PD.5 <i>A6</i>	6 PD.5 <i>A6</i>	6 PD.5 <i>A6</i>	6 PD.5 <i>A6</i>	30 PA.5 -	30 PA.5 -
	WaveGen	AWG 1		2 PD.2 <i>A13</i>		2 PD.2 <i>A13</i>	23 PB.4 <i>A10</i>	23 PB.4 <i>A10</i>
		AWG 2		6 PD.5 <i>A6</i>		6 PD.5 <i>A6</i>	24 PB.5 <i>A11</i>	24 PB.5 <i>A11</i>

GPIO
UART
SPI
I2C
AnalogIn
Spez. (ESP)