

Features

- Low R_{DS(ON)}
- Epoxy Meets UL 94 V-0 Flammability Rating
- · Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

P-Channel MOSFET

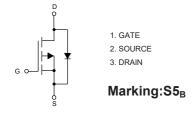
Maximum Ratings

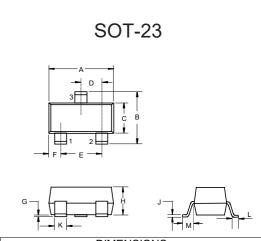
- Operating Junction Temperature Range: -55°C to +150°C
- Storage Temperature: -55°C to +150°C
- Thermal Resistance: 90°C/W Junction to Ambient(Note 2)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DS}	-20	V
Gate-Source Voltage	V _{GS}	±10	V
Drain Current-Continuous	I _D	-4.2	Α
Drain Current-Pulse ^(Note 2)	I _{DM}	-21	Α
Power Dissipation	P _D	1.4	W

Note: 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

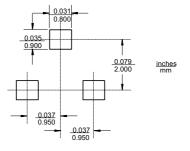
Internal Structure





DIMENSIONS					
DIM INC		HES	MM		NOTE
DIIVI	MIN	MAX	MIN	MAX	NOTE
Α	0.110	0.120	2.80	3.04	
В	0.083	0.104	2.10	2.64	
С	0.047	0.055	1.20	1.40	
D	0.034	0.041	0.85	1.05	
E	0.067	0.083	1.70	2.10	
F	0.018	0.024	0.45	0.60	
G	0.0004	0.006	0.01	0.15	
Н	0.035	0.043	0.90	1.10	
J	0.003	0.007	0.08	0.18	
K	0.012	0.020	0.30	0.51	
L	0.007	0.020	0.20	0.50	
М	0.022 REF		0.55	REF	

Suggested Solder Pad Layout





ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

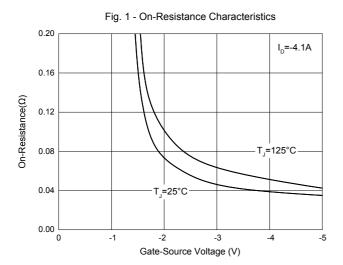
Parameter	Symbol	Test conditions	Min	Тур	Max	Unit	
Static Characteristics							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =-250μA	-20			V	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	-0.5		-0.9	V	
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±10V, V _{DS} =0V			±100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-20V, V _{GS} =0V			-1	μA	
		V _{GS} =-4.5V, I _D =-4A		30	39	mΩ	
Drain-Source On-Resistance ^(Note 4)	R _{DS(on)}	V _{GS} =-2.5V, I _D =-3A		38	49		
		V _{GS} =-1.8V, I _D =-2A		51	63		
Forward Tranconductance ^(Note 4)	g FS	V _{DS} =-5V, I _D =-4.1A	6			S	
Dynamic Characteristics			·				
Input Capacitance(Note 2,5)	C _{iss}			740			
Output Capacitance ^(Note 2,5)	C _{oss}	V _{DS} =-4V,V _{GS} =0V, f=1MHz		290		pF	
Reverse Transfer Capacitance (Note 2,5)	C _{rss}			190			
	0	V _{DS} =-4V,V _{GS} =-4.5V,I _D =-4.1A		7.8	15	nC	
Total Gate Charge ^(Note 2)	Q _g			4.5	9		
Gate-Source Chage ^(Note 2)	Q_{gs}	V _{DS} =-4V,V _{GS} =-2.5V,I _D =-4.1A		1.2			
Gage-Drain Charge ^(Note 2)	Q_{gd}			1.6			
Gate Resistance ^(Note 2,5)	R_g	f=1MHz	1.4	7	14	Ω	
Turn-On Delay Time ^(Note 2,5)	t _{d(on)}			13	20		
Turn-On Rise Time ^(Note 2,5)	t _r	V_{DD} =-4V, V_{GEN} =-4.5V, R_{L} =1.2 Ω ,		35	53		
Turn-Off Delay Time ^(Note 2,5)	t _{d(off)}	I_D =-3.3A, R_G =1 Ω		32	48	ns	
Turn-Off Fall Time ^(Note 2,5)	t _f			10	20		
Turn-On Delay Time ^(Note 2,5)	t _{d(on)}			5	10		
Turn-On Rise Time ^(Note 2,5)	t _r	V_{DD} =-4V, V_{GEN} =-8V, R_L =1.2 Ω ,		11	17		
Turn-Off Delay Time ^(Note 2,5)	t _{d(off)}	I_D =-3.3A, R_G =1 Ω		22	33	ns	
Turn-Off Fall Time ^(Note 2,5)	t _f			16	24		
Drain-Source Body Diode Cha	racteristi	cs		1	1	L	
Continuous Source-Drain Diode Current	I _S	T _C =25°C			-4.2	A	
Pulse Diode Forward Current(Note 4)	I _{SM}				-10		
Body Diode Voltage	V _{SD}	I _F =-3.3A		-0.8	-1.2	V	

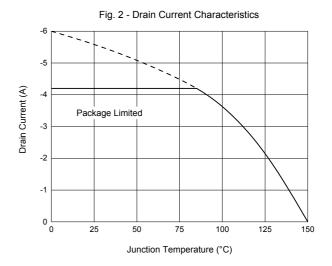
Note:

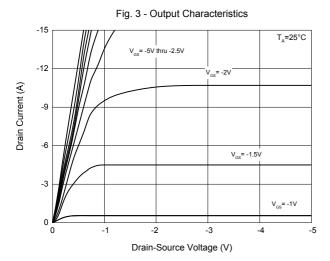
- 2. Guaranteed by Design, Not Subject to Production Testing.
- 3. Repetitive Rating: Pulse Width Limited by Max. Junction Temperature.
- 4. Pulse Test: Pulse Width≤300µs, Duty Cycle≤2%.
- 5. These Parameters Have No Way to Verify.

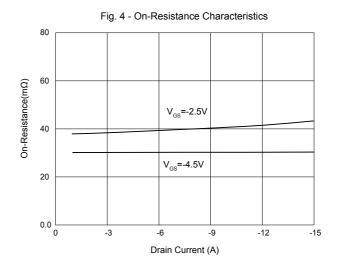


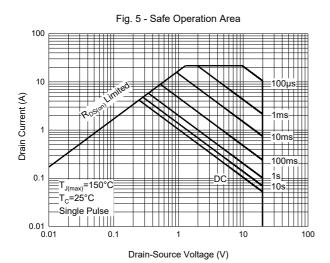
Curve Characteristics













Ordering Information

Device	Packing
Part Number-TP	Tape&Reel:3Kpcs/Reel

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