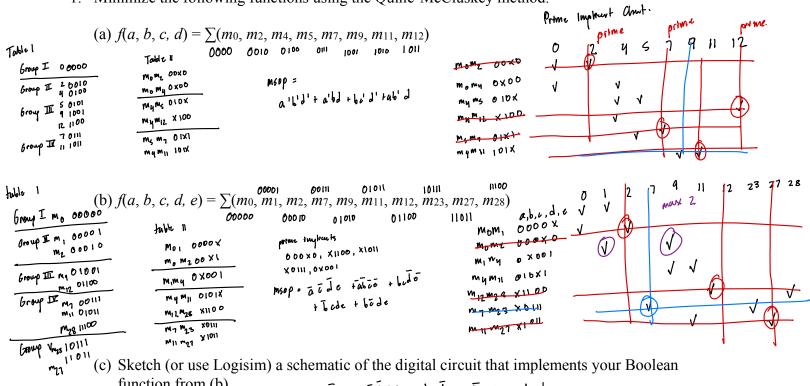
ECE 255 – Introduction to Digital Logic Design **Homework Assignment 4 Due October 13**

Name: Isau Abollin labellu & vols. utk. edu

1. Minimize the following functions using the Quine-McCluskey method.



(c) Sketch (or use Logisim) a schematic of the digital circuit that implements your Boolean Msop = ā o Je + ahie + bode + bode function from (b).

