

**ECE255 – Introduction to Digital Logic Design**  
**Homework Assignment 5**  
**Due November 13**

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1. Show how to use a **T FF** to *implement* a **D FF**.

- (a) First fill in the truth table below for next state  $Q^{+1}$  for the D FF, then fill in the table for input  $T$  such that the T FF yields the correct next state ( $Q^{+1}$ ) for each row in the table.

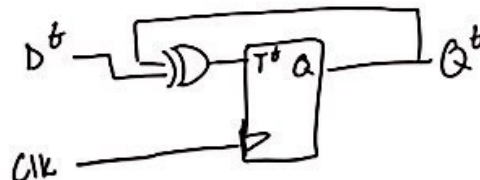
$D'$	$Q'$	$Q^{+1}$	$T$
0	0	0	0
0	1	0	1
1	0	1	1
1	1	1	0

$\begin{matrix} > \text{toggle off} \\ > \text{toggle on.} \end{matrix}$

- (b)  $T$  in the table you just completed is a function of  $D'$  (the input) and  $Q'$  (present state). This is a small state machine. Write the MSOP form for  $T$ . You may minimize to MSOP format any way you see fit.

$$\text{MSOP } T(D', Q') = Q'^{+1} = D' \Rightarrow Q'^{+1} = T' \oplus Q' = \boxed{T'D' \oplus Q'}$$

- (c) Sketch a schematic for this state machine showing the combinational logic for  $T$  and also the T FF with  $Q$  output. Draw by hand or use Logisim to sketch your circuit schematic.



2. Show how to use a **JK FF** to *implement* a **T FF**.

- (a) First fill in the truth table below for next state  $Q^{+1}$  for the JK FF, then fill in the table for inputs  $J'$  and  $K'$  such that the JK FF yields the correct next state ( $Q^{+1}$ ) for each row in the table.

$T$	$Q'$	$Q^{+1}$	$J'$	$K'$
0	0	0	0	X
0	1	1	X	1
1	0	1	1	X
1	1	0	X	0

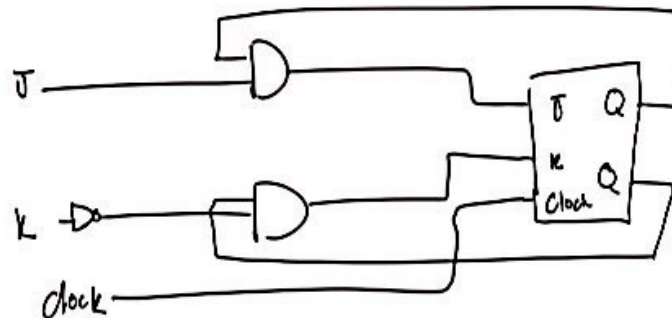
$\begin{matrix} JK = \text{hold present state.} \\ J=0, K=1 \text{ reset state} \\ J=1, K=0 \text{ set next state} \end{matrix}$

- (b)  $J'$  and  $K'$  in the table you just completed are functions of  $T'$  (the input) and  $Q'$  (present state). This is a small state machine. Write the MSOP forms for  $J'$  and  $K'$ . You may minimize to MSOP format any way you see fit.

$$\text{MSOP } J(T, Q) = \overline{T}^b Q^b (m_2)$$

$$\text{MSOP } K(T, Q) = \overline{T}^b Q^b (m_1)$$

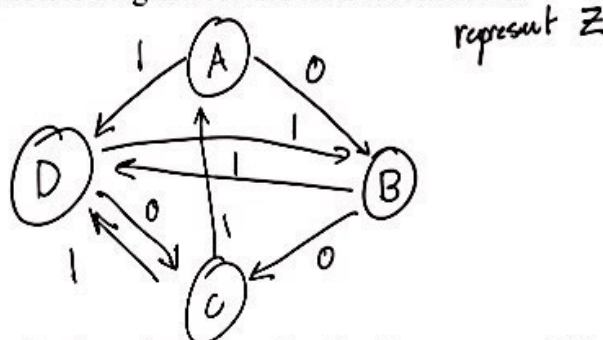
- (c) Sketch a schematic for this state machine showing the combinational logic for inputs  $J'$ ,  $K'$  and also the JK FF with  $Q$  output. Draw by hand or use Logisim to sketch your circuit schematic.



3. You are provided the following state table (assume 1-bit output  $z$ ; state assignments in parentheses):

	$x$	
	0	1
$A(00)$	$D/1$	$B/0$
$B(01)$	$D/1$	$C/0$
$C(10)$	$D/1$	$A/0$
$D(11)$	$B/1$	$C/0$

- (a) Construct a state diagram for the state table shown.



- (b) What is the Boolean logic equation for the output variable  $z$ ?

$$z = \overline{x}$$