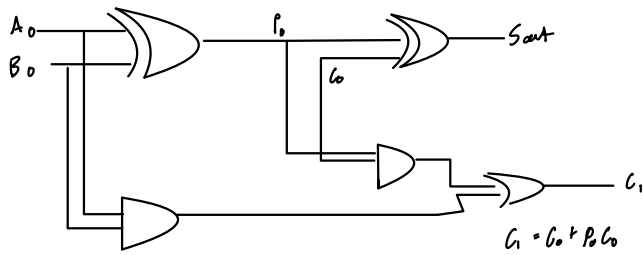
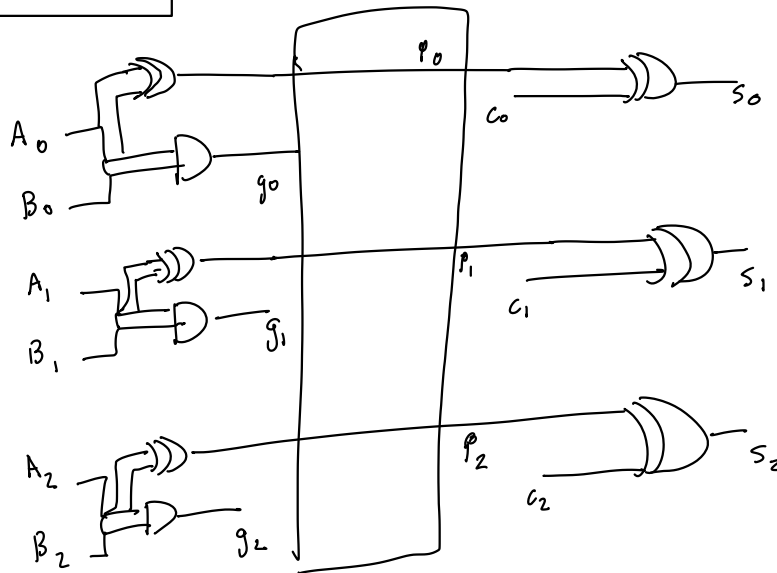
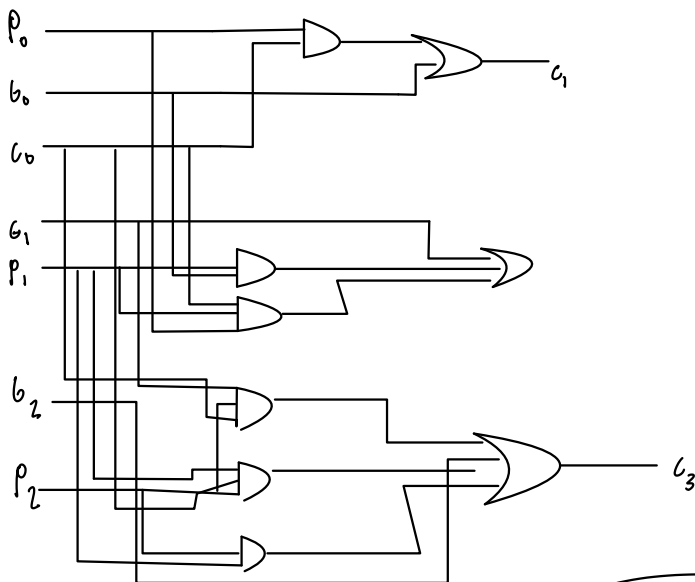


- (b) **Draw a schematic of your 3-bit adder**, showing the carry-lookahead circuit. You can sketch by hand or use a tool such as Logisim to produce your schematic.

*Adder propagation*



*full logic gates*



*roughly with adder.*