

laSalle

T1 UMain
T2 GND

LSMaker

M. HERVAS v2. 2

Charg

D8
R36
R18
IC13
R20
R16
D5

Q1

IC22

L1

D25

C17

C39

C38

C37

GND UBAT

CS7DOCLKDI - + CS6DOCLKDI - + CS5DOCLKDI - +
SPI0 K5 SPI1 K4 SPI2 K3

CEX2

1

40

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D2

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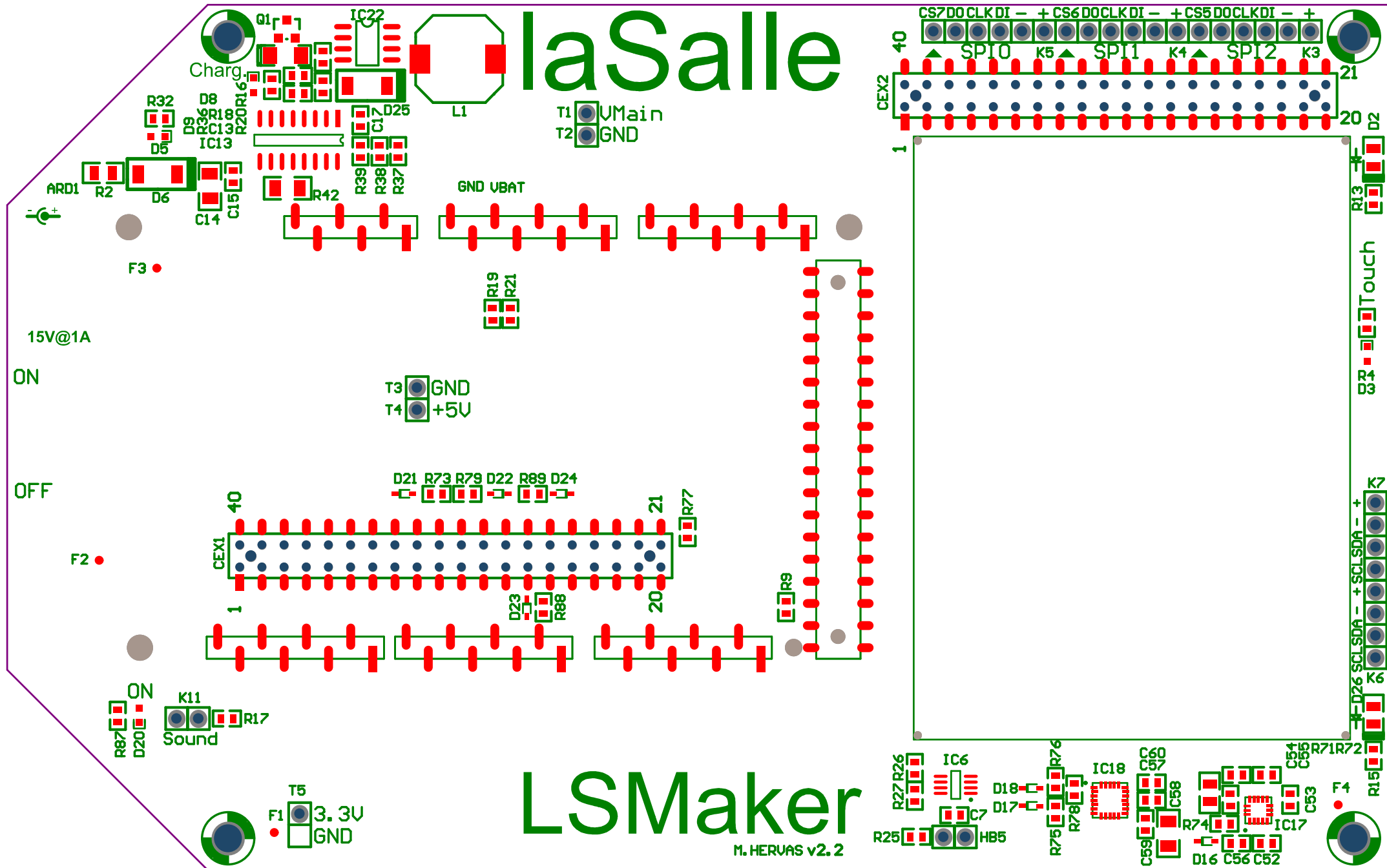
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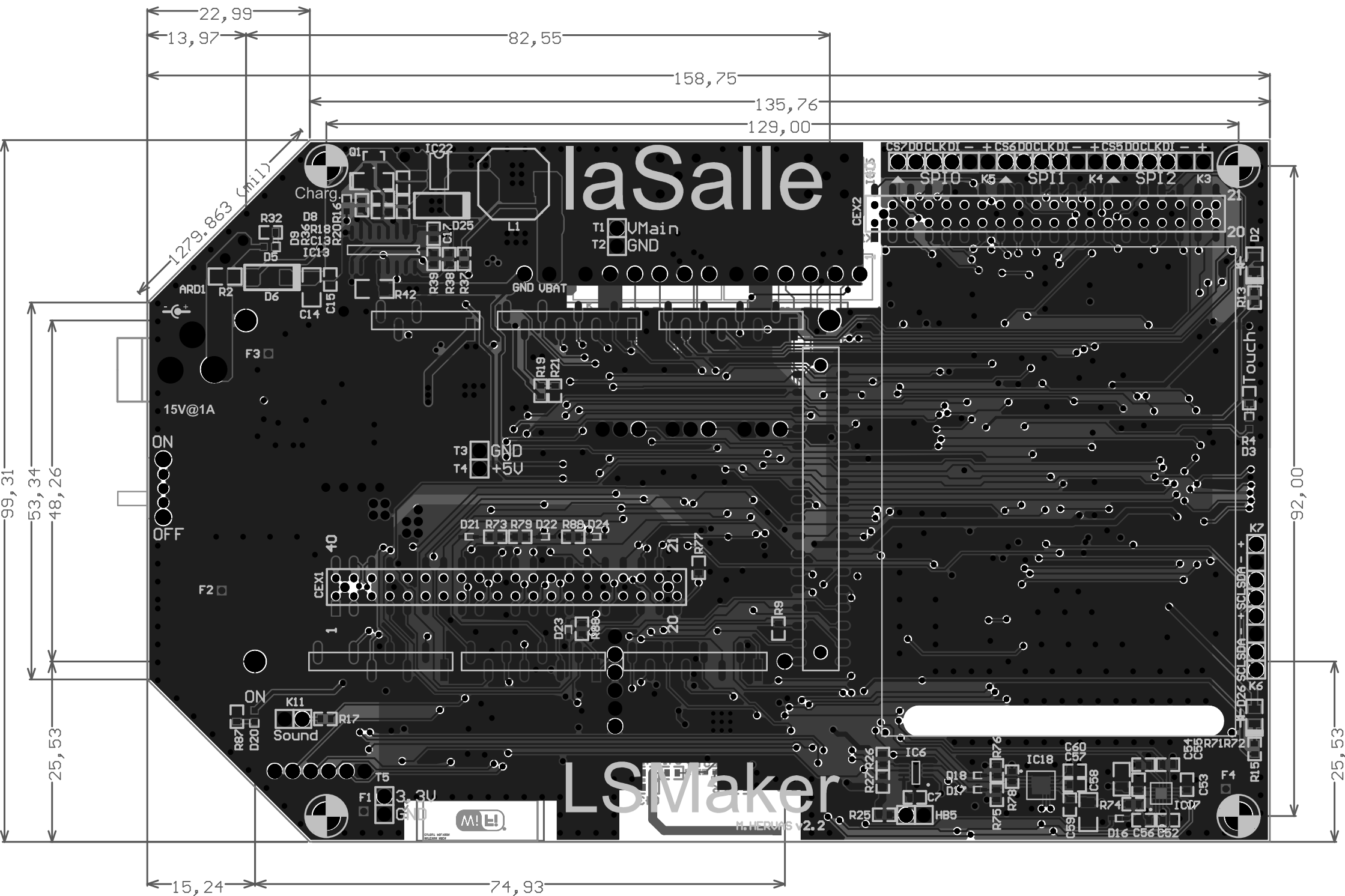
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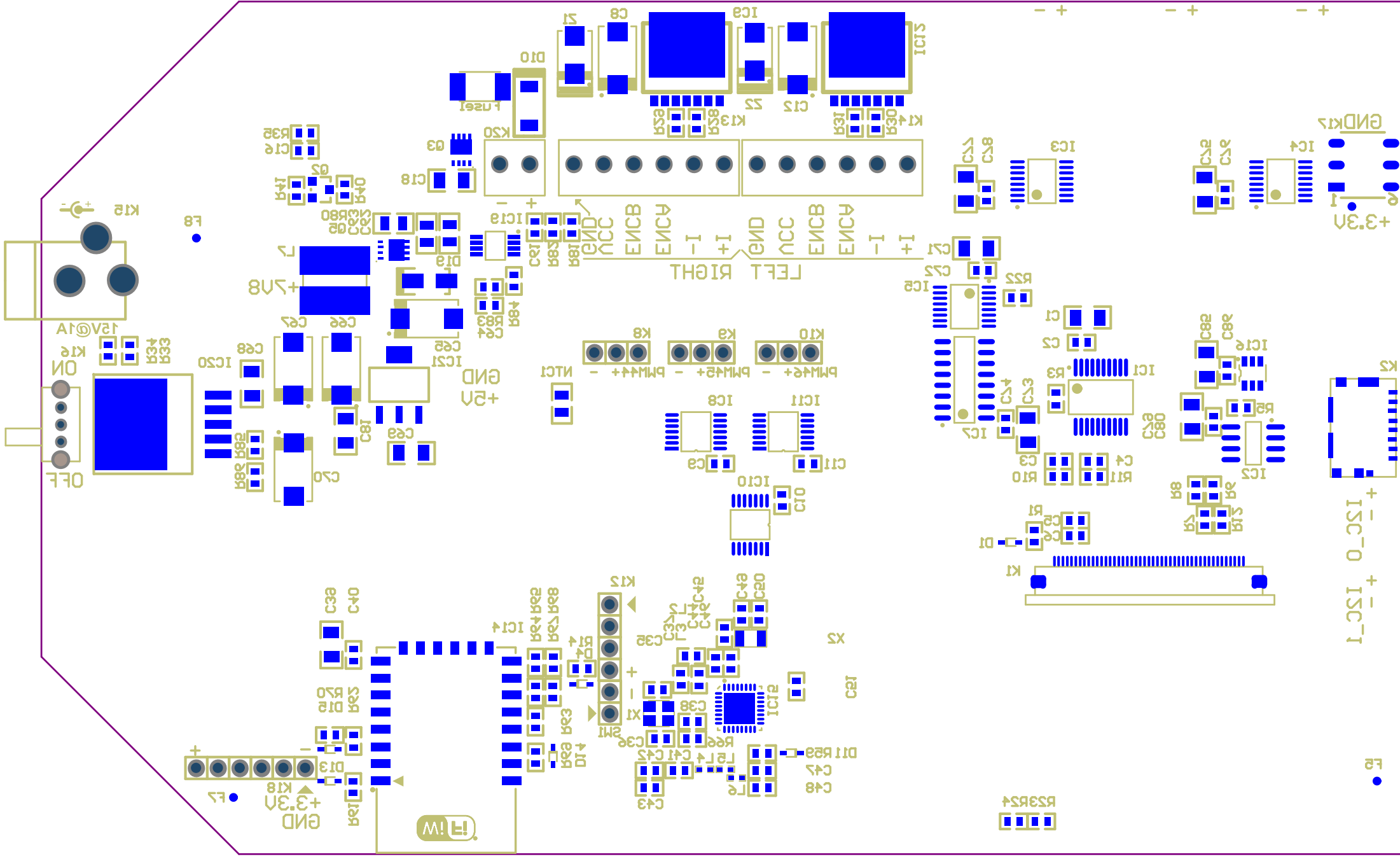
laSalle

LSMaker

M. HERVAS v2.2

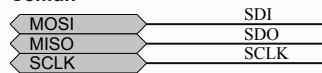




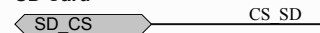


Display and uSD card

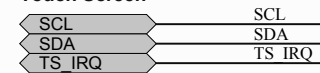
Comun



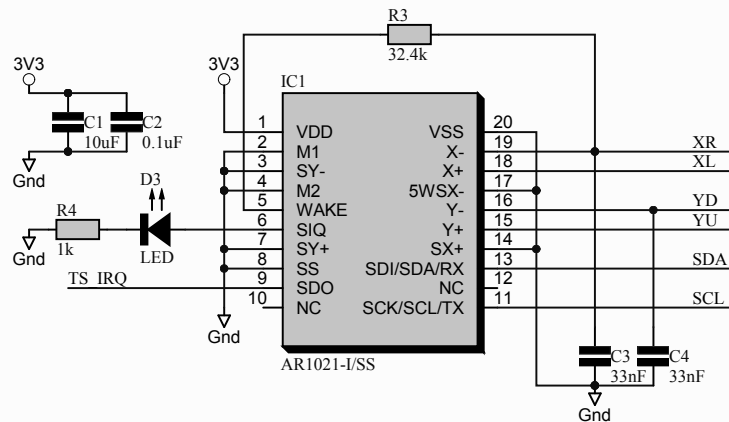
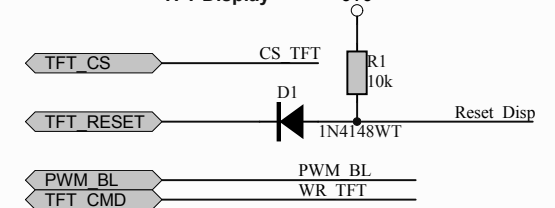
SD Card



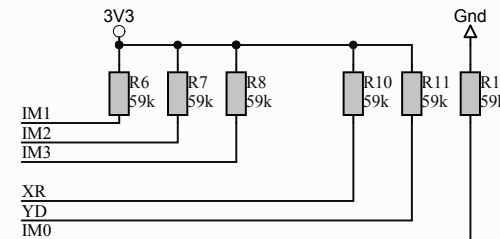
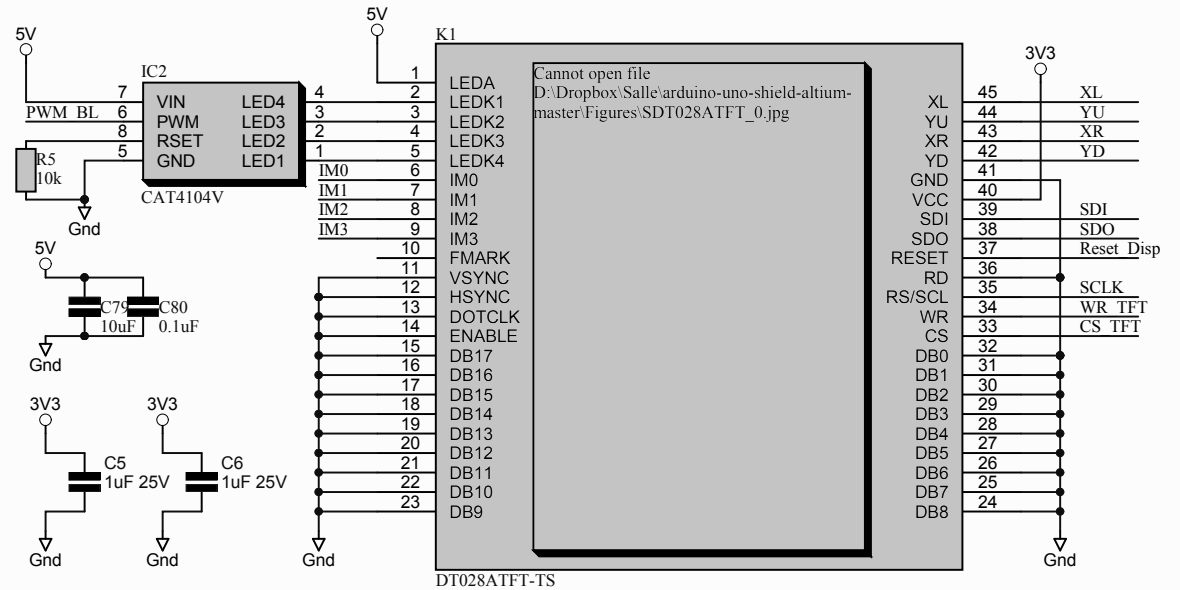
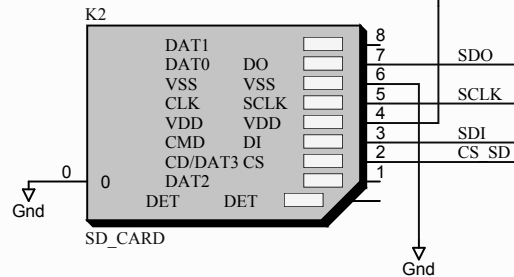
Touch Screen



TFT Display

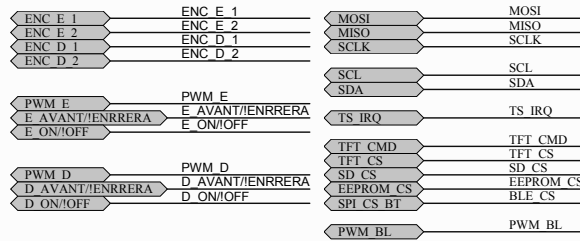


uSD Card



laSalle		LS Placa Base	
Universitat Ramon Llull		Display	
Codi: LS	Ver: V2	Autor: Mhervas	Data: 10-06-2016
Fitxer: Display	Verif.:	Data:	

CPU i connectors d'expansió

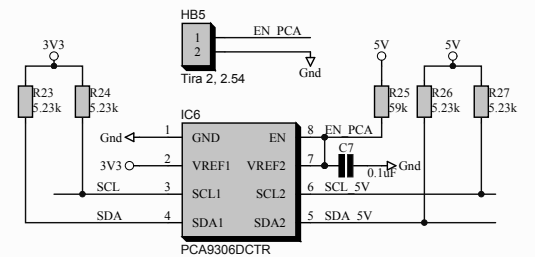
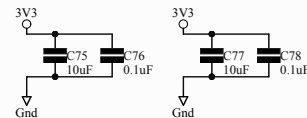
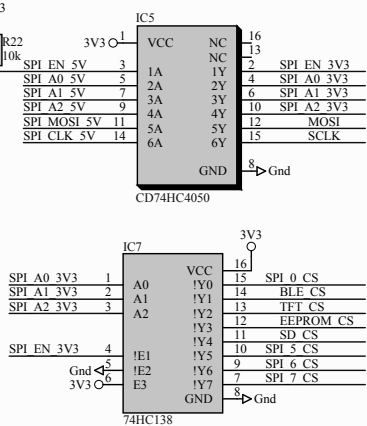
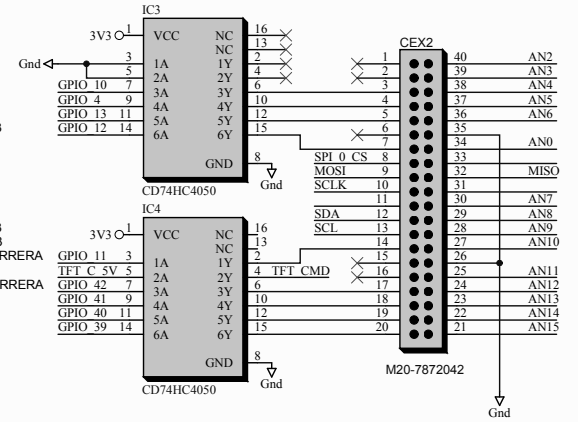
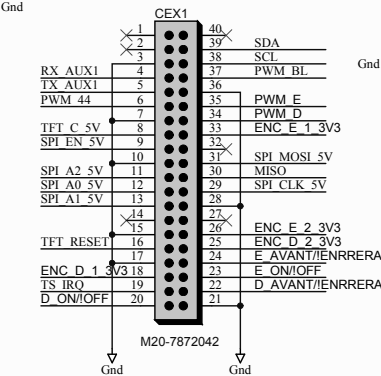
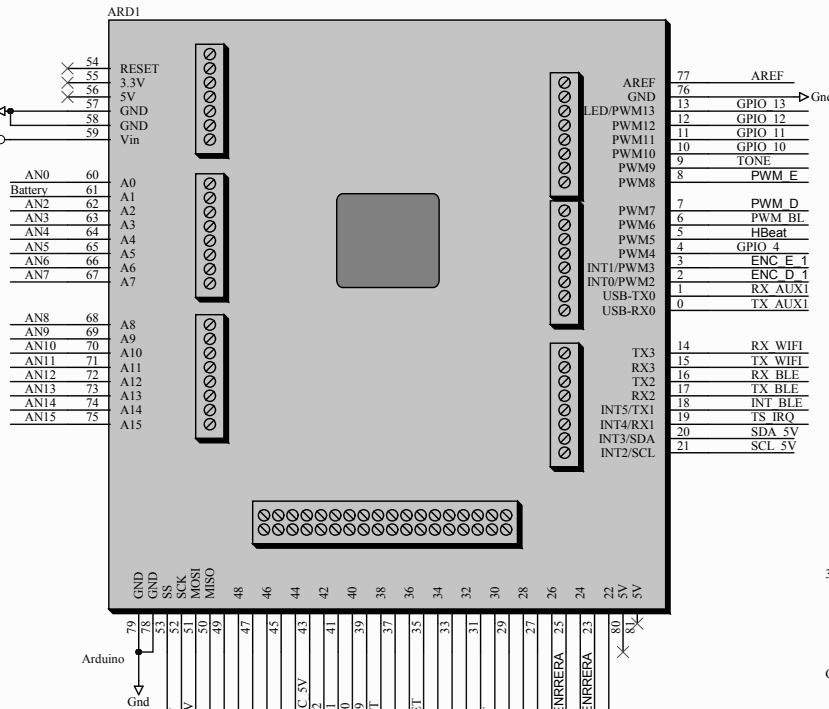
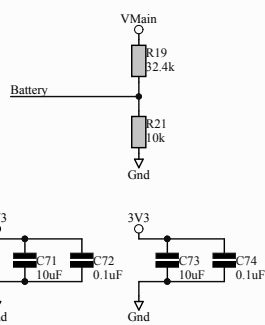
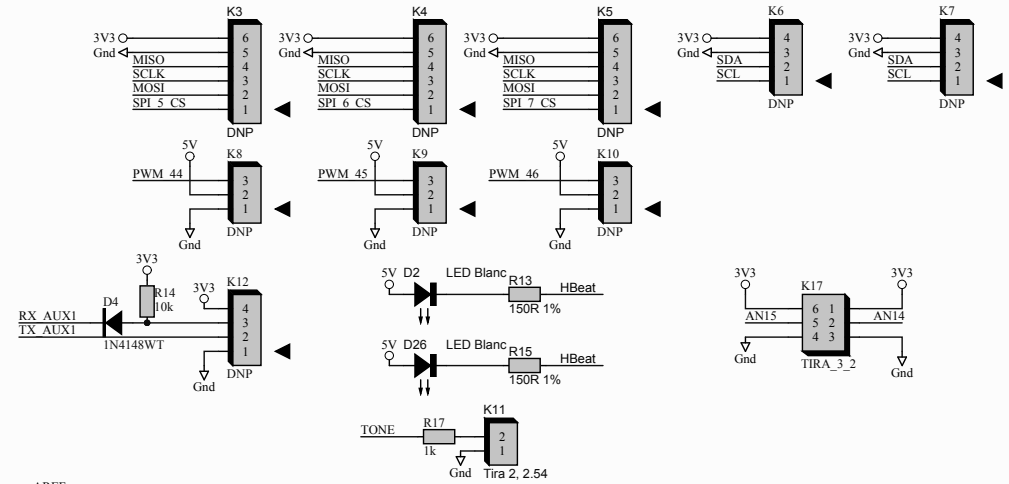
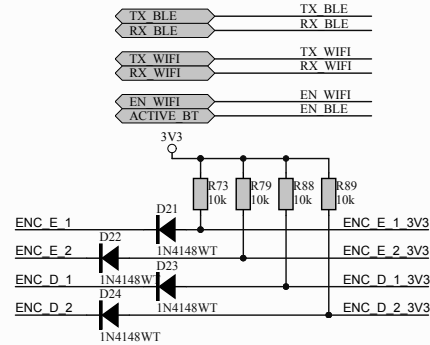


-Vin. The input voltage to the Uno board when it's using an external power source (as opposed to 5 volts from the USB connection or other regulated power source). You can supply voltage through this pin, or, if supplying voltage via the power jack, access it through this pin.

-5V. This pin outputs a regulated 5V from the regulator on the board. The board can be supplied with power either from the DC power jack (7-12V), the USB connector (5V), or the VIN pin of the board (7-12V). Supplying voltage via the 5V or 3.3V pins bypasses the regulator, and can damage your board. We don't advise it.

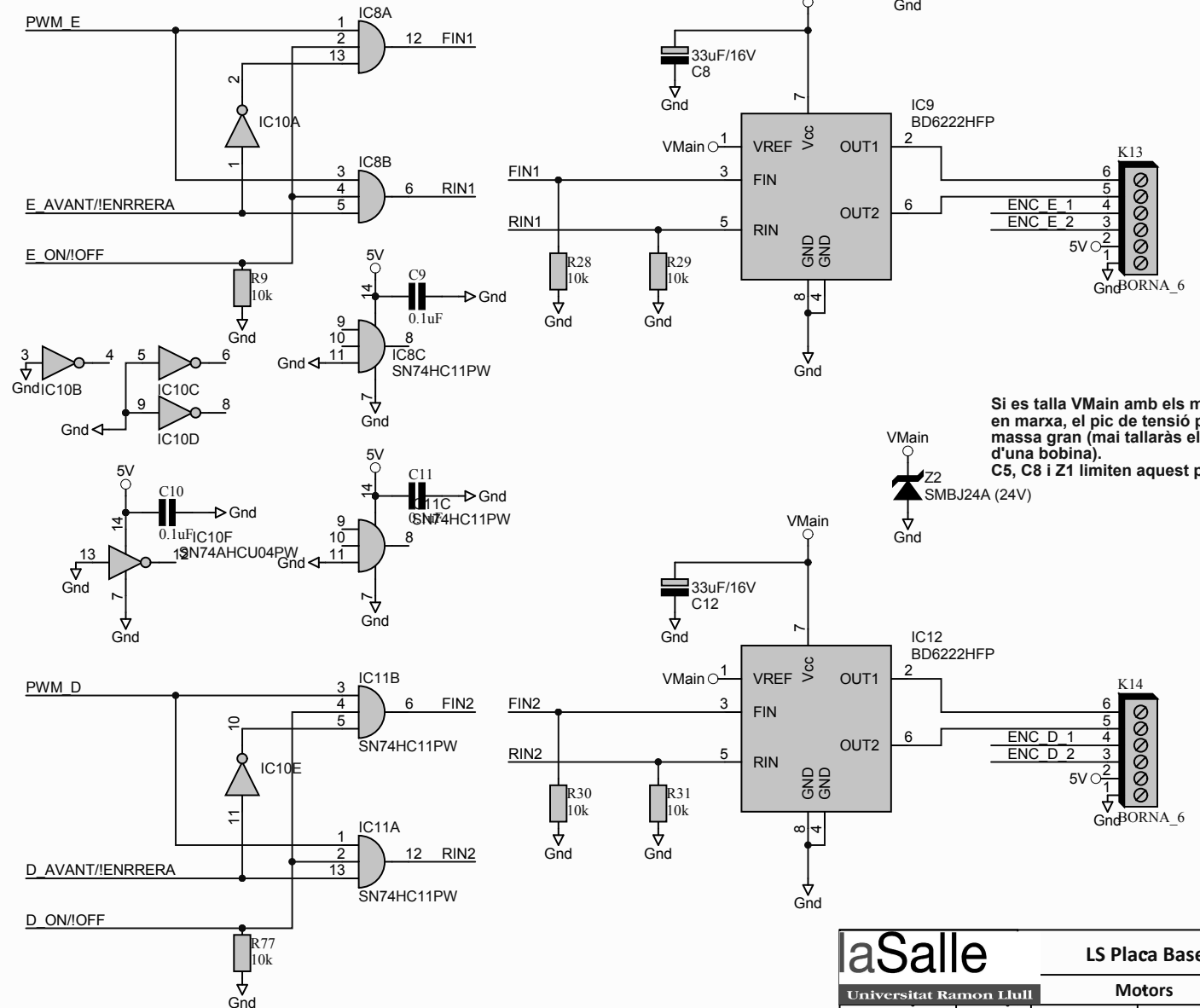
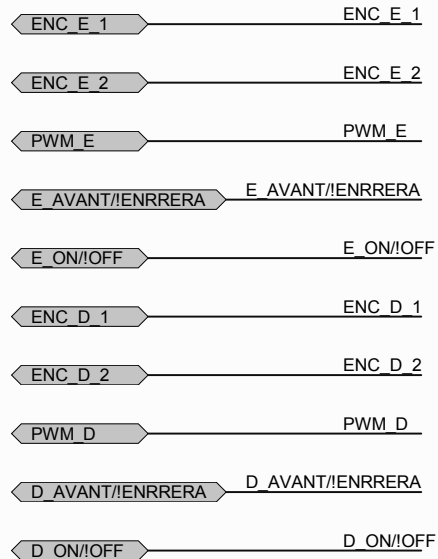
-3V3. A 3.3 volt supply generated by the on-board regulator. Maximum current draw is 50 mA.

- GND. Ground pins.
- IOREF. This pin on the Uno board provides



Adaptador de nivells del bus I2C

Motors

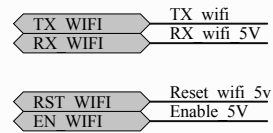


Si es talla VMain amb els motors en marxa, el pic de tensió pot ser massa gran (mai tallaràs el corrent d'una bobina). C5, C8 i Z1 limiten aquest pic

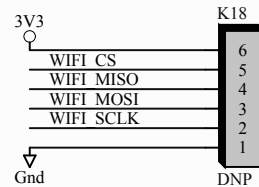
laSalle		LS Placa Base	
Universitat Ramon Llull		Motors	
Codi:	LS	Ver:	V2
Autor:	Mhervas	Data:	10-06-2016
Fitxer:	Motors	Verif.:	Data:

RF

Wifi Interface



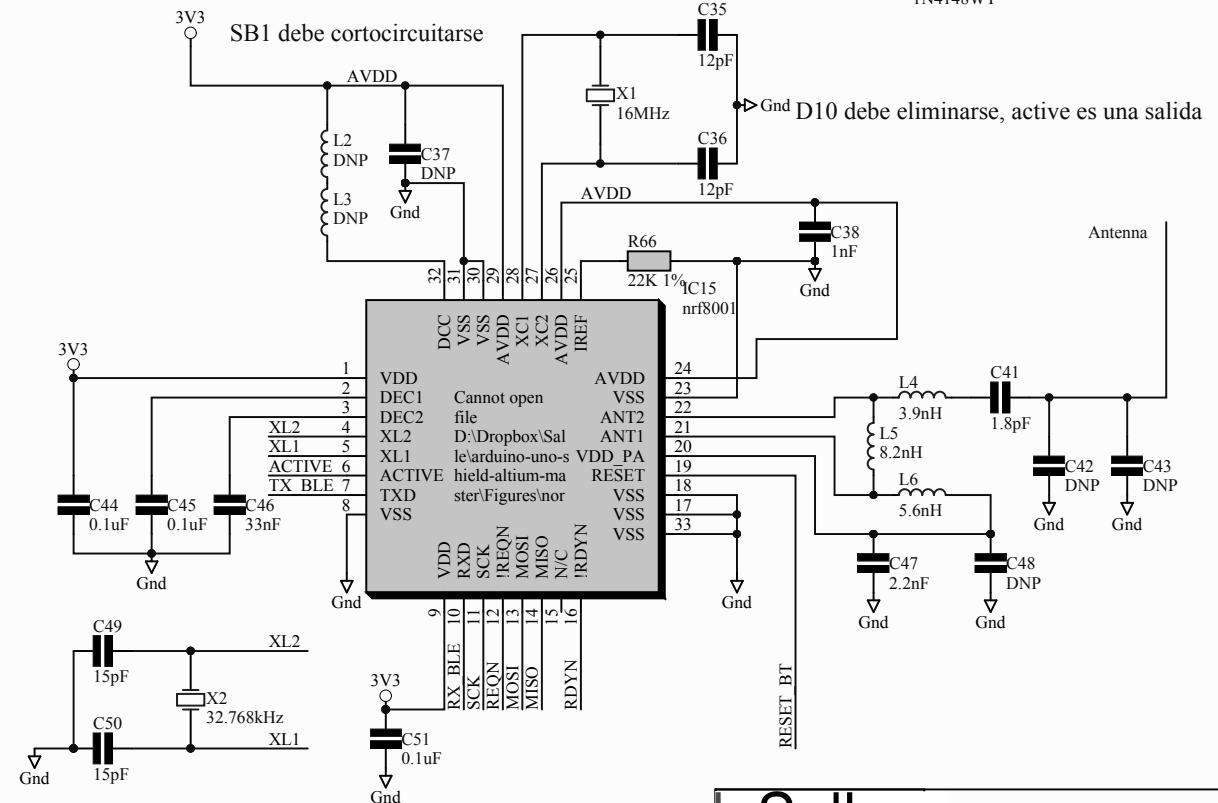
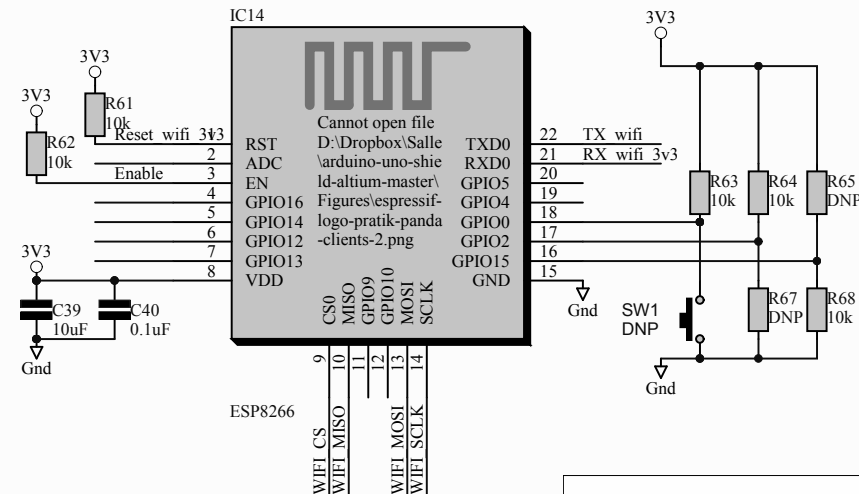
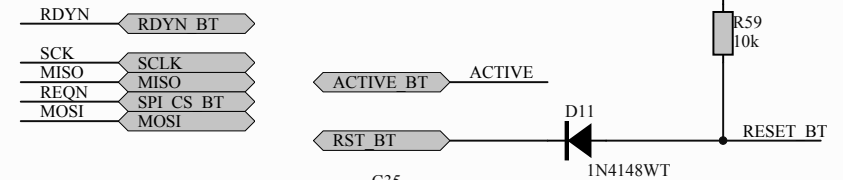
WIFI SPI Config



DTM UART interface



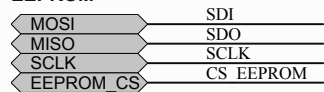
BLE Interface to Application processor



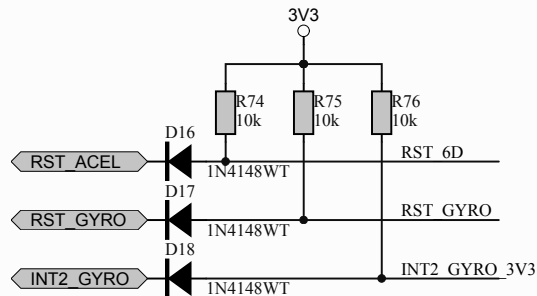
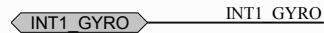
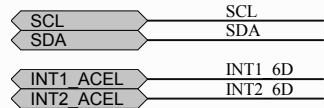
laSalle		LS Placa Base	
Universitat Ramon Llull		RF	
Codi: LS	Ver: V2	Autor: Mhervas	Data: 10-06-2016
Fitxer: RF	Verif.:	Data:	

Sensors i EEPROM

EEPROM

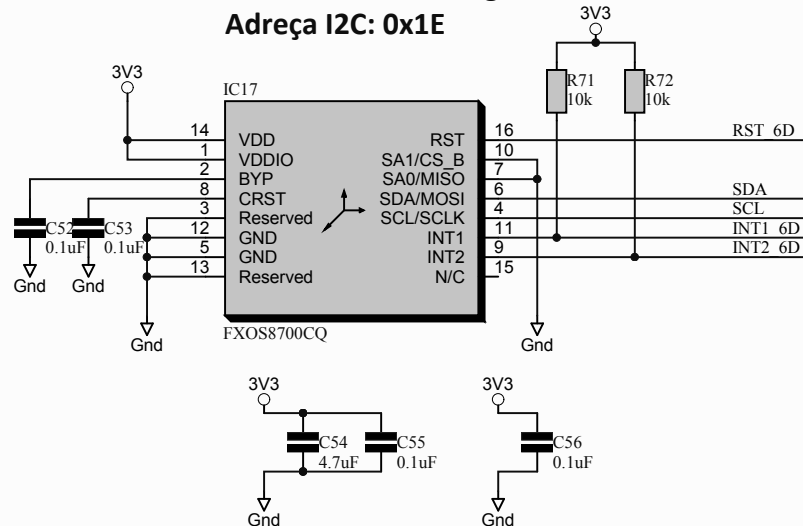


Sensors

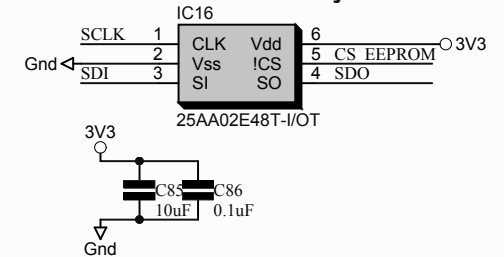


Accelerometer XYZ i Magnetometer

Adreça I2C: 0x1E

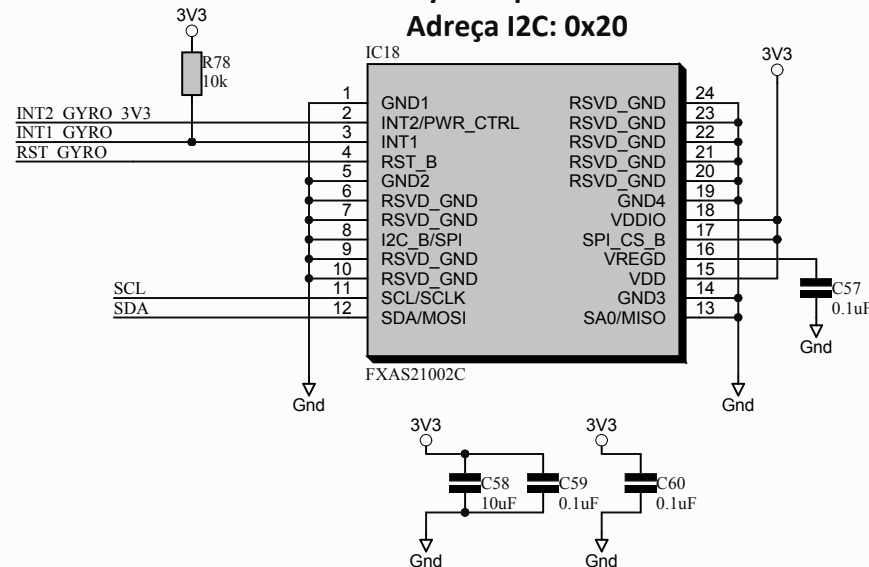


EEPROM amb adreça MAC



Gyroscope XYZ

Adreça I2C: 0x20



laSalle		LS Placa Base	
Universitat Ramon Llull		Sensors and Eeprom	
Codi: LS	Ver: V2	Autor: MHervas	Data: 10-06-2016
Fitxer:	RF	Verif.:	Data:

Tensions de 5V i 3.3V

