c:/My\_Designs/lab09/lab09/src/wave.asdb untitled.awc Signal name Value 80 160 240 320 400 480 560 660 720 **л**rst\_bar 1 лг clk 1 t... **J** rxd 1 t... **J** dord 1 When spi\_rxen is 0, data out Data won't be shifted to data\_out until retains last value as no other spi\_rxen is asserted, thus why it is UU for 40ns data can be shifted in **J** spi\_rxen 40 ns Data\_out alternates between When dord = 1, rxd is shifted into the AA and 55 depending on the value least significant bit which is why data\_out □ **J** data\_out 00 that is shifted from rxd which is a 1 or starts to decrease to 00 UU 02 AA 0 Ju data ou... 0 **.** data ou... **...** data\_ou... **™** data ou... 0 ₁ data ou.. 0 First bit of data from rxd is a 1 and is shifted to the most signifcant ı data ou... 0 bit on the risinng edge of clock **™** end sim fal... **∄ ™ g** 00 00 55 05 01 00 01 55 Cursor 1 800 ns