c:/My_Designs/task2/src/wave.asdb untitled.awc Signal name Value 80 160 240 320 400 480 ns 1 to 0 **⊪** d ■ clk 0 ■ en When rst_bar is 1 q output is 0 rst_bar 1 ${\bf q}$ is 1 when en is 1 and holds the value of 1 from input d on clock rising edge. q becomes 0 when en is 1 and d is 0 on 0 **-** q clock rising edge Cursor 1 500 ns