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/** BME680 I2C addresses */
#define BME680_I2C_ADDR_PRIMARY    UINT8_C(0x76)
#define BME680_I2C_ADDR_SECONDARY  UINT8_C(0x77)

/** BME680 unique chip identifier */
#define BME680_CHIP_ID    UINT8_C(0x61)

/** BME680 coefficients related defines */
#define BME680_COEFF_SIZE    UINT8_C(41)
#define BME680_COEFF_ADDR1_LEN    UINT8_C(25)
#define BME680_COEFF_ADDR2_LEN    UINT8_C(16)

/** BME680 field_x related defines */
#define BME680_FIELD_LENGTH    UINT8_C(15)
#define BME680_FIELD_ADDR_OFFSET    UINT8_C(17)

/** Soft reset command */
#define BME680_SOFT_RESET_CMD    UINT8_C(0xb6)

/** Error code definitions */
#define BME680_OK    INT8_C(0)
/* Errors */
#define BME680_E_NULL_PTR    INT8_C(-1)
#define BME680_E_COM_FAIL    INT8_C(-2)
#define BME680_E_DEV_NOT_FOUND    INT8_C(-3)
#define BME680_E_INVALID_LENGTH    INT8_C(-4)

/* Warnings */
#define BME680_W_DEFINE_PWR_MODE    INT8_C(1)
#define BME680_W_NO_NEW_DATA    INT8_C(2)

/* Info's */
#define BME680_I_MIN_CORRECTION    UINT8_C(1)
#define BME680_I_MAX_CORRECTION    UINT8_C(2)

/** Register map */
/** Other coefficient's address */
```