

- **#define BME680\_REG\_NBCONV\_INDEX** **UINT8\_C(1)**
  - **#define BME680\_REG\_RUN\_GAS\_INDEX** **UINT8\_C(1)**
  - **#define BME680\_REG\_HCTRL\_INDEX** **UINT8\_C(0)**
  - **#define BME680\_MAX\_OVERFLOW\_VAL** **INT32\_C(0x40000000)**
  - **#define BME680\_CONCAT\_BYTES(msb, lsb)** **((uint16\_t)msb << 8) | (uint16\_t)lsb)**
  - **#define BME680\_SET\_BITS(reg\_data, bitname, data)**
  - **#define BME680\_GET\_BITS(reg\_data, bitname)**
  - **#define BME680\_SET\_BITS\_POS\_0(reg\_data, bitname, data)**
  - **#define BME680\_GET\_BITS\_POS\_0(reg\_data, bitname)** **(reg\_data & (bitname##\_MSK))**
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## Detailed Description

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### Macro Definition Documentation

**#define BME680\_ADDR\_GAS\_CONF\_START** **UINT8\_C(0x64)**

Definition at line 153 of file bme680\_defs.h.

**#define BME680\_ADDR\_RANGE\_SW\_ERR\_ADDR** **UINT8\_C(0x04)**

Definition at line 151 of file bme680\_defs.h.

**#define BME680\_ADDR\_RES\_HEAT\_RANGE\_ADDR** **UINT8\_C(0x02)**

Definition at line 150 of file bme680\_defs.h.

**#define BME680\_ADDR\_RES\_HEAT\_VAL\_ADDR** **UINT8\_C(0x00)**

Register map Other coefficient's address

Definition at line 149 of file bme680\_defs.h.

**#define BME680\_ADDR\_SENS\_CONF\_START** **UINT8\_C(0x5A)**

Definition at line 152 of file bme680\_defs.h.

**#define BME680\_BIT\_H1\_DATA\_MSK** **UINT8\_C(0x0F)**

Definition at line 265 of file bme680\_defs.h.

**#define BME680\_CHIP\_ID** **UINT8\_C(0x61)**

BME680 unique chip identifier

Definition at line 117 of file bme680\_defs.h.

**#define BME680\_CHIP\_ID\_ADDR** **UINT8\_C(0xd0)**

Chip identifier

Definition at line 175 of file bme680\_defs.h.