

```

    lcd_spi_transmit_CMD(0xA0);    //initialize DDRAM addr-ctr
    delay_30us();

    for (i=0; i<=15; i++) {
        lcd_spi_transmit_DATA(dsp_buff_3[i]);    //send buff 3
        delay_30us();
    }
}

//*****
//
// Function Name      : "clr_dsp_buff"
// Date              :
// Version            : 1.0
// Target MCU         : SAML21J18B
// Target Hardware    ; DOG LCD
// Author             : Brandon Cheung, Ishabul Haque
// DESCRIPTION
// Clears LCD by filling display buffers with all spaces and calling the
// update_lcd_dog function
//
// Warnings           : none
// Restrictions        : none
// Algorithms          : none
// References          : none
//
// Revision History    : Initial version
//
//*****
extern void clr_dsp_buff(void) {
    sprintf(dsp_buff_1, "                ");    //loads display buffers with all spaces
    sprintf(dsp_buff_2, "                ");
    sprintf(dsp_buff_3, "                ");
    update_lcd_dog();
}

```