

#define BME680_SLEEP_MODE UINT8_C(0)
 Power mode settings
 Definition at line 207 of file bme680_defs.h.

#define BME680_SOFT_RESET_ADDR UINT8_C(0xe0)
 Soft reset register
 Definition at line 178 of file bme680_defs.h.

#define BME680_SOFT_RESET_CMD UINT8_C(0xb6)
 Soft reset command
 Definition at line 129 of file bme680_defs.h.

#define BME680_SPI_RD_MSK UINT8_C(0x80)

 Definition at line 263 of file bme680_defs.h.

#define BME680_SPI_WR_MSK UINT8_C(0x7f)

 Definition at line 264 of file bme680_defs.h.

#define BME680_T1_LSB_REG (33)

 Definition at line 303 of file bme680_defs.h.

#define BME680_T1_MSB_REG (34)

 Definition at line 304 of file bme680_defs.h.

#define BME680_T2_LSB_REG (1)
 Array Index to Field data mapping for Calibration Data
 Definition at line 275 of file bme680_defs.h.

#define BME680_T2_MSB_REG (2)

 Definition at line 276 of file bme680_defs.h.

#define BME680_T3_REG (3)

 Definition at line 277 of file bme680_defs.h.

#define BME680_TMP_BUFFER_LENGTH UINT8_C(40)
 Buffer length macro declaration
 Definition at line 225 of file bme680_defs.h.

#define BME680_W_DEFINE_PWR_MODE INT8_C(1)

 Definition at line 140 of file bme680_defs.h.