```c
    int8_t rslt = 0;                //return 0 for success, non-zero for failure
    reg_addr |= 0x80;               //mask bit 7 to a '1'
    REG_PORT_OUTCLR1 |= 0x80;       //CS = 0 -> BME680 selected
    spi_transfer(reg_addr);         //send control byte with register address
    while (len--) {
        *reg_data = spi_transfer(0);
        reg_data++;
    }
    REG_PORT_OUTSET1 |= 0x80;       //CS = 1 -> BME680 deselected
    return rslt;
}

int8_t user_spi_write (uint8_t dev_id, uint8_t reg_addr, uint8_t *reg_data, uint16_t len) {
    int8_t rslt = 0;
    REG_PORT_OUTCLR1 |= 0x80;       //CS = 0 -> BME680 selected
    spi_transfer(reg_addr); //send control byte with register address
    while (len--) {
        spi_transfer(reg_data);
        reg_data++;
    }
    REG_PORT_OUTSET1 |= 0x80;       //CS = 1 -> BME680 deselected
    return rslt;
}
```