Risk Response Plan Form

Project: Melody

1st Step: Risk Identification

Name of the Risk: PCB manufacturing delay or design mistake	ID N° 1

Risk Description:
The PCB arrives too late and/or doesn't work when it does.

2nd Step: Risk Evaluation

Impact: ☐1(Very Low) ☐2(Low) ☐3(Average) **X**4(High) ☐5(Very High)

Explanation:
If it doesn't work or arrives too late, it will delay a lot the electronic part of the project or make it impossible

Probability: ☐1(Very Low) ☐2(Low) ☐**3**(Average) **X4**(High) ☐5(Very High)

Explanation:
Not every student have a lot of experience designing PCBs, so it increases the probability of manufacturing delay or design mistake.

3rd Step: Risk Response Plan

Task, Who will do it, When it will be done!

Strategies and Tasks that should be performed in order to reduce the "Impact"/"Probability" of this risk:

Prevention Tasks:
Validate PCB with a breadboard prototype. Review PCB design with peers. Send PCB design for manufacturing early.

Mitigation Tasks:

Maintain a universal board version of the circuit as a fallback.
Prepare for minor fixes using jumper wires if the PCB has only minor flaws.

Transfer* (use in last case, avoid if possible):

Acceptance Tasks (avoid at all costs!):

(* At Integration Workshop 3, it would not be possible to "transfer" the Risk outside of the team!)

Re-evaluated Impact (1~5): 2	**Re-evaluated Probability (1-5):2**

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Form based on Gasnier, 2000 (IMAN Editor), adjusted by Wille(UTFPR), translated to English by Fabro(UTFPR).