# ASIC-Oriented Comparative Review of Hardware Security Algorithms for Internet of Things Applications

Mohamed A. Bahnasawi<sup>1</sup>, Khalid Ibrahim<sup>2</sup>, Ahmed Mohamed<sup>3</sup>, Mohamed Khalifa Mohamed<sup>4</sup>, Ahmed Moustafa<sup>5</sup>, Kareem Abdelmonem<sup>6</sup>, Yehea Ismail<sup>7</sup>, Hassan Mostafa<sup>8</sup>

1,3,5,6,8 Electronics and Electrical Communications Engineering Department, Cairo University, Giza, Egypt
<sup>2</sup>Electronics Department, Faculty of Information Engineering and Technology, German University in Cairo, Cairo, Egypt
<sup>4</sup>Electrical, Electronics and Communications Engineering Department, Faculty of Engineering, Alexandria University, Egypt
<sup>7,8</sup>Center for Nanoelectronics and Devices, AUC and Zewail City of Science and Technology, New Cairo, Egypt
<sup>1</sup>m\_bahnasawi@outlook.com; <sup>2</sup>khalid.mohie@student.guc.edu.eg; <sup>3</sup>ahmedmohamed.eece@gmail.com;
<sup>4</sup>mohamed.mohamed@student.tut.fi; <sup>5</sup>ahmed\_moustafa\_94@yahoo.com; <sup>6</sup>kareem.men3em@gmail.com; <sup>7</sup>y.ismail@aucegypt.edu;
<sup>8</sup>hmostafa@uwaterloo.ca }

Abstract— Research into the security of the Internet of Things (IoT) needs to utilize particular algorithms that offer ultra-low power consumption and a long lifespan, along with other parameters such as strong immunity against attacks, lower chip area and acceptable throughput. This paper offers an ASIC-oriented comparative review of the most popular and powerful cryptographic algorithms worldwide, namely AES, 3DES, Twofish and RSA. The ASIC implementations of those algorithms are analyzed by studying statistical data extracted from the ASIC layouts of the algorithms and comparing them to determine the most suited algorithms for IoT hardware-security applications. The AES algorithm is found to be the most suitable algorithm for IoT applications.

Keywords—Internet of Things (IoT); Hardware Security; Application Specific Integrated Circuit (ASIC); Cryptography; Cryptographic Algorithms; AES; 3DES; Twofish; RSA

### I. Introduction

Cryptography has been used for centuries to secure communication. Due to the enormous expansion of the digital world, secure cryptographic algorithms have become intensively used in a wide number of applications such as Wireless Local Area Networks (WLAN), Wireless Sensor Networks (WSN), smart cards and medical devices. Accordingly, efficient, secure and low power cryptographic algorithms have become crucial.

Many cryptographic algorithms are widely available and used in information security. They can be categorized into symmetric (private) and asymmetric (public) key cryptographic algorithms. In symmetric-key cryptography, only one key is used to encrypt and decrypt data. In contrast, asymmetric-key cryptography uses two keys, a private and a public key [1]. The public key is used for encryption while the private key is used for decryption. The RSA algorithm is an example of an asymmetric-key algorithm. Public key encryption is based on mathematical functions and is computationally intensive. Symmetric-key algorithms are suited for certain applications while asymmetric-key algorithms are better for different ones.

Applications have different requirements for the cryptographic algorithms that secure them, varying in the throughput needed, the extent of immunity against attacks, power consumption, the area needed on-chip, etc. The Internet of Things (IoT) focuses mainly on low-power implementations of security algorithms. This is because most of them are related to autonomous applications or embedded systems applications (i.e. an ultra-thin sensor system [2] embedded inside clothes or biomedical applications). These applications have very limited power budgets to lengthen the lifetime of their batteries.

In this paper, different symmetric-key and asymmetric-key cryptographic algorithms are investigated. ASIC implementations of these algorithms are presented and statistical data such as power consumption are compared to conclude which algorithms are the most suited for IoT security constraints. The following popular and powerful algorithms are chosen to be studied: AES, 3DES, Twofish and RSA.

The rest of this paper is organized as follows. Section II gives a brief overview of each of the chosen algorithms and analyzes the immunity against attacks of each algorithm. The statistical data extracted out of the ASIC implementations are tabulated and compared in Section III. In addition, the corresponding layout implementations are presented. Finally, Section IV concludes and discusses the results of the paper.

# II. IMMUNITY AGAINST ATTACKS

### A. AES

The Advanced Encryption Standard (AES) was established in 2001 by NIST as the current standard for encrypting electronic data. AES uses the Rijndael cipher, which is a symmetric-key block cipher. The Rijndael cipher supports numerous block and key sizes, but AES chooses to have a fixed block size of 128 bits and three key size variants to choose from: 128, 192 and 256. The AES therefore benefits from strong security and high flexibility.

Brute force attacks against AES implementations are currently not feasible due to the large key size of AES, but future advances in Quantum Computing could be a threat in this regard. In addition, there are possible attacks published in the literature that are computationally faster than a full brute force attack. attack, but they are still not computationally feasible [3]. No practical attacks are currently known against correct implementations of AES. Furthermore, researchers have attempted to enhance the security of AES against certain attacks, such as algebraic attacks.

AES has proved its immunity against attacks. Hence, AES has become the perfect choice for numerous applications, including not only wireless standards such as Wi-Fi, ZigBee and WiMAX but also, the security of smart cards and bit-stream security in FPGAs [4].

## B. 3DES

The fundamental standard for encrypting information was for a time a symmetric algorithm known as the Data Encryption Standard (DES). However, nowadays the 56-bit key of DES is not considered sufficient to encrypt critical data. Therefore, 3DES or the Triple Data Encryption Standard was developed to solve the clear issues in DES without needing to design a completely new cryptosystem.

3DES simply extends the key size of DES by applying the algorithm three times successively with three different keys. Thus, the combined key size is 168 bits. Moreover, repeating the iteration three times leads to benefits such as increases in encryption level and average time.

3DES is considerably more immune to brute force attacks than DES. Furthermore, the time required to check all possible keys at 50 billion keys per second is 800 days [5]. However, 3DES is vulnerable to meet-in-the-middle attacks. This is because 3DES uses three independent keys, giving an overall key length of 168 bits. The overall key length can be reduced to 112 bits if only two independent keys are used, but this might not be secure enough. A different attack, linear cryptanalysis (LC), was made in 1994 by Matsui and Yamagishi. LC is one of the most powerful attacks against block ciphers. In LC, a linear approximation is performed to discover the block cipher behavior. If enough pairs of a plaintext and its corresponding cipher text are available, LC can be used to obtain information about the key [6].

### C. Twofish

The Twofish cipher was first published in 1998 by Bruce Schneier, John Kelsey, Doug Whiting, David Wagner, Chris Hall and Niels Ferguson. It is a 128-bit symmetric key block cipher with a variable key length of 128, 192 or 256 bits. Furthermore, it is derived from the Blowfish, SAFER and DES algorithms and was one of the five finalists of the AES contest.

Niels Ferguson published an impossible differential attack in 1999 [7], which could break 6 rounds out of 16 for

the 256-bit key version. Moreover, one of the best published cryptanalysis on the Twofish is a truncated differential cryptanalysis of the full 16-round version. Theoretically, it would take 251 plaintexts, which are 32 petabytes of data, to find a good truncated differential pair [8]. Accordingly, Twofish can't be broken remotely.

## D. RSA

The RSA cryptosystem, invented by Ron Rivest, Adi Shamir and Len Adleman, was first publicized in the August 1977 issue of Scientific American. Furthermore, RSA is the most used public-key algorithm which is used to send an encrypted message without a separate exchange of secret keys. Nowadays, many protocols like S/MIME, SSH, SSL/TLS and OpenPGP rely on RSA for encryption and digital signature functions.

Prime factorization of the modulus N is the first attack on an RSA public key. If N is factorized, the attacker can get  $\Phi(N)$ , then the decryption exponent D. This attack is referred to as a brute-force attack on RSA [9]. This attack is not feasible when RSA is used properly with a sufficiently long key. Hence, it is recommended to use a key size larger than 2048 bits [10]. There are other indirect attacks that can break RSA without using prime factorization. However, they are not devastating attacks. Given that the RSA has been properly implemented, it can be trusted to provide the required security.

# III. COMPARISON OF THE ASIC IMPLEMENTATIONS' EXTRACTED DATA

Verified modules obtained for AES [11], 3DES [12], Twofish [13] and RSA [14] were implemented and synthesized using Synopsys DC Tools to get the netlist and the Cadence Encounter Tool to get the layout. These implementations are analyzed to establish a full comparison on post-layout level between the four algorithms, making it possible to estimate their performance in IoT applications.

This comparison is based on the UMC 130nm CMOS technology with an operating voltage of 1.08 V. Fig. 1 portrays the layouts of the cryptographic algorithms. Table I shows the extracted power in mWatt and area in mm<sup>2</sup> at a constant frequency and Table II shows the extracted frequency in MHz and area in mm<sup>2</sup> at a constant power.

The RSA implementation results in the highest power consumption and area compared to the other cryptographic algorithms at the same operating frequency. Comparing the results of RSA with other published designs [18] proves that the RSA algorithm is a high power encryption algorithm. Even though these designs depend on low power methods, the power consumption of each of them is still very high. Table III shows a comparison between our results and the results of some of the most suitable designs for each algorithm which are published in literature. This comparison is based on frequency, power and area.

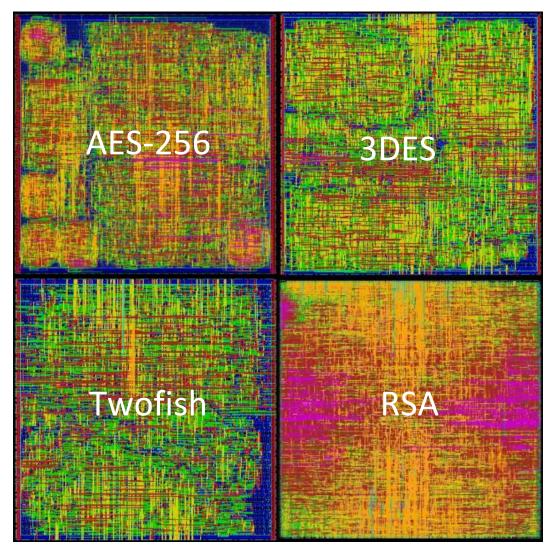


Fig. 1. ASIC layouts of the cryptographic algorithms

TABLE I. COMPARISON AT CONSTANT FREQUENCY

Frequency		10		100		200	
(MHz)		Power (mWatt)	Area (mm <sup>2</sup> )	Power (mWatt)	Area (mm <sup>2</sup> )	Power (mWatt)	Area (mm <sup>2</sup> )
AES	128	0.724	0.148	6.197	0.148	12.24	0.148
	192	0.729	0.149	6.218	0.149	12.31	0.149
	256	0.727	0.149	6.216	0.149	12.28	0.149
3DES		0.968	0.217	8.728	0.217	17.75	0.217
Twofish		0.675	0.101	N/A		N/A	
RSA		9.38	1.236	N/A		N/A	

TABLE II. COMPARISON AT CONSTANT POWER

Power		1		10		20	
(mWatt)		Freq. (MHz)	Area (mm <sup>2</sup> )	Freq. (MHz)	Area (mm <sup>2</sup> )	Freq. (MHz)	Area (mm <sup>2</sup> )
	128		0.148		0.148		0.150
AES	192	15	0.149	160	0.149	320	0.151
	256		0.149		0.149		0.151
3DES		10	0.217	110	0.217	227	0.217
Twofish		15	0.108	N/A		N/A	
RSA		0.5	1.030	11	1.257	N/A	

TABLE III. COMPARISON BETWEEN OUR DESIGNS AND PREVIOUS DESIGNS

Design		Freq. (MHz)	Area (mm <sup>2</sup> )	Power (mWatt)	Voltage (Volt)	Technology
AES	Ours [11]	20	0.148	1.328	1.08	UMC 130nm
	Ref. [15]	20	0.2489	1.477	1.62	SMIC 180nm
3DES	Ours [12]	181.81	0.217	15.75	1.08	UMC 130nm
	Ref. [16]	181.81	0.55	17.47	-	TSMC 130nm
Twofish	Ours [13]	66	0.1007	4.273	1.08	UMC 130nm
	Ref. [17]	66	2.75*2.75	44	3.3	TSMC 130nm
RSA	Ours [14]	11.9	1.283	11.43	1.08	UMC 130nm
	Ref. [18]	13.56	0.27	15	-	SMIC 130nm

As can be seen in Table III, our implementation of the AES verified module, obtained from [11], results in less area and power than the implementation of [15]. Similarly, our implementations of the 3DES and Twofish modules, which were obtained from [12] and [13], also use less area and consume less power than the implementations they are compared to. In the case of the AES implementations, the improved performance of our implementation, compared to the published design, can be credited in part to the improvement in technologies used.

## IV. CONCLUSION

This paper presents a thorough ASIC hardware implementation comparison of some of the most commonly used security algorithms in IoT applications. Our implementations are conducted using the ASIC approach and are evaluated using the UMC 130nm CMOS technology. This comparative review covers a variety of the most crucial specifications that are the main constraints of IoT applications, such as power consumption, frequency, throughput, area and immunity against attacks. The chosen algorithms in our study are AES, 3DES, Twofish and RSA.

The ASIC implementations of the AES, 3DES and Twofish have acceptable power consumption and chip area. Hence, we recommend them for ultra-low-power IoT applications that are mostly used in biomedical applications. The results for the RSA implementation are not acceptable for IoT applications due to its huge power consumption and chip area. In terms of throughput, RSA is also not recommended because of the huge number of required cycles to finish the encryption process. Choosing to use 3DES is not recommended in applications requiring impeccable security. The AES algorithm is the most secure against practical attacks, especially the 256-bit flavor. Furthermore, the AES implementation results show that it needs only 99, 119 and 139 cycles for the 128-bits, 192-bits and 256-bits keys, respectively. In addition, the power consumption and area of the AES implementation are more suitable for IoT applications, compared with the other algorithms that focus mainly on achieving the best security and ultra-high throughput. Thus, it is concluded that AES is the most suitable security algorithm for IoT applications.

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