

```

/* USER CODE BEGIN Header */
/**

*****

* @file           : main.c
* @brief          : Main program body

*****

* @attention

*
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*                 opensource.org/licenses/BSD-3-Clause
*

*****

*/
/* USER CODE END Header */

/* Includes
-----*/
#include "main.h"

/* Private includes
-----*/
/* USER CODE BEGIN Includes */

```

```
/* USER CODE END Includes */

/* Private typedef
-----*/
/* USER CODE BEGIN PTD */

/* USER CODE END PTD */

/* Private define
-----*/
/* USER CODE BEGIN PD */
/* USER CODE END PD */

/* Private macro
-----*/
/* USER CODE BEGIN PM */

/* USER CODE END PM */

/* Private variables
-----*/
I2C_HandleTypeDef hi2c1;

UART_HandleTypeDef huart2;

/* USER CODE BEGIN PV */

/* USER CODE END PV */

/* Private function prototypes
-----*/
void SystemClock_Config(void);
static void MX_GPIO_Init(void);
static void MX_I2C1_Init(void);
static void MX_USART2_UART_Init(void);
/* USER CODE BEGIN PFP */

/* USER CODE END PFP */
```

```
/* Private user code
-----*/

/* USER CODE BEGIN 0 */
uint8_t hexToAscii(uint8_t n)//4-bit hex value converted to an ascii
character
{
    if (n>=0 && n<=9) n = n + '0';
    else n = n - 10 + 'A';
    return n;
}
/* USER CODE END 0 */

int main(void)
{
    HAL_Init();
    SystemClock_Config();
    MX_GPIO_Init();
    MX_I2C1_Init();
    MX_USART2_UART_Init();
    /* USER CODE BEGIN 2 */
    //check that device is ready to operate
    if (HAL_I2C_IsDeviceReady(&hi2c1, 0xD0, 10, HAL_MAX_DELAY) == HAL_OK)
    {
        for (int i = 1; i<=10;i++) // indicator of ready device
        {
            HAL_GPIO_TogglePin(GPIOB, GPIO_PIN_3);
            HAL_Delay(250);
        }
    }

    //Transmit via I2C to set clock
    uint8_t secbuffer [2], minbuffer [2], hourbuffer [2], alarmsecbuffer[2],
    alarmminbuffer[2], alarmhourbuffer[2], control[2], status[2];

    control[0] = 0x0E;
    control[1] = 0x1d;
    HAL_I2C_Master_Transmit(&hi2c1, 0xD0, control, 2, 10);

    status[0] = 0x0F;
    status[1] = 0x88;
    HAL_I2C_Master_Transmit(&hi2c1, 0xD0, status, 2, 10);
```

```
// seconds
secbuffer[0] = 0x00; //register address
secbuffer[1] = 0x00; //data to put in register --> 0 sec
HAL_I2C_Master_Transmit(&hi2c1, 0xD0, secbuffer, 2, 10);
// minutes
minbuffer[0] = 0x01; //register address
minbuffer[1] = 0x45; //data to put in register --> 15 min
HAL_I2C_Master_Transmit(&hi2c1, 0xD0, minbuffer, 2, 10);
// hours
hourbuffer[0] = 0x02; //register address
hourbuffer[1] = 0x50; //data to put in register 01001001 --> 7 am
HAL_I2C_Master_Transmit(&hi2c1, 0xD0, hourbuffer, 2, 10);
//Receive via I2C and forward to UART

alarmsecbuffer[0] = 0x07;
alarmsecbuffer[1] = 0x00;
HAL_I2C_Master_Transmit(&hi2c1, 0xD0, alarmsecbuffer, 2, 10);

alarmminbuffer[0] = 0x08;
alarmminbuffer[1] = 0x46;
HAL_I2C_Master_Transmit(&hi2c1, 0xD0, alarmminbuffer, 2, 10);

alarmhourbuffer[0] = 0x09;
alarmhourbuffer[1] = 0x50;
HAL_I2C_Master_Transmit(&hi2c1, 0xD0, alarmhourbuffer, 2, 10);

uint8_t out[] = {0,0,':',0,0,':',0,0,'\r','\n'};
while (1)
{
    //send seconds register address 00h to read from
    HAL_I2C_Master_Transmit(&hi2c1, 0xD0, secbuffer, 1, 10);
    //read data of register 00h to secbuffer[1]
    HAL_I2C_Master_Receive(&hi2c1, 0xD1, secbuffer+1, 1, 10);
    //prepare UART output
    out[6] = hexToAscii(secbuffer[1] >> 4 );
    out[7] = hexToAscii(secbuffer[1] & 0x0F );
```

```
HAL_I2C_Master_Transmit(&hi2c1, 0xD0, minbuffer, 1, 10);
HAL_I2C_Master_Receive(&hi2c1, 0xD1, minbuffer+1, 1, 10);
out[3] = hexToAscii(minbuffer[1] >> 4 );
out[4] = hexToAscii(minbuffer[1] & 0x0F );
HAL_I2C_Master_Transmit(&hi2c1, 0xD0, hourbuffer, 1, 10);
HAL_I2C_Master_Receive(&hi2c1, 0xD1, hourbuffer+1, 1, 10);
out[0] = hexToAscii((hourbuffer[1] >> 4) & 1);
out[1] = hexToAscii(hourbuffer[1] & 0x0F);
// transmit time to UART
HAL_UART_Transmit(&huart2,out, sizeof(out), 10);
HAL_Delay(1000);
}
/* USER CODE END 2 */
}

/**
 * @brief System Clock Configuration
 * @retval None
 */
void SystemClock_Config(void)
{
    RCC_OscInitTypeDef RCC_OscInitStruct = {0};
    RCC_ClkInitTypeDef RCC_ClkInitStruct = {0};
    RCC_PeriphCLKInitTypeDef PeriphClkInit = {0};

    /** Configure LSE Drive Capability
     */
    HAL_PWR_EnableBkUpAccess();
    __HAL_RCC_LSEDRIVE_CONFIG(RCC_LSEDRIVE_LOW);
    /** Initializes the RCC Oscillators according to the specified
parameters
     * in the RCC_OscInitTypeDef structure.
     */
    RCC_OscInitStruct.OscillatorType =
RCC_OSCILLATORTYPE_LSE|RCC_OSCILLATORTYPE_MSI;
    RCC_OscInitStruct.LSEState = RCC_LSE_ON;
    RCC_OscInitStruct.MSIState = RCC_MSI_ON;
    RCC_OscInitStruct.MSICalibrationValue = 0;
    RCC_OscInitStruct.MSIClockRange = RCC_MSIRANGE_6;
    RCC_OscInitStruct.PLL.PLLState = RCC_PLL_ON;
```

```
RCC_OscInitStruct.PLL.PLLSource = RCC_PLLSOURCE_MSI;
RCC_OscInitStruct.PLL.PLLM = 1;
RCC_OscInitStruct.PLL.PLLN = 16;
RCC_OscInitStruct.PLL.PLLP = RCC_PLLP_DIV7;
RCC_OscInitStruct.PLL.PLLQ = RCC_PLLQ_DIV2;
RCC_OscInitStruct.PLL.PLLR = RCC_PLLR_DIV2;
if (HAL_RCC_OscConfig(&RCC_OscInitStruct) != HAL_OK)
{
    Error_Handler();
}
/** Initializes the CPU, AHB and APB buses clocks
 */
RCC_ClkInitStruct.ClockType = RCC_CLOCKTYPE_HCLK|RCC_CLOCKTYPE_SYCLK
                             |RCC_CLOCKTYPE_PCLK1|RCC_CLOCKTYPE_PCLK2;
RCC_ClkInitStruct.SYSCLKSource = RCC_SYSCLKSOURCE_PLLCLK;
RCC_ClkInitStruct.AHBCLKDivider = RCC_SYSCLK_DIV1;
RCC_ClkInitStruct.APB1CLKDivider = RCC_HCLK_DIV1;
RCC_ClkInitStruct.APB2CLKDivider = RCC_HCLK_DIV1;

if (HAL_RCC_ClockConfig(&RCC_ClkInitStruct, FLASH_LATENCY_1) != HAL_OK)
{
    Error_Handler();
}
PeriphClkInit.PeriphClockSelection =
RCC_PERIPHCLK_USART2|RCC_PERIPHCLK_I2C1;
PeriphClkInit.Usart2ClockSelection = RCC_USART2CLKSOURCE_PCLK1;
PeriphClkInit.I2C1ClockSelection = RCC_I2C1CLKSOURCE_PCLK1;
if (HAL_RCCEx_PeriphCLKConfig(&PeriphClkInit) != HAL_OK)
{
    Error_Handler();
}
/** Configure the main internal regulator output voltage
 */
if (HAL_PWREx_ControlVoltageScaling(PWR_REGULATOR_VOLTAGE_SCALE1) !=
HAL_OK)
{
    Error_Handler();
}
/** Enable MSI Auto calibration
 */
```

```
    HAL_RCCEx_EnableMSIPLMode();
}

/**
 * @brief I2C1 Initialization Function
 * @param None
 * @retval None
 */
static void MX_I2C1_Init(void)
{
    /* USER CODE BEGIN I2C1_Init 0 */

    /* USER CODE END I2C1_Init 0 */

    /* USER CODE BEGIN I2C1_Init 1 */

    /* USER CODE END I2C1_Init 1 */
    hi2c1.Instance = I2C1;
    hi2c1.Init.Timing = 0x00707CBB;
    hi2c1.Init.OwnAddress1 = 0;
    hi2c1.Init.AddressingMode = I2C_ADDRESSINGMODE_7BIT;
    hi2c1.Init.DualAddressMode = I2C_DUALADDRESS_DISABLE;
    hi2c1.Init.OwnAddress2 = 0;
    hi2c1.Init.OwnAddress2Masks = I2C_OA2_NOMASK;
    hi2c1.Init.GeneralCallMode = I2C_GENERALCALL_DISABLE;
    hi2c1.Init.NoStretchMode = I2C_NOSTRETCH_DISABLE;
    if (HAL_I2C_Init(&hi2c1) != HAL_OK)
    {
        Error_Handler();
    }
    /** Configure Analogue filter
     */
    if (HAL_I2CEx_ConfigAnalogFilter(&hi2c1, I2C_ANALOGFILTER_ENABLE) !=
HAL_OK)
    {
        Error_Handler();
    }
    /** Configure Digital filter
     */
}
```

```
if (HAL_I2CEx_ConfigDigitalFilter(&hi2c1, 0) != HAL_OK)
{
    Error_Handler();
}

/* USER CODE BEGIN I2C1_Init 2 */

/* USER CODE END I2C1_Init 2 */

}

/**
 * @brief USART2 Initialization Function
 * @param None
 * @retval None
 */
static void MX_USART2_UART_Init(void)
{
    /* USER CODE BEGIN USART2_Init 0 */

    /* USER CODE END USART2_Init 0 */

    /* USER CODE BEGIN USART2_Init 1 */

    /* USER CODE END USART2_Init 1 */
    huart2.Instance = USART2;
    huart2.Init.BaudRate = 115200;
    huart2.Init.WordLength = UART_WORDLENGTH_8B;
    huart2.Init.StopBits = UART_STOPBITS_1;
    huart2.Init.Parity = UART_PARITY_NONE;
    huart2.Init.Mode = UART_MODE_TX_RX;
    huart2.Init.HwFlowCtl = UART_HWCONTROL_NONE;
    huart2.Init.OverSampling = UART_OVERSAMPLING_16;
    huart2.Init.OneBitSampling = UART_ONE_BIT_SAMPLE_DISABLE;
    huart2.AdvancedInit.AdvFeatureInit = UART_ADVFEATURE_NO_INIT;
    if (HAL_UART_Init(&huart2) != HAL_OK)
    {
        Error_Handler();
    }
    /* USER CODE BEGIN USART2_Init 2 */
```



```
/* USER CODE END USART2_Init 2 */  
  
}  
  
/**  
 * @brief GPIO Initialization Function  
 * @param None  
 * @retval None  
 */  
static void MX_GPIO_Init(void)  
{  
    GPIO_InitTypeDef GPIO_InitStruct = {0};  
  
    /* GPIO Ports Clock Enable */  
    __HAL_RCC_GPIOC_CLK_ENABLE();  
    __HAL_RCC_GPIOA_CLK_ENABLE();  
    __HAL_RCC_GPIOB_CLK_ENABLE();  
  
    /*Configure GPIO pin Output Level */  
    HAL_GPIO_WritePin(GPIOA, GPIO_PIN_7, GPIO_PIN_RESET);  
  
    /*Configure GPIO pin Output Level */  
    HAL_GPIO_WritePin(GPIOB, GPIO_PIN_3, GPIO_PIN_RESET);  
  
    /*Configure GPIO pin : PA7 */  
    GPIO_InitStruct.Pin = GPIO_PIN_7;  
    GPIO_InitStruct.Mode = GPIO_MODE_OUTPUT_PP;  
    GPIO_InitStruct.Pull = GPIO_NOPULL;  
    GPIO_InitStruct.Speed = GPIO_SPEED_FREQ_LOW;  
    HAL_GPIO_Init(GPIOA, &GPIO_InitStruct);  
  
    /*Configure GPIO pin : PB3 */  
    GPIO_InitStruct.Pin = GPIO_PIN_3;  
    GPIO_InitStruct.Mode = GPIO_MODE_OUTPUT_PP;  
    GPIO_InitStruct.Pull = GPIO_NOPULL;  
    GPIO_InitStruct.Speed = GPIO_SPEED_FREQ_LOW;  
    HAL_GPIO_Init(GPIOB, &GPIO_InitStruct);  
  
}
```

```
/* USER CODE BEGIN 4 */

/* USER CODE END 4 */

/**
 * @brief This function is executed in case of error occurrence.
 * @retval None
 */
void Error_Handler(void)
{
    /* USER CODE BEGIN Error_Handler_Debug */
    /* User can add his own implementation to report the HAL error return
state */
    __disable_irq();
    while (1)
    {
    }
    /* USER CODE END Error_Handler_Debug */
}

#ifdef  USE_FULL_ASSERT
/**
 * @brief Reports the name of the source file and the source line number
 * where the assert_param error has occurred.
 * @param file: pointer to the source file name
 * @param line: assert_param error line source number
 * @retval None
 */
void assert_failed(uint8_t *file, uint32_t line)
{
    /* USER CODE BEGIN 6 */
    /* User can add his own implementation to report the file name and line
number,
ex: printf("Wrong parameters value: file %s on line %d\r\n", file,
line) */
    /* USER CODE END 6 */
}

#endif /* USE_FULL_ASSERT */
```

```

/***** (C) COPYRIGHT STMicroelectronics *****/
FILE****/

```

Lab Report Q2:

Link to Youtube: <https://youtu.be/TKC-NhlmbKM>

Code:

```
/* USER CODE BEGIN Header */  
/**  
  
*****  
****  
 * @file           : main.c  
 * @brief          : Main program body  
  
*****  
****  
 * @attention  
 *  
 * <h2><center>&copy; Copyright (c) 2021 STMicroelectronics.  
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 * License. You may obtain a copy of the License at:  
 *  
 *             opensource.org/licenses/BSD-3-Clause  
 *  
*****  
****  
 */  
/* USER CODE END Header */  
  
/* Includes  
-----*/  
#include "main.h"  
#include "stdio.h"  
/* Private includes  
-----*/  
  
/* USER CODE BEGIN Includes */
```

```
/* USER CODE END Includes */

/* Private typedef
-----*/
/* USER CODE BEGIN PTD */

/* USER CODE END PTD */

/* Private define
-----*/
/* USER CODE BEGIN PD */
/* USER CODE END PD */

/* Private macro
-----*/
/* USER CODE BEGIN PM */

/* USER CODE END PM */

/* Private variables
-----*/
I2C_HandleTypeDef hi2c1;

UART_HandleTypeDef huart1;
float temp;
/* USER CODE BEGIN PV */

/* USER CODE END PV */

/* Private function prototypes
-----*/
void SystemClock_Config(void);
static void MX_GPIO_Init(void);
static void MX_I2C1_Init(void);
static void MX_USART1_UART_Init(void);
/* USER CODE BEGIN PFP */

/* USER CODE END PFP */
```

```
/* Private user code
-----*/
/* USER CODE BEGIN 0 */

/* USER CODE END 0 */

/**
 * @brief The application entry point.
 * @retval int
 */
int main(void)
{
    /* USER CODE BEGIN 1 */

    /* USER CODE END 1 */

    /* MCU
Configuration-----*/

    /* Reset of all peripherals, Initializes the Flash interface and the
Systick. */
    HAL_Init();

    /* USER CODE BEGIN Init */

    /* USER CODE END Init */

    /* Configure the system clock */
    SystemClock_Config();

    /* USER CODE BEGIN SysInit */

    /* USER CODE END SysInit */

    /* Initialize all configured peripherals */
    MX_GPIO_Init();
    MX_I2C1_Init();
    MX_USART1_UART_Init();
    /* USER CODE BEGIN 2 */
```

```
/* USER CODE END 2 */

/* Infinite loop */
/* USER CODE BEGIN WHILE */
while (1)
{
    /* USER CODE END WHILE */
    uint8_t tempLSB[2], tempMSB[2];
    uint8_t eos[2];
    uint8_t osf[2];
    char out[50];

    tempMSB[0] = 0x11;
    tempLSB[0] = 0x12;
    __HAL_UART_ENABLE_IT(&huart1, UART_IT_RXNE);

    eos[0] = 0x0E;
    eos[1] = 0x3C;
    HAL_I2C_Master_Transmit(&hi2c1, 0xD0, eos, 2, 10);

    osf[0] = 0x0E;
    osf[1] = 0x3C;
    HAL_I2C_Master_Transmit(&hi2c1, 0xD0, osf, 2, 10);

    //send to register address 11h
    HAL_I2C_Master_Transmit(&hi2c1, 0xD0, tempMSB, 1, 10);
    //read data of register 11h to tempMSP[1]
    HAL_I2C_Master_Receive(&hi2c1, 0xD1, tempMSB+1, 1, 10);
    temp=tempMSB[1];

    //send to register address 11h
    HAL_I2C_Master_Transmit(&hi2c1, 0xD0, tempLSB, 1, 10);
    //read data of register 11h to tempMSP[1]
    HAL_I2C_Master_Receive(&hi2c1, 0xD1, tempLSB+1, 1, 10);
    //actual_temp_decimal=tempLSB[1];
    tempLSB[1] = tempLSB[1] >> 6;
```

```
temp = temp + tempLSB[1] *0.25;

sprintf(out, "Temperature: %f \n", temp);
HAL_UART_Transmit(&huart1, out, sizeof(out),HAL_MAX_DELAY);
/* USER CODE BEGIN 3 */
}
/* USER CODE END 3 */
}

/**
 * @brief System Clock Configuration
 * @retval None
 */
void SystemClock_Config(void)
{
    RCC_OscInitTypeDef RCC_OscInitStruct = {0};
    RCC_ClkInitTypeDef RCC_ClkInitStruct = {0};
    RCC_PeriphCLKInitTypeDef PeriphClkInit = {0};

    /** Configure LSE Drive Capability
    */
    HAL_PWR_EnableBkUpAccess();
    __HAL_RCC_LSEDRIVE_CONFIG(RCC_LSEDRIVE_LOW);
    /** Initializes the RCC Oscillators according to the specified
parameters
    * in the RCC_OscInitTypeDef structure.
    */
    RCC_OscInitStruct.OscillatorType =
RCC_OSCILLATORTYPE_LSE|RCC_OSCILLATORTYPE_MSI;
    RCC_OscInitStruct.LSEState = RCC_LSE_ON;
    RCC_OscInitStruct.MSIState = RCC_MSI_ON;
    RCC_OscInitStruct.MSICalibrationValue = 0;
    RCC_OscInitStruct.MSIClockRange = RCC_MSIRANGE_6;
    RCC_OscInitStruct.PLL.PLLState = RCC_PLL_NONE;
    if (HAL_RCC_OscConfig(&RCC_OscInitStruct) != HAL_OK)
    {
        Error_Handler();
    }

    /** Initializes the CPU, AHB and APB buses clocks
    */
}
```

```
RCC_ClkInitStruct.ClockType = RCC_CLOCKTYPE_HCLK|RCC_CLOCKTYPE_SYSCLK
                              |RCC_CLOCKTYPE_PCLK1|RCC_CLOCKTYPE_PCLK2;
RCC_ClkInitStruct.SYSCLKSource = RCC_SYSCLKSOURCE_MSI;
RCC_ClkInitStruct.AHBCLKDivider = RCC_SYSCLK_DIV1;
RCC_ClkInitStruct.APB1CLKDivider = RCC_HCLK_DIV1;
RCC_ClkInitStruct.APB2CLKDivider = RCC_HCLK_DIV1;

if (HAL_RCC_ClockConfig(&RCC_ClkInitStruct, FLASH_LATENCY_0) != HAL_OK)
{
    Error_Handler();
}
PeriphClkInit.PeriphClockSelection =
RCC_PERIPHCLK_USART1|RCC_PERIPHCLK_I2C1;
PeriphClkInit.Usart1ClockSelection = RCC_USART1CLKSOURCE_PCLK2;
PeriphClkInit.I2C1ClockSelection = RCC_I2C1CLKSOURCE_PCLK1;
if (HAL_RCCEx_PeriphCLKConfig(&PeriphClkInit) != HAL_OK)
{
    Error_Handler();
}
/** Configure the main internal regulator output voltage
 */
if (HAL_PWREx_ControlVoltageScaling(PWR_REGULATOR_VOLTAGE_SCALE1) !=
HAL_OK)
{
    Error_Handler();
}
/** Enable MSI Auto calibration
 */
HAL_RCCEx_EnableMSIPLLMode();
}

/**
 * @brief I2C1 Initialization Function
 * @param None
 * @retval None
 */
static void MX_I2C1_Init(void)
{
    /* USER CODE BEGIN I2C1_Init 0 */
```



```
/* USER CODE END I2C1_Init 0 */

/* USER CODE BEGIN I2C1_Init 1 */

/* USER CODE END I2C1_Init 1 */
hi2c1.Instance = I2C1;
hi2c1.Init.Timing = 0x00000E14;
hi2c1.Init.OwnAddress1 = 0;
hi2c1.Init.AddressingMode = I2C_ADDRESSINGMODE_7BIT;
hi2c1.Init.DualAddressMode = I2C_DUALADDRESS_DISABLE;
hi2c1.Init.OwnAddress2 = 0;
hi2c1.Init.OwnAddress2Masks = I2C_OA2_NOMASK;
hi2c1.Init.GeneralCallMode = I2C_GENERALCALL_DISABLE;
hi2c1.Init.NoStretchMode = I2C_NOSTRETCH_DISABLE;
if (HAL_I2C_Init(&hi2c1) != HAL_OK)
{
    Error_Handler();
}
/** Configure Analogue filter
 */
if (HAL_I2CEx_ConfigAnalogFilter(&hi2c1, I2C_ANALOGFILTER_ENABLE) !=
HAL_OK)
{
    Error_Handler();
}
/** Configure Digital filter
 */
if (HAL_I2CEx_ConfigDigitalFilter(&hi2c1, 0) != HAL_OK)
{
    Error_Handler();
}
/* USER CODE BEGIN I2C1_Init 2 */

/* USER CODE END I2C1_Init 2 */

}

/**
 * @brief USART1 Initialization Function

```

```
    * @param None
    * @retval None
    */
static void MX_USART1_UART_Init(void)
{

    /* USER CODE BEGIN USART1_Init 0 */

    /* USER CODE END USART1_Init 0 */

    /* USER CODE BEGIN USART1_Init 1 */

    /* USER CODE END USART1_Init 1 */
    huart1.Instance = USART1;
    huart1.Init.BaudRate = 115200;
    huart1.Init.WordLength = UART_WORDLENGTH_8B;
    huart1.Init.StopBits = UART_STOPBITS_1;
    huart1.Init.Parity = UART_PARITY_NONE;
    huart1.Init.Mode = UART_MODE_TX_RX;
    huart1.Init.HwFlowCtl = UART_HWCONTROL_NONE;
    huart1.Init.OverSampling = UART_OVERSAMPLING_16;
    huart1.Init.OneBitSampling = UART_ONE_BIT_SAMPLE_DISABLE;
    huart1.AdvancedInit.AdvFeatureInit = UART_ADVFEATURE_NO_INIT;
    if (HAL_UART_Init(&huart1) != HAL_OK)
    {
        Error_Handler();
    }
    /* USER CODE BEGIN USART1_Init 2 */

    /* USER CODE END USART1_Init 2 */

}

/**
 * @brief GPIO Initialization Function
 * @param None
 * @retval None
 */
static void MX_GPIO_Init(void)
{
```

```
/* GPIO Ports Clock Enable */
__HAL_RCC_GPIOC_CLK_ENABLE();
__HAL_RCC_GPIOA_CLK_ENABLE();
__HAL_RCC_GPIOB_CLK_ENABLE();

}

/* USER CODE BEGIN 4 */

/* USER CODE END 4 */

/**
 * @brief This function is executed in case of error occurrence.
 * @retval None
 */
void Error_Handler(void)
{
    /* USER CODE BEGIN Error_Handler_Debug */
    /* User can add his own implementation to report the HAL error return
state */
    __disable_irq();
    while (1)
    {
    }
    /* USER CODE END Error_Handler_Debug */
}

#ifdef  USE_FULL_ASSERT
/**
 * @brief Reports the name of the source file and the source line number
 *        where the assert_param error has occurred.
 * @param file: pointer to the source file name
 * @param line: assert_param error line source number
 * @retval None
 */
void assert_failed(uint8_t *file, uint32_t line)
{
    /* USER CODE BEGIN 6 */

```

```

    /* User can add his own implementation to report the file name and line
number,
        ex: printf("Wrong parameters value: file %s on line %d\r\n", file,
line) */
    /* USER CODE END 6 */
}
#endif /* USE_FULL_ASSERT */

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FILE*****/

```

Experiment 4:

Link to Youtube: <https://youtu.be/fAAjeTCI3O8>

Code:

```

/* USER CODE BEGIN Header */
/**
 *
 * *****
 *
 * @file      : main.c
 * @brief     : Main program body
 *
 * *****
 *
 * @attention
 *
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 * the "License"; You may not use this file except in compliance with the
 * License. You may obtain a copy of the License at:
 *
 *             opensource.org/licenses/BSD-3-Clause
 *
 * *****
 */

```

```
/* USER CODE END Header */
/* Includes
-----*/
#include "main.h"

/* Private includes
-----*/
/* USER CODE BEGIN Includes */

/* USER CODE END Includes */

/* Private typedef
-----*/
/* USER CODE BEGIN PTD */

/* USER CODE END PTD */

/* Private define
-----*/
/* USER CODE BEGIN PD */
/* USER CODE END PD */

/* Private macro
-----*/
/* USER CODE BEGIN PM */

/* USER CODE END PM */

/* Private variables
-----*/
SPI_HandleTypeDef hspi1;

UART_HandleTypeDef huart1;

/* USER CODE BEGIN PV */

/* USER CODE END PV */

/* Private function prototypes
-----*/
```

```
void SystemClock_Config(void);
static void MX_GPIO_Init(void);
static void MX_SPI1_Init(void);
static void MX_USART1_UART_Init(void);
/* USER CODE BEGIN PFP */

/* USER CODE END PFP */

/* Private user code
-----*/
/* USER CODE BEGIN 0 */
uint8_t hexToAscii(uint8_t n)//4-bit hex value converted to an ascii
character
{
    if (n>=0 && n<=9) n = n + '0';
    else n = n - 10 + 'A';
    return n;
}

/* USER CODE END 0 */

/**
 * @brief The application entry point.
 * @retval int
 */
int main(void)
{
    /* USER CODE BEGIN 1 */
    volatile int hex_1, hex_2, hex_3;
    uint8_t hex_c_1, hex_c_2, hex_c_3;
    uint8_t newline= 10;
    uint8_t c_r= 13;

    /* USER CODE END 1 */

    /* MCU
Configuration-----*/

    /* Reset of all peripherals, Initializes the Flash interface and the
Systick. */
```

```
HAL_Init();

/* USER CODE BEGIN Init */

/* USER CODE END Init */

/* Configure the system clock */
SystemClock_Config();

/* USER CODE BEGIN SysInit */

/* USER CODE END SysInit */

/* Initialize all configured peripherals */
MX_GPIO_Init();
MX_SPI1_Init();
MX_USART1_UART_Init();
/* USER CODE BEGIN 2 */

/* USER CODE END 2 */

/* Infinite loop */
/* USER CODE BEGIN WHILE */
uint8_t txdata = 0, rxdata;

    HAL_GPIO_WritePin(GPIOB, GPIO_PIN_5, 1);
    HAL_Delay(10);
    HAL_GPIO_WritePin(GPIOB, GPIO_PIN_5, 0);
    HAL_Delay(10);
    HAL_GPIO_WritePin(GPIOB, GPIO_PIN_5, 1);
    HAL_Delay(10);
while (1)
{
    HAL_GPIO_WritePin(GPIOB, GPIO_PIN_5, 0);

    txdata = 1;
    HAL_SPI_TransmitReceive(&hspi1, &txdata, &rxdata, 1, 100);

    txdata = 160;
```

```
    HAL_SPI_TransmitReceive(&hspi1,&txdata,&rxdata,1,100);
    hex_1 = rxdata %16;

    txdata = 0;
    HAL_SPI_TransmitReceive(&hspi1,&txdata,&rxdata,1,100);
    hex_3 = rxdata %16;
    hex_2 = rxdata>>4;

    hex_1 = hex_1%16;
    hex_2 = hex_2%16;
    hex_3 = hex_3%16;
    hex_c_1 = hexToAscii(hex_1 );
    hex_c_2 = hexToAscii(hex_2);
    hex_c_3 = hexToAscii(hex_3);

    HAL_UART_Transmit(&huart1,&hex_c_1, 1, 10);
    HAL_UART_Transmit(&huart1,&hex_c_2, 1, 10);
    HAL_UART_Transmit(&huart1,&hex_c_3, 1, 10);
    HAL_UART_Transmit(&huart1,&newline, 1, 10);
    HAL_UART_Transmit(&huart1,&c_r, 1, 10);

    HAL_GPIO_WritePin(GPIOB, GPIO_PIN_5, 1);
    HAL_Delay(10);

    /* USER CODE END WHILE */

    /* USER CODE BEGIN 3 */
}
/* USER CODE END 3 */
}

/**
 * @brief System Clock Configuration
 * @retval None
 */
void SystemClock_Config(void)
{

```



```
RCC_OscInitTypeDef RCC_OscInitStruct = {0};
RCC_ClkInitTypeDef RCC_ClkInitStruct = {0};
RCC_PeriphCLKInitTypeDef PeriphClkInit = {0};

/** Configure LSE Drive Capability
 */
HAL_PWR_EnableBkUpAccess();
__HAL_RCC_LSEDRIVE_CONFIG(RCC_LSEDRIVE_LOW);
/** Initializes the RCC Oscillators according to the specified
parameters
 * in the RCC_OscInitTypeDef structure.
 */
RCC_OscInitStruct.OscillatorType =
RCC_OSCILLATORTYPE_LSE|RCC_OSCILLATORTYPE_MSI;
RCC_OscInitStruct.LSEState = RCC_LSE_ON;
RCC_OscInitStruct.MSIState = RCC_MSI_ON;
RCC_OscInitStruct.MSICalibrationValue = 0;
RCC_OscInitStruct.MSIClockRange = RCC_MSIRANGE_6;
RCC_OscInitStruct.PLL.PLLState = RCC_PLL_NONE;
if (HAL_RCC_OscConfig(&RCC_OscInitStruct) != HAL_OK)
{
    Error_Handler();
}
/** Initializes the CPU, AHB and APB buses clocks
 */
RCC_ClkInitStruct.ClockType = RCC_CLOCKTYPE_HCLK|RCC_CLOCKTYPE_SYSCLK
                               |RCC_CLOCKTYPE_PCLK1|RCC_CLOCKTYPE_PCLK2;
RCC_ClkInitStruct.SYSCLKSource = RCC_SYSCLKSOURCE_MSI;
RCC_ClkInitStruct.AHBCLKDivider = RCC_SYSCLK_DIV1;
RCC_ClkInitStruct.APB1CLKDivider = RCC_HCLK_DIV1;
RCC_ClkInitStruct.APB2CLKDivider = RCC_HCLK_DIV1;

if (HAL_RCC_ClockConfig(&RCC_ClkInitStruct, FLASH_LATENCY_0) != HAL_OK)
{
    Error_Handler();
}
PeriphClkInit.PeriphClockSelection = RCC_PERIPHCLK_USART1;
PeriphClkInit.Usart1ClockSelection = RCC_USART1CLKSOURCE_PCLK2;
if (HAL_RCCEx_PeriphCLKConfig(&PeriphClkInit) != HAL_OK)
{
```

```
    Error_Handler();
}

/** Configure the main internal regulator output voltage
 */
if (HAL_PWREx_ControlVoltageScaling(PWR_REGULATOR_VOLTAGE_SCALE1) !=
HAL_OK)
{
    Error_Handler();
}

/** Enable MSI Auto calibration
 */
HAL_RCCEx_EnableMSIPLLMode();
}

/**
 * @brief SPI1 Initialization Function
 * @param None
 * @retval None
 */
static void MX_SPI1_Init(void)
{
    /* USER CODE BEGIN SPI1_Init 0 */

    /* USER CODE END SPI1_Init 0 */

    /* USER CODE BEGIN SPI1_Init 1 */

    /* USER CODE END SPI1_Init 1 */
    /* SPI1 parameter configuration*/
    hspi1.Instance = SPI1;
    hspi1.Init.Mode = SPI_MODE_MASTER;
    hspi1.Init.Direction = SPI_DIRECTION_2LINES;
    hspi1.Init.DataSize = SPI_DATASIZE_8BIT;
    hspi1.Init.CLKPolarity = SPI_POLARITY_LOW;
    hspi1.Init.CLKPhase = SPI_PHASE_1EDGE;
    hspi1.Init.NSS = SPI_NSS_SOFT;
    hspi1.Init.BaudRatePrescaler = SPI_BAUDRATEPRESCALER_2;
    hspi1.Init.FirstBit = SPI_FIRSTBIT_MSB;
    hspi1.Init.TIMode = SPI_TIMODE_DISABLE;
```

```
hspi1.Init.CRCCalculation = SPI_CRCCALCULATION_DISABLE;
hspi1.Init.CRCPolynomial = 7;
hspi1.Init.CRCLength = SPI_CRC_LENGTH_DATASIZE;
hspi1.Init.NSSPMode = SPI_NSS_PULSE_ENABLE;
if (HAL_SPI_Init(&hspi1) != HAL_OK)
{
    Error_Handler();
}
/* USER CODE BEGIN SPI1_Init 2 */

/* USER CODE END SPI1_Init 2 */

}

/**
 * @brief USART1 Initialization Function
 * @param None
 * @retval None
 */
static void MX_USART1_UART_Init(void)
{
    /* USER CODE BEGIN USART1_Init 0 */

    /* USER CODE END USART1_Init 0 */

    /* USER CODE BEGIN USART1_Init 1 */

    /* USER CODE END USART1_Init 1 */
    huart1.Instance = USART1;
    huart1.Init.BaudRate = 115200;
    huart1.Init.WordLength = UART_WORDLENGTH_8B;
    huart1.Init.StopBits = UART_STOPBITS_1;
    huart1.Init.Parity = UART_PARITY_NONE;
    huart1.Init.Mode = UART_MODE_TX_RX;
    huart1.Init.HwFlowCtl = UART_HWCONTROL_NONE;
    huart1.Init.OverSampling = UART_OVERSAMPLING_16;
    huart1.Init.OneBitSampling = UART_ONE_BIT_SAMPLE_DISABLE;
    huart1.AdvancedInit.AdvFeatureInit = UART_ADVFEATURE_NO_INIT;
    if (HAL_UART_Init(&huart1) != HAL_OK)
```

```
{  
    Error_Handler();  
}  
/* USER CODE BEGIN USART1_Init 2 */  
  
/* USER CODE END USART1_Init 2 */  
}  
  
/**  
 * @brief GPIO Initialization Function  
 * @param None  
 * @retval None  
 */  
static void MX_GPIO_Init(void)  
{  
    GPIO_InitTypeDef GPIO_InitStruct = {0};  
  
    /* GPIO Ports Clock Enable */  
    __HAL_RCC_GPIOC_CLK_ENABLE();  
    __HAL_RCC_GPIOA_CLK_ENABLE();  
    __HAL_RCC_GPIOB_CLK_ENABLE();  
  
    /*Configure GPIO pin Output Level */  
    HAL_GPIO_WritePin(GPIOB, GPIO_PIN_5, GPIO_PIN_RESET);  
  
    /*Configure GPIO pin : PB5 */  
    GPIO_InitStruct.Pin = GPIO_PIN_5;  
    GPIO_InitStruct.Mode = GPIO_MODE_OUTPUT_PP;  
    GPIO_InitStruct.Pull = GPIO_NOPULL;  
    GPIO_InitStruct.Speed = GPIO_SPEED_FREQ_LOW;  
    HAL_GPIO_Init(GPIOB, &GPIO_InitStruct);  
}  
  
/* USER CODE BEGIN 4 */  
  
/* USER CODE END 4 */  
  
/**
```

```
    * @brief This function is executed in case of error occurrence.
    * @retval None
    */
void Error_Handler(void)
{
    /* USER CODE BEGIN Error_Handler_Debug */
    /* User can add his own implementation to report the HAL error return
state */
    __disable_irq();
    while (1)
    {
    }
    /* USER CODE END Error_Handler_Debug */
}

#ifdef USE_FULL_ASSERT
/**
 * @brief Reports the name of the source file and the source line number
 *         where the assert_param error has occurred.
 * @param file: pointer to the source file name
 * @param line: assert_param error line source number
 * @retval None
 */
void assert_failed(uint8_t *file, uint32_t line)
{
    /* USER CODE BEGIN 6 */
    /* User can add his own implementation to report the file name and line
number,
ex: printf("Wrong parameters value: file %s on line %d\r\n", file,
line) */
    /* USER CODE END 6 */
}
#endif /* USE_FULL_ASSERT */

/***** (C) COPYRIGHT STMicroelectronics *****END OF
FILE*****/
```