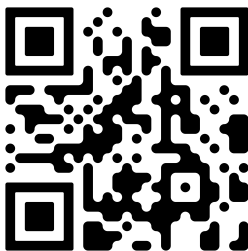


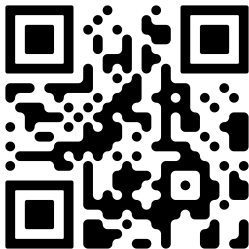
FINITE-VALUED STREAMING STRING TRANSDUCERS

Emmanuel Filiot, **Ismaël Jecker**, Christof Löding,
Anca Muscholl, Gabriele Puppis, Sarah Winter



ismaeljecker.github.io

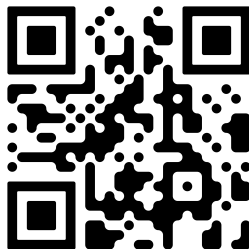
Finite-valued regular relations form a class of binary relations that enjoys good algorithmic properties



ismaeljecker.github.io

Theorem: We can decide in polynomial space whether a given **SST** defines a **finite-valued regular relation**

Theorem: Every **finite-valued regular relation** can be decomposed into a finite union of **regular functions**



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TRANSDUCERS

Transducers are abstract machines that recognise **relations**

$$R \subseteq \Sigma^* \times \Gamma^*$$

Rational relations

recognised by finite state transducers

Regular relations

recognised by streaming string transducers

TRANSDUCERS

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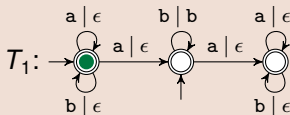
Finite state transducer (FST)

Input:

a	b	b	a	a	a	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---

Output:

T_1 produces one of the b-blocks of its input



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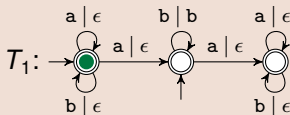
Finite state transducer (FST)

Input:

a	b	b	a	a	a	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---

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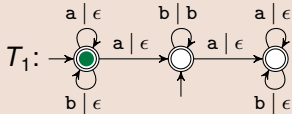
Finite state transducer (FST)

Input:

a	b	b	a	a	a	b	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---	---

Output:

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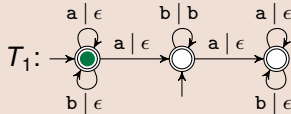
Finite state transducer (FST)

Input:

a	b	b	a	a	a	b	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---	---

Output:

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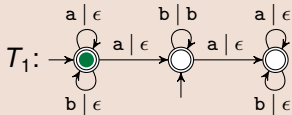
Finite state transducer (FST)

Input:

a	b	b	a	a	b	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---

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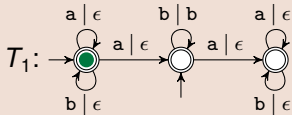
Finite state transducer (FST)

Input:

a	b	b	a	a	a	b	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---	---

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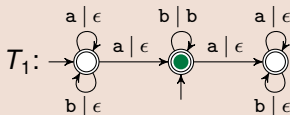
Finite state transducer (FST)

Input:

a	b	b	a	a	a	b	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---	---

Output:

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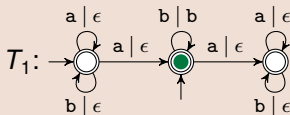
Input:

a	b	b	a	a	a	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---

Output:

b

T_1 produces one of the b-blocks of its input



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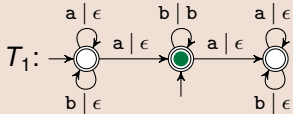
Input:

a	b	b	a	a	a	b	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---	---

Output:

b	b
---	---

T_1 produces one of the b-blocks of its input



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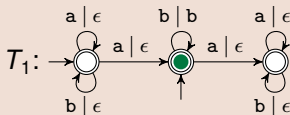
Input:

a	b	b	a	a	a	b	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---	---

Output:

b	b	b
---	---	---

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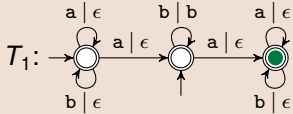
Input:

a	b	b	a	a	a	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---

Output:

b	b	b
---	---	---

T_1 produces one of the b-blocks of its input



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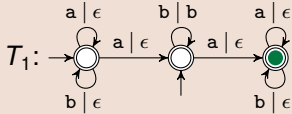
Input:

a	b	b	a	a	a	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---

Output:

b	b	b
---	---	---

T_1 produces one of the b-blocks of its input



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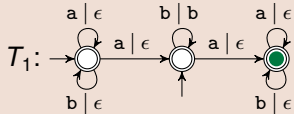
Input:

a	b	b	a	a	a	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---

Output:

b	b	b
---	---	---

T_1 produces one of the b-blocks of its input



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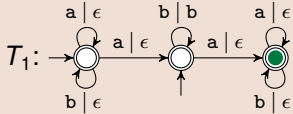
Input:

a	b	b	a	a	a	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---

Output:

b	b	b
---	---	---

T_1 produces one of the b-blocks of its input



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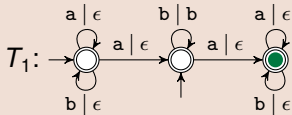
Input:

a	b	b	a	a	a	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---

Output:

b	b	b
---	---	---

T_1 produces one of the b-blocks of its input



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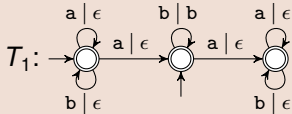
Input:

a	b	b	a	a	a	b	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---	---

Output:

b	b	b
---	---	---

T_1 produces one of the b-blocks of its input



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Finite state transducer (FST)

Input:

a	b	b	a	a	a	b	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---	---

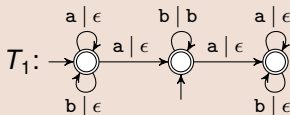
Output:

b	b	b
---	---	---

b

b	b
---	---

T_1 produces one of the b-blocks of its input



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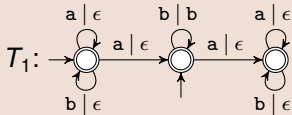
Input:

a	b	b	a	a	a	b	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---	---

Output:

b	b	b
b		
b	b	

T_1 produces one of the b-blocks of its input



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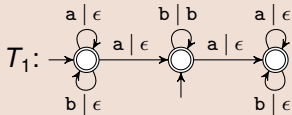
Input:

a	b	b	a	a	a	b	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---	---

Output:

b	b	b
b		
b	b	

T_1 produces one of the b-blocks of its input



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Streaming string transducer (SST)

Input:

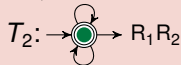
a	b	b	a	a	a	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---

R_1 :

R_2 :

T_2 sorts its input

$$a \mid R_1 := R_1 a$$



$$b \mid R_2 := R_2 b$$

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Streaming string transducer (SST)

Input:

a	b	b	a	a	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---

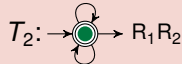
R_1 :

a

R_2 :

T_2 sorts its input

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Input:

a	b	b	a	a	a	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---

R_1 :

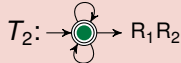
a

R_2 :

b

T_2 sorts its input

$a \mid R_1 := R_1 a$



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Input:

a	b	b	a	a	a	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---

R_1 :

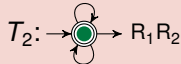
a

R_2 :

b	b
---	---

T_2 sorts its input

$a \mid R_1 := R_1 a$



$b \mid R_2 := R_2 b$

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Streaming string transducer (SST)

Input:

a	b	b	a	a	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---

R_1 :

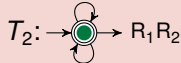
a	a
---	---

R_2 :

b	b
---	---

T_2 sorts its input

$a \mid R_1 := R_1 a$



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Input:

a	b	b	a	a	a	b	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---	---

R_1 :

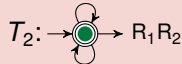
a	a	a
---	---	---

R_2 :

b	b
---	---

T_2 sorts its input

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Input:

a	b	b	a	a	a	b	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---	---

R_1 :

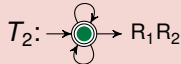
a	a	a	a
---	---	---	---

R_2 :

b	b
---	---

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Input:

a	b	b	a	a	a	b	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---	---

R_1 :

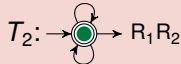
a	a	a	a
---	---	---	---

R_2 :

b	b	b
---	---	---

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Input:

a	b	b	a	a	a	b	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---	---

R_1 :

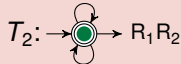
a	a	a	a
---	---	---	---

R_2 :

b	b	b	b
---	---	---	---

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Input:

a	b	b	a	a	a	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---

R_1 :

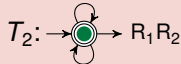
a	a	a	a
---	---	---	---

R_2 :

b	b	b	b	b
---	---	---	---	---

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Input:

a	b	b	a	a	a	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---

R_1 :

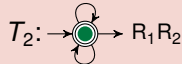
a	a	a	a	a
---	---	---	---	---

R_2 :

b	b	b	b	b
---	---	---	---	---

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Input:

a	b	b	a	a	a	b	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---	---

R_1 :

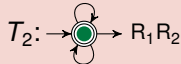
a	a	a	a	a
---	---	---	---	---

R_2 :

b	b	b	b	b	b
---	---	---	---	---	---

T_2 sorts its input

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Input:

a	b	b	a	a	a	b	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---	---

R_1 :

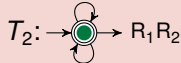
a	a	a	a	a	a
---	---	---	---	---	---

R_2 :

b	b	b	b	b	b
---	---	---	---	---	---

T_2 sorts its input

$$a \mid R_1 := R_1 a$$



$$b \mid R_2 := R_2 b$$

Rational relations

recognised by finite state transducers

Regular relations

recognised by streaming string transducers

TRANSDUCERS

Transducers are abstract machines that recognise **relations**

Streaming string transducer (SST)

Input:

a	b	b	a	a	a	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---

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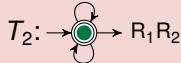
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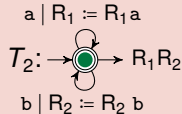
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a	a	a	a	a	a	a	a
---	---	---	---	---	---	---	---

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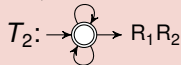
a	b	b	a	a	a	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---

Output:

a	a	a	a	a	a	a	a	b	b	b	b	b
---	---	---	---	---	---	---	---	---	---	---	---	---

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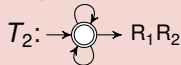
a	b	b	a	a	a	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---

Output:

a	a	a	a	a	a	a	a	b	b	b	b	b
---	---	---	---	---	---	---	---	---	---	---	---	---

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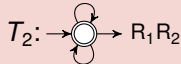
a	b	b	a	a	a	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---

Output:

a	a	a	a	a	a	a	a	b	b	b	b	b
---	---	---	---	---	---	---	---	---	---	---	---	---

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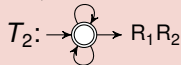
a	b	b	a	a	a	b	b	a	b	a	a	a
---	---	---	---	---	---	---	---	---	---	---	---	---

Output:

a	a	a	a	a	a	a	a	b	b	b	b	b
---	---	---	---	---	---	---	---	---	---	---	---	---

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TRANSDUCERS

Equivalence of **rational relations** is **undecidable**

Rational relations

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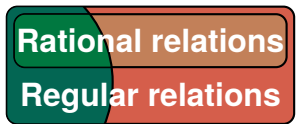
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TRANSDUCERS

Equivalence of **rational relations** is **undecidable**

Equivalence of **regular functions** is **decidable**

↳ each input is mapped to at most 1 output



functions

recognised by finite state transducers

recognised by streaming string transducers

TRANSDUCERS

Equivalence of **rational relations** is **undecidable**

Equivalence of **regular functions** is **decidable**

↳ each input is mapped to at most 1 output

Equivalence of **finite-valued regular relations** is **decidable**

↳ $\exists k \in \mathbb{N}$ s.t. each input is mapped to at most k outputs



recognised by finite state transducers

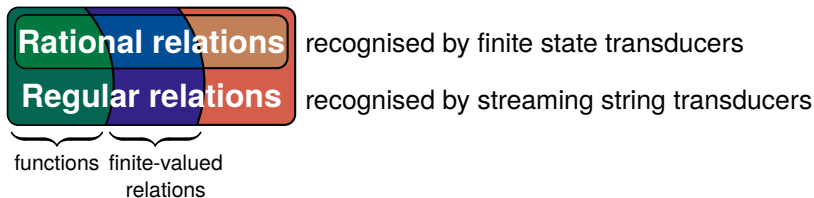
recognised by streaming string transducers

functions finite-valued
relations

TRANSDUCERS

Theorem: We can decide in polynomial space whether a given **SST** defines a **finite-valued regular relation**

Theorem: Every **finite-valued regular relation** can be decomposed into a finite union of **regular functions**



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⇒ The equivalence problem for finite-valued SST is in ELEMENTARY

⇒ Finite-valued **2-way FST** are as expressive as finite-valued SST



recognised by finite state transducers

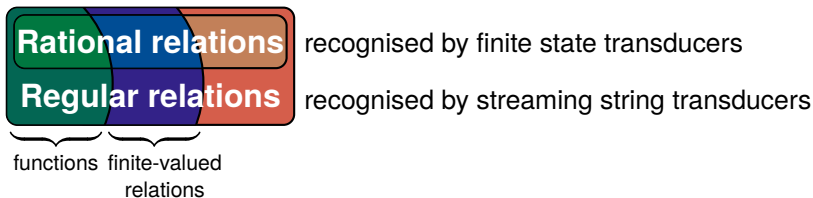
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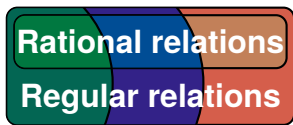
Theorem: We can decide in polynomial space whether a given **SST** defines a **finite-valued regular relation**

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Raised as open problems by [2011. Alur, Deshmukh]

Known to hold for FST [1989. Weber], [1993. Weber];

...and for SST with a single register [2017. Gallot et al.]



recognised by finite state transducers

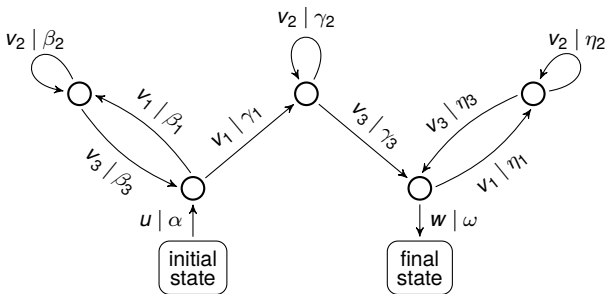
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DECIDING FINITE VALUEDNESS

Theorem: We can decide in polynomial space whether a given SST defines a **finite-valued regular relation**

Forbidden pattern: (inspired by [2008. De Souza])

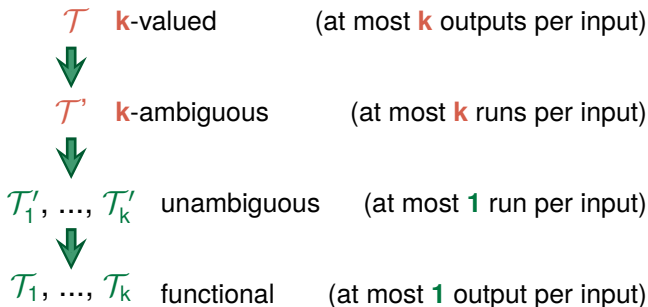


- The relation recognised by the pattern is not 1-valued
- The substitutions produced on the loops have idempotent structure

THE DECOMPOSITION THEOREM

Theorem: Every **finite-valued regular relation** can be decomposed into a finite union of **regular functions**

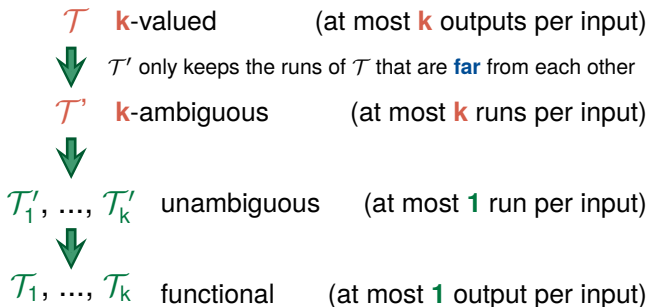
Proof: (inspired by [2008. Sakarovitch, de Souza] and relying on [2023. FJLW])



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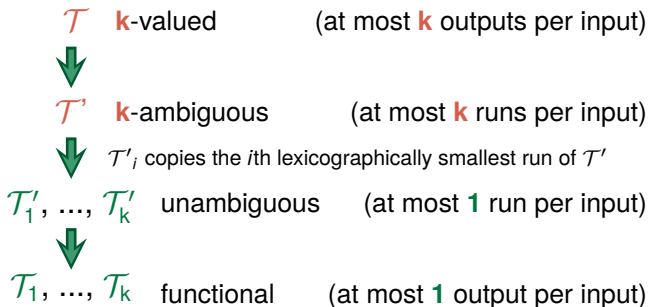
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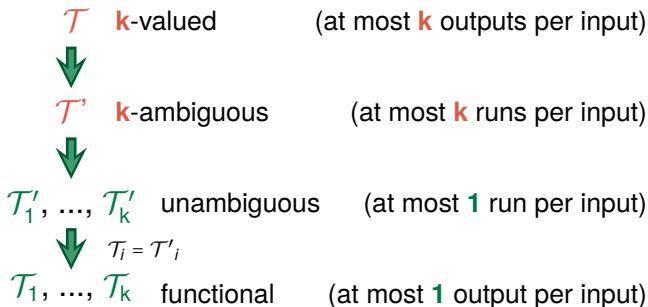
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3 Maître de conférences positions

set to begin in the academic year 2025-2026

Contact me for more information:

ismael.jecker@gmail.com